

# Ji-Hwan Seol

## List of Publications by Year in descending order

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Version: 2024-02-01

4  
papers

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citations

2682572

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2550090

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#	ARTICLE	IF	CITATIONS
1	A 43 nW, 32 kHz, $\hat{\Delta}\pm 4.2$ ppm Piecewise Linear Temperature-Compensated Crystal Oscillator With $\hat{\sigma}^2_{\text{mod}}$ -Modulated Load Capacitance. IEEE Journal of Solid-State Circuits, 2022, 57, 1175-1186.	5.4	1
2	Reference Oversampling PLL Achieving $\hat{\sigma}^2_{\text{mod}} \sim 256$ -dB FoM and $\hat{\sigma}^2_{\text{mod}} \sim 78$ -dBc Reference Spur. IEEE Journal of Solid-State Circuits, 2021, 56, 2993-3007.	5.4	12
3	A 67-fs <sub>rms</sub> Jitter, $\hat{\sigma}^2_{\text{mod}} \sim 130$ dBc/Hz In-Band Phase Noise, $\hat{\sigma}^2_{\text{mod}} \sim 256$ -dB FoM Reference Oversampling Digital PLL With Proportional Path Timing Control. IEEE Solid-State Circuits Letters, 2020, 3, 430-433.	2.0	7
4	A Reference Oversampling Digital Phase-Locked Loop with -240 dB FOM and -80 dBc Reference Spur. , 2019, , .		6