

Ji-Hwan Seol

List of Publications by Year in descending order

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#	ARTICLE	IF	CITATIONS
1	Reference Oversampling PLL Achieving $\hat{\sim}256$ -dB FoM and $\hat{\sim}78$ -dBc Reference Spur. IEEE Journal of Solid-State Circuits, 2021, 56, 2993-3007.	5.4	12
2	A $67\text{-fs}_{\text{rms}}$ Jitter, $\hat{\sim}130$ dBc/Hz In-Band Phase Noise, $\hat{\sim}256$ -dB FoM Reference Oversampling Digital PLL With Proportional Path Timing Control. IEEE Solid-State Circuits Letters, 2020, 3, 430-433.	2.0	7
3	A Reference Oversampling Digital Phase-Locked Loop with -240 dB FOM and -80 dBc Reference Spur. , 2019, , .		6
4	A 43 nW, 32 kHz, $\hat{\pm}4.2$ ppm Piecewise Linear Temperature-Compensated Crystal Oscillator With $\hat{\sim}1\text{f}$ -Modulated Load Capacitance. IEEE Journal of Solid-State Circuits, 2022, 57, 1175-1186.	5.4	1