

Mikhail Asiatici

List of Publications by Year in descending order

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Version: 2024-02-01

7
papers

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citations

2258059

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1872680

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7
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7
times ranked

105
citing authors

#	ARTICLE	IF	CITATIONS
1	Virtualized Execution Runtime for FPGA Accelerators in the Cloud. IEEE Access, 2017, 5, 1900-1910.	4.2	56
2	Through-Glass Vias for Glass Interposers and MEMS Packaging Applications Fabricated Using Magnetic Assembly of Microscale Metal Wires. IEEE Access, 2018, 6, 44306-44317.	4.2	21
3	Through Silicon Vias With Invar Metal Conductor for High-Temperature Applications. Journal of Microelectromechanical Systems, 2017, 26, 158-168.	2.5	10
4	Capacitive inertial sensing at high temperatures of up to 400 Â°C. Sensors and Actuators A: Physical, 2016, 238, 361-368.	4.1	3
5	Snap-On User-Space Manager for Dynamically Reconfigurable System-on-Chips. IEEE Access, 2019, 7, 103938-103947.	4.2	2
6	How Many CPU Cores is an FPGA Worth? Lessons Learned from Accelerating String Sorting on a CPU-FPGA System. Journal of Signal Processing Systems, 0, , 1.	2.1	2
7	Request, Coalesce, Serve, and Forget: Miss-Optimized Memory Systems for Bandwidth-Bound Cache-Unfriendly Applications on FPGAs. ACM Transactions on Reconfigurable Technology and Systems, 2022, 15, 1-33.	2.5	1