

# Shuangming Yang

## List of Publications by Year in descending order

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29  
papers

1,117  
citations

687335

13  
h-index

888047

17  
g-index

30  
all docs

30  
docs citations

30  
times ranked

545  
citing authors

#	ARTICLE	IF	CITATIONS
1	Neuromorphic Context-Dependent Learning Framework With Fault-Tolerant Spike Routing. IEEE Transactions on Neural Networks and Learning Systems, 2022, 33, 7126-7140.	11.3	101
2	BiCoSS: Toward Large-Scale Cognition Brain With Multigranular Neuromorphic Architecture. IEEE Transactions on Neural Networks and Learning Systems, 2022, 33, 2801-2815.	11.3	96
3	CerebelluMorphic: Large-Scale Neuromorphic Model and Architecture for Supervised Motor Learning. IEEE Transactions on Neural Networks and Learning Systems, 2022, 33, 4398-4412.	11.3	81
4	Robust Spike-Based Continual Meta-Learning Improved by Restricted Minimum Error Entropy Criterion. Entropy, 2022, 24, 455.	2.2	108
5	SAM: A Unified Self-Adaptive Multicompartmental Spiking Neuron Model for Learning With Working Memory. Frontiers in Neuroscience, 2022, 16, 850945.	2.8	47
6	Heterogeneous Ensemble-Based Spike-Driven Few-Shot Online Learning. Frontiers in Neuroscience, 2022, 16, .	2.8	72
7	Efficient Spike-Driven Learning With Dendritic Event-Based Processing. Frontiers in Neuroscience, 2021, 15, 601109.	2.8	120
8	Reconstruction of a Fully Paralleled Auditory Spiking Neural Network and FPGA Implementation. IEEE Transactions on Biomedical Circuits and Systems, 2021, 15, 1320-1331.	4.0	4
9	Reconstruction of Brain-inspired Visual Spiking Neural Network on BiCoSS. , 2021, , .		1
10	Scalable Digital Neuromorphic Architecture for Large-Scale Biophysically Meaningful Neural Network With Multi-Compartment Neurons. IEEE Transactions on Neural Networks and Learning Systems, 2020, 31, 148-162.	11.3	229
11	Deep Convolutional Neural Network for Detection of Disorders of Consciousness. , 2020, , .		3
12	Scalable Implementation of Hippocampal Network on Digital Neuromorphic System towards Brain-Inspired Intelligence. Applied Sciences (Switzerland), 2020, 10, 2857.	2.5	9
13	Digital Implementation of the Retinal Spiking Neural Network under Light Stimulation. , 2019, , .		2
14	Behavior of a Hippocampal Spiking Network and FPGA Implementation. , 2019, , .		0
15	Efficient Implementation of Cerebellar Purkinje Cell With the CORDIC Algorithm on LaCSNN. Frontiers in Neuroscience, 2019, 13, 1078.	2.8	4
16	Digital Implementation of the Spiking Neural Network and Its Digit Recognition. , 2019, , .		3
17	A real-time virtual manipulator simulation platform based on FPGA. , 2019, , .		0
18	Real-time implementation of the cerebellum neural network. , 2019, , .		1

#	ARTICLE	IF	CITATIONS
19	Real-time implementation of the Purkinje network on digital neuromorphic system. , 2019, , .		1
20	Real-Time Neuromorphic System for Large-Scale Conductance-Based Spiking Neural Networks. IEEE Transactions on Cybernetics, 2019, 49, 2490-2503.	9.5	95
21	FPGA-based spiking neural network with hippocampal oscillation dynamics towards biologically meaningful prostheses. , 2018, , .		0
22	Hardware Implementation of the Cerebellar Neural Network with Conductance-based Models. , 2018, , .		0
23	Cost-efficient FPGA implementation of a biologically plausible dopamine neural network and its application. Neurocomputing, 2018, 314, 394-408.	5.9	27
24	Efficient implementation of a real-time estimation system for thalamocortical hidden Parkinsonian properties. Scientific Reports, 2017, 7, 40152.	3.3	24
25	FPGA-based hardware simulation of nonlinear autoregressive Volterra model to reconstruct the single neuron spike pattern. International Journal of Modern Physics B, 2017, 31, 1750238.	2.0	1
26	Efficient hardware implementation of the subthalamic nucleusâ€œexternal globus pallidus oscillation system and its dynamics investigation. Neural Networks, 2017, 94, 220-238.	5.9	25
27	A multi-FPGA embedded system for the emulation of modular small-world network with real time dynamics. , 2016, , .		2
28	Cost-efficient FPGA implementation of basal ganglia and their Parkinsonian analysis. Neural Networks, 2015, 71, 62-75.	5.9	59
29	Multi-FPGA implementation of feedforward network and its performance analysis. , 2015, , .		1