

Hsiang-Chi Cheng

List of Publications by Year in descending order

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citing authors

#	ARTICLE	IF	CITATIONS
1	A 13.56 MHz CMOS High-Efficiency Active Rectifier With Dynamically Controllable Comparator for Biomedical Wireless Power Transfer Systems. IEEE Access, 2018, 6, 49979-49989.	4.2	13
2	An all-digital DLL with duty-cycle correction using reusable TDC. International Journal of Circuit Theory and Applications, 2016, 44, 1055-1070.	2.0	7
3	A self-calibrated delay-locked loop with low static phase error. International Journal of Circuit Theory and Applications, 2016, 44, 929-944.	2.0	4
4	All-digital controlled boost DC-DC converter with all-digital DLL-based calibration. Microelectronics Journal, 2015, 46, 970-980.	2.0	4