

Zehra GÃ¼lru Ãam

List of Publications by Year in descending order

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21
times ranked

204
citing authors

#	ARTICLE	IF	CITATIONS
1	A New PID Controller Circuit Design Using CFOAs. Circuits, Systems, and Signal Processing, 2021, 40, 1166-1182.	2.0	4
2	Application and Modeling of a Novel 4D Memristive Chaotic System for Communication Systems. Circuits, Systems, and Signal Processing, 2020, 39, 3320-3349.	2.0	23
3	A new universal mutator circuit for memcapacitor and meminductor elements. AEU - International Journal of Electronics and Communications, 2020, 119, 153180.	2.9	38
4	Second Generation Current Conveyor Based Floating Fractional Order Memristance Simulator and a New Dynamical System. Cybernetics and Information Technologies, 2020, 20, 68-80.	1.1	1
5	Dual-Output Operational Transconductance Amplifier-Based Electronically Controllable Memristance Simulator Circuit. Circuits, Systems, and Signal Processing, 2019, 38, 26-40.	2.0	11
6	A New Fractional Order Memristance Simulator Circuit Design. , 2019, , .		1
7	Simulation and implementation of memristive chaotic system and its application for communication systems. Sensors and Actuators A: Physical, 2019, 290, 107-118.	4.1	38
8	A novel memristive true random number generator design. COMPEL - the International Journal for Computation and Mathematics in Electrical and Electronic Engineering, 2019, 38, 1931-1947.	0.9	4
9	Voltage Differencing Gain Amplifier-Based nth-Order Low-Pass Voltage-Mode Filter. Journal of Circuits, Systems and Computers, 2018, 27, 1850089.	1.5	6
10	Chaotic Lorenz Synchronization Circuit Design for Secure Communication. , 2018, , .		4
11	An Analog Chaotic Lorenz Circuit Based on CCII+ and Multiplier. , 2018, , .		5
12	Novel Feature Extraction Methodology with Evaluation in Artificial Neural Networks Based Fingerprint Recognition System. Tehnicki Vjesnik, 2018, 25, .	0.2	1
13	Offline signature identification via HOG features and artificial neural networks. , 2017, , .		14
14	A New Floating Memristance Simulator Circuit Based on Second Generation Current Conveyor. Journal of Circuits, Systems and Computers, 2017, 26, 1750029.	1.5	51
15	Generalized regression neural network based efficient memristor modeling. , 2016, , .		1
16	Learning parameter optimization of Multi-Layer Perceptron using Artificial Bee Colony, Genetic Algorithm and Particle Swarm Optimization. , 2015, , .		9
17	High gain, linear, low cost digitally controlled VHF-UHF analog transponder for nano satellites. , 2013, , .		0
18	Simultaneously matched SiGe transistor RF mixer with high dynamic range. , 2013, , .		0

#	ARTICLE	IF	CITATIONS
19	Input and output matched SiGe RF Mixer with High Dynamic Range. , 2013, , .		0
20	A New Dynamical Circuit Based on CCII+, Physical Implementation and Synchronization. Journal of Circuits, Systems and Computers, 0, , 2150289.	1.5	0
21	Memristör tabanlı kaotik çarpslar devresi geliştirilmesi. Journal of the Faculty of Engineering and Architecture of Gazi University, 0, , .	0.8	2