

Debaprasad Das

List of Publications by Year in descending order

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18
g-index

57
all docs

57
docs citations

57
times ranked

216
citing authors

#	ARTICLE	IF	CITATIONS
1	An IoT-based waste management system monitored by cloud. Journal of Material Cycles and Waste Management, 2018, 20, 1574-1582.	3.0	78
2	Analysis of Crosstalk in Single- and Multiwall Carbon Nanotube Interconnects and Its Impact on Gate Oxide Reliability. IEEE Nanotechnology Magazine, 2011, 10, 1362-1370.	2.0	67
3	Crosstalk and Gate Oxide Reliability Analysis in Graphene Nanoribbon Interconnects. , 2011, , .		28
4	Reduced thickness interconnect model using GNR to avoid crosstalk effects. Journal of Computational Electronics, 2016, 15, 367-380.	2.5	22
5	An IoT based building health monitoring system supported by cloud. Journal of Reliable Intelligent Environments, 2020, 6, 141-152.	5.2	22
6	Stability Analysis in Top-Contact and Side-Contact Graphene Nanoribbon Interconnects. IETE Journal of Research, 2017, 63, 588-596.	2.6	21
7	Design of ternary logic circuits using CNTFET. , 2018, , .		20
8	Electro-thermal RF modeling and performance analysis of graphene nanoribbon interconnects. Journal of Computational Electronics, 2018, 17, 1695-1708.	2.5	19
9	Modeling and Analysis of Electro-Thermal Impact of Crosstalk Induced Gate Oxide Reliability in Pristine and Intercalation Doped MLG NR Interconnects. IEEE Transactions on Device and Materials Reliability, 2019, 19, 543-550.	2.0	18
10	Analysis of a temperature-dependent delay optimization model for GNR interconnects using a wire sizing method. Journal of Computational Electronics, 2018, 17, 1536-1548.	2.5	16
11	Crosstalk analysis in Carbon Nanotube interconnects and its impact on gate oxide reliability. , 2010, , .		13
12	Modeling of IR-Drop induced delay fault in CNT and GNR power distribution networks. , 2012, , .		12
13	Crosstalk overshoot/undershoot analysis and its impact on gate oxide reliability in multi-wall carbon nanotube interconnects. Journal of Computational Electronics, 2011, 10, 360-372.	2.5	11
14	Modeling of single-wall carbon nanotube interconnects for different process, temperature, and voltage conditions and investigating timing delay. Journal of Computational Electronics, 2012, 11, 349-363.	2.5	9
15	Investigating the Applicability of Graphene Nanoribbon as Signal and Power Interconnects for Nanometer Designs. Journal of Circuits, Systems and Computers, 2016, 25, 1650001.	1.5	9
16	Design of ternary encoder and decoder using CNTFET. International Journal of Electronics, 2022, 109, 135-151.	1.4	9
17	Design of Content Addressable Memory Architecture Using Carbon Nanotube Field Effect Transistors. Lecture Notes in Computer Science, 2012, , 233-242.	1.3	9
18	Reference free speech quality estimation for diverse data condition. International Journal of Speech Technology, 2019, 22, 585-599.	2.2	7

#	ARTICLE	IF	CITATIONS
19	Simultaneous switching noise and IR drop in graphene nanoribbon power distribution networks. , 2012, , .		6
20	RF performance analysis of graphene nanoribbon interconnect. , 2014, , .		6
21	Unified model for analyzing timing delay and crosstalk effects in Carbon Nanotube interconnects. , 2012, , .		5
22	Design of content addressable memory cell using carbon nanotube field effect transistors. , 2016, , .		5
23	Analysis of delay fault in GNR power interconnects. International Journal of Numerical Modelling: Electronic Networks, Devices and Fields, 2018, 31, e2308.	1.9	5
24	An Efficient VLSI Test Data Compression Scheme for Circular Scan Architecture Based on Modified Ant Colony Meta-heuristic. Journal of Electronic Testing: Theory and Applications (JETTA), 2020, 36, 327-342.	1.2	5
25	Analysis of temperature dependent power supply voltage drop in graphene nanoribbon and Cu based power interconnects. AIMS Materials Science, 2016, 3, 1493-1506.	1.4	5
26	Analysis of Simultaneous Switching Noise and IR-Drop in Side-Contact Multilayer Graphene Nanoribbon Power Distribution Network. Journal of Circuits, Systems and Computers, 2018, 27, 1850001.	1.5	4
27	A Novel Signed Array Multiplier. , 2010, , .		3
28	Timing Analysis in Carbon Nanotube Interconnects with Process, Temperature, and Voltage Variations. , 2010, , .		3
29	IR drop analysis in single- and multi-wall carbon nanotube power interconnects in sub-nanometer designs. , 2011, , .		3
30	RF performance analysis of single- and multi-wall carbon nanotube interconnect. , 2011, , .		3
31	A Novel GNR Interconnect Model to Reduce Crosstalk Delay. , 2014, , .		3
32	Text detection from natural scene images for Manipuri Meetei Mayek script. , 2015, , .		3
33	Modeling and Performance Analysis of MLGNR Interconnects. Journal of Circuits, Systems and Computers, 2018, 27, 1850214.	1.5	3
34	Temperatureâ€dependent analysis of crosstalk and delay uncertainty in multilayer graphene nanoribbon interconnects. Engineering Reports, 2020, 2, e12185.	1.7	3
35	Performance analysis of temperature dependent GNR interconnect. , 2016, , .		2
36	Modeling and Performance Analysis of Graphene Nanoribbon Interconnects. The National Academy of Sciences, India, 2017, 40, 325-329.	1.3	2

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37	Performance modeling of intercalation doped graphene-nanoribbon interconnects. , 2018, , .		2
38	Thermal Stability Analysis of Graphene Nano-ribbon Interconnect and Applicability for Terahertz Frequency. The National Academy of Sciences, India, 2020, 43, 253-257.	1.3	2
39	Electrical transport in graphene nanoribbon interconnect. , 2014, , .		1
40	Design of 9-transistor content addressable memory cells using Schottky-barrier carbon nanotube field effect transistors. , 2016, , .		1
41	An IoT based Wireless Energy Harvesting using Efficient Voltage Doubler Stages in a RF to DC Converter. , 2018, , .		1
42	Analysis of Temperature-Dependent Crosstalk for Graphene Nanoribbon and Copper Interconnects. IETE Journal of Research, 2019, , 1-12.	2.6	1
43	Modeling and performance analysis of folded graphene nanoribbon interconnect. International Journal of Numerical Modelling: Electronic Networks, Devices and Fields, 2021, 34, e2834.	1.9	1
44	A survey on pristine and intercalation doped graphene nanoribbon interconnect for future VLSI circuits. AIMS Materials Science, 2021, 8, 247-260.	1.4	1
45	Delay optimization using repeater insertion in folded graphene nanoribbon interconnect systems. International Journal of Numerical Modelling: Electronic Networks, Devices and Fields, 2021, 34, e2872.	1.9	1
46	Design of 4-Bit Array Multiplier Using Multi-wall Carbon Nanotube Interconnects. , 2012, , .		0
47	Thermal effect aware X-bit filling technique for peak temperature reduction during VLSI testing. , 2016, , .		0
48	A stochastic heuristic based approach to test vector reordering for dynamic test power reduction. , 2016, , .		0
49	Temperature dependent stability model for graphene nanoribbon interconnects. AIP Conference Proceedings, 2016, , .	0.4	0
50	Delay minimization of multilayer graphene nanoribbon based interconnect using wire sizing method. , 2016, , .		0
51	Temperature dependent IR-drop and delay analysis in side-contact multilayer graphene nanoribbon based power interconnects. , 2016, , .		0
52	An Inter-Test Cube Bit Stream Connectivity-Optimized X-Filling Approach Aiming Shift Power Reduction. Advances in Intelligent Systems and Computing, 2018, , 477-487.	0.6	0
53	Using Probabilistic Optimization Algorithms to Reduce PAPR in OFDM System. Communications in Computer and Information Science, 2019, , 271-279.	0.5	0
54	Comparative Stability Analysis of Pristine and AsF5 Intercalation Doped Top Contact Graphene Nano Ribbon Interconnects. , 2019, , .		0

#	ARTICLE	IF	CITATIONS
55	Design of Voltage Level Shifter Using CNTFETs. The National Academy of Sciences, India, 2021, 44, 139-142.	1.3	0
56	Design of Voltage Level Shifter Using CNTFET and Analysis of Process Voltage Temperature Variation. Journal of Circuits, Systems and Computers, 0, , .	1.5	0