

Feng Sun

List of Publications by Year in descending order

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Version: 2024-02-01

13
papers

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1684188
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times ranked

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citing authors

#	ARTICLE	IF	CITATIONS
1	An Efficient Neural-Network-Based Microseismic Monitoring Platform for Hydraulic Fracture on an Edge Computing Architecture. <i>Sensors</i> , 2018, 18, 1828.	3.8	19
2	Design of Millimeter-Wave Bandpass Filters With Broad Bandwidth in Si-Based Technology. <i>IEEE Transactions on Electron Devices</i> , 2019, 66, 1174-1181.	3.0	14
3	Design of On-Chip Millimeter-Wave Bandpass Filters Using Multilayer Patterned-Ground Element in 0.13- μm (Bi)-CMOS Technology. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2019, 67, 5159-5170.	4.6	14
4	Hydraulic Fracturing-Induced Seismicity at the Hot Dry Rock Site of the Gonghe Basin in China. <i>Acta Geologica Sinica</i> , 2021, 95, 1835-1843.	1.4	7
5	Miniaturised millimetre-wave BPF with broad stopband suppression in silicon-germanium technology. <i>IET Microwaves, Antennas and Propagation</i> , 2020, 14, 308-313.	1.4	5
6	Design and application of a borehole-surface microseismic monitoring system. <i>Instrumentation Science and Technology</i> , 2017, 45, 233-247.	1.8	4
7	Improvement of a microseismic monitoring data-transmission system based on a load-balancing scheme and a high-throughput polling mechanism. <i>IET Communications</i> , 2019, 13, 3595-3600.	2.2	4
8	On-Chip Millimeter-Wave Bandpass Filter Design Using Multi-Layer Modified-Ground-Ring Structure. , 2019, , .		3
9	Microseismic Event Detection of Hot Dry Rock based on the Gated Recurrent Unit Model and a Support Vector Machine. <i>Acta Geologica Sinica</i> , 2021, 95, 1940-1947.	1.4	2
10	Design of on-chip resonator using distributed capacitors in 0.13- μm (Bi)-CMOS technology. <i>Microwave and Optical Technology Letters</i> , 2021, 63, 107-111.	1.4	1
11	Seismic exploration method to detect underground mined-out areas in the molybdenum mine. <i>WIT Transactions on Engineering Sciences</i> , 2015, , .	0.0	0
12	Design of Compact Millimeter-Wave Bandpass Filter With Multiple Transmission Zeros Using Edge-Coupled Resonators in Silicon Technology. , 2020, , .		0
13	A CMOS 60-GHz Asymmetrical SPDT Switch with Enhanced Power Handling Capability. , 2020, , .		0