

Swagata Bhattacharjee

List of Publications by Year in descending order

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12
papers

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1937685

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docs citations

12
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45
citing authors

#	ARTICLE	IF	CITATIONS
1	Treatment of Femoral Non-Union with the Gene-Activated Osteoplastic Material: Ð-Case Report. <i>Travmatologi I Ortopedi Rossii</i> , 2021, 27, 66-74.	0.5	0
2	Investigation on noise performance of $\text{InAs}_x\text{Sb}_{1-x}$ MOSFETs with compositional variations. <i>Microsystem Technologies</i> , 2020, 26, 1133-1140.	2.0	1
3	Development of noise model for InAsSb MOSFETs and their application in low noise amplifiers. <i>Microsystem Technologies</i> , 2019, 25, 1555-1562.	2.0	3
4	Effects of sidewall spacer layers on thermal and low frequency noise performance of SOI UTB MOSFETs. <i>Microsystem Technologies</i> , 2018, , 1.	2.0	1
5	Analysis of Low Frequency Noise in Nanoscale $\text{InAs}_x\text{Sb}_{1-x}$ MOSFETs with Varying Compositions. , 2018, , .		1
6	Effects of composition fraction on subthreshold behavior of $\text{InAs}_x\text{Sb}_{1-x}$ channel UTB MOSFETs. , 2018, , .		0
7	Analog circuit performance of high mobility ultrathin-body InAsSb -on-insulator MOSFETs. , 2014, , .		1
8	Temperature dependent model for threshold voltage and subthreshold slope of strained-Si channel MOSFETs with a polysilicon gate. <i>Microelectronics Reliability</i> , 2014, 54, 1527-1533.	1.7	9
9	Accurate modeling of the influence of back gate bias and interface roughness on the threshold voltage of nanoscale DG MOSFETs. <i>Microelectronics Reliability</i> , 2013, 53, 363-370.	1.7	5
10	Performance analysis of long Ge channel double gate (DG) p MOSFETs with high-k gate dielectrics based on carrier concentration formulation. <i>Microelectronics Reliability</i> , 2011, 51, 1105-1112.	1.7	7
11	Modeling of threshold voltage and subthreshold slope of nanoscale DG MOSFETs. <i>Semiconductor Science and Technology</i> , 2008, 23, 015010.	2.0	26
12	Effects of gate bias on the threshold voltage of nanoscale double gate MOSFETs. , 2007, , .		3