

# Tao Zhang

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/6147212/publications.pdf>

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15  
papers

166  
citations

3311381

1  
h-index

3475538

1  
g-index

15  
all docs

15  
docs citations

15  
times ranked

224  
citing authors

#	ARTICLE	IF	CITATIONS
1	BACH: A Bandwidth-Aware Hybrid Cache Hierarchy Design with Nonvolatile Memories. Journal of Computer Science and Technology, 2016, 31, 20-35.	1.5	5
2	TSV power supply array electromigration lifetime analysis in 3D ICS. , 2014, , .		0
3	Using multi-level cell STT-RAM for fast and energy-efficient local checkpointing. , 2014, , .		16
4	3D-SWIFT. , 2014, , .		15
5	CREAM: A Concurrent-Refresh-Aware DRAM Memory architecture. , 2014, , .		30
6	Half-DRAM: A high-bandwidth and low-power DRAM architecture from the rethinking of fine-grained activation. , 2014, , .		21
7	Thermomechanical Stress-Aware Management for 3D IC Designs. , 2013, , .		9
8	Lazy Precharge: An overhead-free method to reduce precharge overhead for memory parallelism improvement of DRAM system. , 2013, , .		2
9	An efficient run-time encryption scheme for non-volatile main memory. , 2013, , .		10
10	Using NEM relay to improve 3DIC cost efficiency. , 2012, , .		4
11	MorphCache: A Reconfigurable Adaptive Multi-level Cache hierarchy. , 2011, , .		23
12	Leveraging on-chip DRAM stacking in an embedded 3D multi-core DSP system. , 2011, , .		3
13	A 3D SoC design for H.264 application with on-chip DRAM stacking. , 2010, , .		17
14	A customized design of DRAM controller for on-chip 3D DRAM stacking. , 2010, , .		9
15	Investigation and comparison of thermal distribution in synchronous and asynchronous 3D ICs. , 2009, , .		2