Tao Zhang

List of Publications by Year
in descending order

15 papers	166 citations	3311381 1 h-index	3475538 1 g-index
15 all docs	15 docs citations	15 times ranked	224 citing authors

#	Article	IF	Citations
1	CREAM: A Concurrent-Refresh-Aware DRAM Memory architecture. , 2014, , .		30
2	MorphCache: A Reconfigurable Adaptive Multi-level Cache hierarchy. , 2011, , .		23
3	Half-DRAM: A high-bandwidth and low-power DRAM architecture from the rethinking of fine-grained activation. , 2014, , .		21
4	A 3D SoC design for H.264 application with on-chip DRAM stacking. , 2010, , .		17
5	Using multi-level cell STT-RAM for fast and energy-efficient local checkpointing. , 2014, , .		16
6	3D-SWIFT. , 2014, , .		15
7	An efficient run-time encryption scheme for non-volatile main memory. , 2013, , .		10
8	A customized design of DRAM controller for on-chip 3D DRAM stacking. , 2010, , .		9
9	Thermomechanical Stress-Aware Management for 3D IC Designs. , 2013, , .		9
10	BACH: A Bandwidth-Aware Hybrid Cache Hierarchy Design with Nonvolatile Memories. Journal of Computer Science and Technology, 2016, 31, 20-35.	1.5	5
11	Using NEM relay to improve 3DIC cost efficiency. , 2012, , .		4
12	Leveraging on-chip DRAM stacking in an embedded 3D multi-core DSP system. , 2011, , .		3
13	Investigation and comparison of thermal distribution in synchronous and asynchronous 3D ICs. , 2009, , .		2
14	Lazy Precharge: An overhead-free method to reduce precharge overhead for memory parallelism improvement of DRAM system. , 2013, , .		2
15	TSV power supply array electromigration lifetime analysis in 3D ICS. , 2014, , .		0