

# Avni MorgÃel

## List of Publications by Year in descending order

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16  
papers

165  
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1306789

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docs citations

16  
times ranked

106  
citing authors

#	ARTICLE	IF	CITATIONS
1	An alternative carry-save arithmetic for new generation field programmable gate arrays. Turkish Journal of Electrical Engineering and Computer Sciences, 2016, 24, 435-447.	0.9	0
2	Basic circuits for multi-valued sequential logic. Analog Integrated Circuits and Signal Processing, 2013, 74, 91-96.	0.9	8
3	High-performance CMOS CCI in a 0.35 $\mu\text{m}$ CMOS technology and a new all-pass filter application. Turkish Journal of Electrical Engineering and Computer Sciences, 2013, 21, 1584-1594.	0.9	2
4	ON THE REALIZATION OF HIGH PERFORMANCE CURRENT CONVEYORS AND THEIR APPLICATIONS. Journal of Circuits, Systems and Computers, 2013, 22, 1350015.	1.0	9
5	SELF-BIASING CURRENT CONVEYOR FOR HIGH FREQUENCY APPLICATIONS. Journal of Circuits, Systems and Computers, 2012, 21, 1250039.	1.0	12
6	High Performance Wideband CMOS CCI and its Application in Inductance Simulator Design. Advances in Electrical and Computer Engineering, 2012, 12, 21-26.	0.5	27
7	A current-mode multi-valued adder circuit for multi-operand addition. International Journal of Electronics, 2011, 98, 735-751.	0.9	5
8	AB flip-flop, a first step to multilevel sequential logic. Electronics Letters, 2011, 47, 310.	0.5	2
9	High performance 16-bit MCML multiplier. , 2009, , .		9
10	Wideband self-biased CMOS CCII. , 2008, , .		16
11	Novel multiple function analog filter structures and a dual-mode multifunction filter. International Journal of Electronics, 2006, 93, 637-650.	0.9	3
12	Current-mode level restoration: circuit for multi-valued logic. Electronics Letters, 2005, 41, 230.	0.5	12
13	Implementation of Multi-Valued Logic Gates Using Full Current-Mode CMOS Circuits. Analog Integrated Circuits and Signal Processing, 2004, 39, 191-204.	0.9	38
14	Implementation of multi-valued logic, simultaneous literal operations with full CMOS current-mode threshold circuits. Electronics Letters, 2002, 38, 160.	0.5	16
15	Decision directed channel parameter estimation and tracking using erroneous detectors. Signal Processing, 1991, 25, 307-318.	2.1	1
16	Wide-band hybrid analog/Digital frequency domain adaptive filter. IEEE Transactions on Acoustics, Speech, and Signal Processing, 1984, 32, 762-769.	2.0	5