Avni MorgÃœl

List of Publications by Year
in descending order

16 papers	165 citations	1306789 7 h-index	1199166 12 g-index
16 all docs	16 docs citations	16 times ranked	106 citing authors

#	Article	IF	Citations
1	Implementation of Multi-Valued Logic Gates Using Full Current-Mode CMOS Circuits. Analog Integrated Circuits and Signal Processing, 2004, 39, 191-204.	0.9	38
2	High Performance Wideband CMOS CCI and its Application in Inductance Simulator Design. Advances in Electrical and Computer Engineering, 2012, 12, 21-26.	0.5	27
3	Implementation of multi-valued logic, simultaneous literal operations with full CMOS current-mode threshold circuits. Electronics Letters, 2002, 38, 160.	0.5	16
4	Wideband self-biased CMOS CCII. , 2008, , .		16
5	Current-mode level restoration: circuit for multi-valued logic. Electronics Letters, 2005, 41, 230.	0.5	12
6	SELF-BIASING CURRENT CONVEYOR FOR HIGH FREQUENCY APPLICATIONS. Journal of Circuits, Systems and Computers, 2012, 21, 1250039.	1.0	12
7	High performance 16-bit MCML multiplier. , 2009, , .		9
8	ON THE REALIZATION OF HIGH PERFORMANCE CURRENT CONVEYORS AND THEIR APPLICATIONS. Journal of Circuits, Systems and Computers, 2013, 22, 1350015.	1.0	9
9	Basic circuits for multi-valued sequential logic. Analog Integrated Circuits and Signal Processing, 2013, 74, 91-96.	0.9	8
10	Wide-band hybrid analog/Digital frequency domain adaptive filter. IEEE Transactions on Acoustics, Speech, and Signal Processing, 1984, 32, 762-769.	2.0	5
11	A current-mode multi-valued adder circuit for multi-operand addition. International Journal of Electronics, 2011, 98, 735-751.	0.9	5
12	Novel multiple function analog filter structures and a dual-mode multifunction filter. International Journal of Electronics, 2006, 93, 637-650.	0.9	3
13	AB flip-flop, a first step to multilevel sequential logic. Electronics Letters, 2011, 47, 310.	0.5	2
14	High-performance CMOS CCI in a 0.35 $mu $m CMOS technology and a new all-pass filter application. Turkish Journal of Electrical Engineering and Computer Sciences, 2013, 21, 1584-1594.	0.9	2
15	Decision directed channel parameter estimation and tracking using erroneous detectors. Signal Processing, 1991, 25, 307-318.	2.1	1
16	An alternative carry-save arithmetic for new generation field programmable gate arrays. Turkish Journal of Electrical Engineering and Computer Sciences, 2016, 24, 435-447.	0.9	0