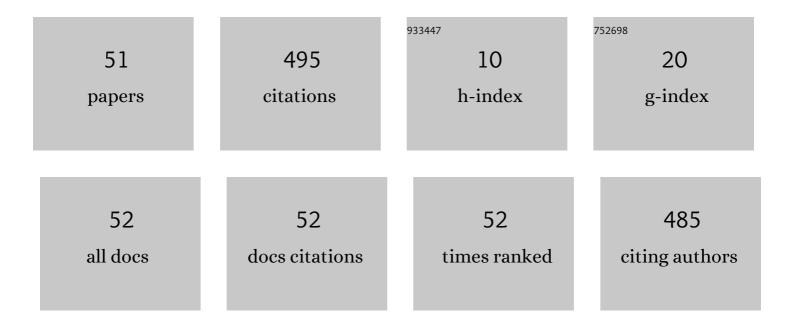
Esmaeil Najafi Aghdam

List of Publications by Year in descending order

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#	Article	IF	CITATIONS
1	Rapid prototyping of whole-thermoplastic microfluidics with built-in microvalves using laser ablation and thermal fusion bonding. Sensors and Actuators B: Chemical, 2018, 255, 100-109.	7.8	104
2	A highly sensitive and specific capacitive aptasensor for rapid and label-free trace analysis of Bisphenol A (BPA) in canned foods. Biosensors and Bioelectronics, 2017, 89, 1059-1067.	10.1	76
3	Design and characterization of a passive, disposable wireless AC-electroosmotic lab-on-a-film for particle and fluid manipulation. Sensors and Actuators B: Chemical, 2016, 235, 330-342.	7.8	49
4	Power efficient, low loss and ultra-high isolation RF MEMS switch dedicated for antenna switch applications. Microelectronics Journal, 2017, 69, 64-72.	2.0	20
5	A novel zero dead zone PFD and efficient CP for PLL applications. Analog Integrated Circuits and Signal Processing, 2018, 95, 83-91.	1.4	19
6	A new electrostatically actuated rotary three-state DC-contact RF MEMS switch for antenna switch applications. Microsystem Technologies, 2017, 23, 231-243.	2.0	18
7	Enhancement of mechanical resonant modes by miniaturization of frequency tunable MEMS-enabled microstrip patch antenna. Microsystem Technologies, 2015, 21, 773-783.	2.0	15
8	A Novel SPDT Rotary RF MEMS Switch for Low Loss and Power Efficient Signal Routing. IETE Journal of Research, 2016, 62, 68-80.	2.6	14
9	A low power current-reuse LC-VCO with an adaptive body-biasing technique. AEU - International Journal of Electronics and Communications, 2018, 89, 56-61.	2.9	14
10	Optimization of ACEK-enhanced, PCB-based biosensor for highly sensitive and rapid detection of bisphenol a in low resource settings. Biosensors and Bioelectronics, 2022, 196, 113745.	10.1	14
11	A novel electrostatically actuated spdt rotary RF MEMS switch for ultra-broadband applications. , 2015, , .		12
12	Digital background calibration algorithm and its FPGA implementation for timing mismatch correction of time-interleaved ADC. Analog Integrated Circuits and Signal Processing, 2019, 99, 299-310.	1.4	11
13	A Low Power, Low Noise, Single-Ended to Differential TIA for Ultrasound Imaging Probes. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 607-611.	3.0	11
14	A low-power CT 2nd order Delta Sigma modulator using a new design methodology for biomedical applications. AEU - International Journal of Electronics and Communications, 2021, 137, 153779.	2.9	8
15	A Dual Band Fractional-N Frequency Synthesizer with a Self-Calibrated Charge Pump for WLAN Standards. Journal of Circuits, Systems and Computers, 2018, 27, 1850131.	1.5	7
16	Adaptive Body Biasing Circuit for Reliability and Variability Compensation of a Low Power RF Amplifier. IEEE Transactions on Device and Materials Reliability, 2019, 19, 226-232.	2.0	7
17	Low complexity digital background calibration algorithm for the correction of timing mismatch in time-interleaved ADCs. Microelectronics Journal, 2019, 83, 117-125.	2.0	7
18	A low power CMOS programmable gain amplifier employing positive feedback technique. International Journal of Circuit Theory and Applications, 2022, 50, 2982-2996.	2.0	7

#	Article	IF	CITATIONS
19	Completely first order and tone free partitioned data weighted averaging technique used in a multibit delta sigma modulator. , 2009, , .		6
20	Frequency agile MEMS patch antenna for reconfigurable RF front-ends. , 2014, , .		6
21	3-state, high contact and release force RF MEMS switch designed based on MetalMUMPs process. , 2017, , .		5
22	A novel noise-coupled time-interleaved delta-sigma modulator with analysis of practical limitations. Analog Integrated Circuits and Signal Processing, 2020, 102, 389-401.	1.4	5
23	A Neurostimulator IC With Impedance-Aware Dynamic-Precision One-Shot Charge Balancing. IEEE Solid-State Circuits Letters, 2021, 4, 202-205.	2.0	5
24	Ultrasonic dispersion system design and optimization using multiple transducers. , 2009, , .		4
25	Reconfigurable hybrid CT/DT delta-sigma modulator with op-amp sharing technique dedicated to multi mode receivers. Analog Integrated Circuits and Signal Processing, 2014, 79, 413-426.	1.4	4
26	Real-Time Control of Resonance Point of Piezoelectric Transducers Based on Class D Power Converter. , 2019, , .		4
27	A CT ΔΣ modulator using 4-bit asynchronous SAR quantizer and MPDWA DEM. AEU - International Journal of Electronics and Communications, 2019, 99, 236-246.	2.9	4
28	A 24-Channel Neurostimulator IC With Channel-Specific Energy-Efficient Hybrid Preventive-Detective Dynamic-Precision Charge Balancing. IEEE Access, 2021, 9, 95884-95895.	4.2	4
29	Stability and sensitivity analysis and optimization of delta sigma modulators. , 2015, , .		3
30	A high sensitive and robust controllable MEMS gyroscope with inherently linear control force using a high performance 2-DOF oscillator. Microsystem Technologies, 2015, 21, 227-237.	2.0	3
31	A 3-11GHz current-reuse low noise amplifier for ultra-wideband recievers. , 2016, , .		3
32	A high speed single-pole two-stage fully differential amplifier with intrinsic CMFB. Analog Integrated Circuits and Signal Processing, 2017, 90, 207-216.	1.4	3
33	Noise-Coupled Time-Interleaved Delta–Sigma Modulator with Reduced Hardware Complexity. Journal of Circuits, Systems and Computers, 2021, 30, 2150071.	1.5	3
34	A 24-Channel Neurostimulator IC with One-Shot Impedance-Adaptive Channel-Specific Charge Balancing. , 2021, , .		3
35	A low power reconfigurable multi-mode continuous time Delta Sigma modulator for seven different mobile standards with VCO-based quantizer. Analog Integrated Circuits and Signal Processing, 2017, 90, 321-331.	1.4	2
36	Design of an IR-UWB transmitter with adaptive PSD in 0.02–1.4 Gpps. , 2017, , .		2

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37	A wide-band noise-cancelling direct-conversion balun-LNA-mixer front-end. Analog Integrated Circuits and Signal Processing, 2018, 96, 67-78.	1.4	2
38	Design and implementation a microcontroller based high power ultrasonic dispersion system with self frequency adjusting property. , 2013, , .		1
39	A new two-path band pass Delta Sigma Modulator structure with tunability in filter resonance frequency. , 2015, , .		1
40	"Skip–Swap―method instead of "Skip–Fill―method in background calibration of pipelined ADCs. Analog Integrated Circuits and Signal Processing, 2015, 84, 127-135.	1.4	1
41	DNC-SMASH structure improvement for high-resolution wideband applications. , 2017, , .		1
42	A Wideband Continuous Time Quadrature Delta Sigma Modulator Based on a Real DSM for Low Power WLAN Receiver. Journal of Circuits, Systems and Computers, 2018, 27, 1850044.	1.5	1
43	A 2.4ÂGHz integer-N frequency synthesizer for ZigBee applications. Analog Integrated Circuits and Signal Processing, 2019, 99, 167-175.	1.4	1
44	An Ultra-Low-Power, 16 Bits CT Delta-Sigma Modulator Using 4-Bit Asynchronous SAR Quantizer for Medical Applications. Journal of Circuits, Systems and Computers, 2020, 29, 2050056.	1.5	1
45	A Power-Efficient Configurable FSK–OOK Transmitter with Scalable Data Rate for Wireless Medical Applications. Circuits, Systems, and Signal Processing, 2020, 39, 2776-2795.	2.0	1
46	Design of a SAW-less, noise-canceling receiver using an LPTV analysis of a general system with an arbitrary number of N-path filters. AEU - International Journal of Electronics and Communications, 2020, 126, 153373.	2.9	1
47	A high contact force and highâ€isolation radioâ€frequency microelectromechanical systems switch for radioâ€frequency frontâ€end applications. International Journal of Circuit Theory and Applications, 0, , .	2.0	1
48	Center frequency and bandwidth tunable band pass delta sigma modulator. , 2016, , .		0
49	A Resource-Optimized Patient-Specific Nonlinear-SVM Hypertension Detection Algorithm for Minimally-Invasive High Blood Pressure Control. , 2020, , .		0
50	An Ultrasonic Tomography Flowmeter Implementation for Gas/Liquid Two-Phase Flow Measurement. , 2021, , .		0
51	A second-order two-channel time-interleaved delta-sigma modulator circuit design. Analog Integrated Circuits and Signal Processing, 0, , .	1.4	Ο