Mingqiang Huang

List of Publications by Year
in descending order

16	1,035	840119	1058022
papers	citations	11	14
		h-index	g-index

17	17	17	1889
all docs	docs citations	times ranked	citing authors

#	Article	IF	Citations
1	Broadband Black‐Phosphorus Photodetectors with High Responsivity. Advanced Materials, 2016, 28, 3481-3485.	11.1	364
2	Multifunctional high-performance van der Waals heterostructures. Nature Nanotechnology, 2017, 12, 1148-1154.	15.6	278
3	Performance Potential and Limit of MoS_2 Transistors. Advanced Materials, 2015, 27, 1547-1552.	11.1	92
4	A transverse tunnelling field-effect transistor made from a van der Waals heterostructure. Nature Electronics, 2020, 3, 106-112.	13.1	69
5	Flexible electronic synapse enabled by ferroelectric field effect transistor for robust neuromorphic computing. Applied Physics Letters, 2020, 117, .	1.5	57
6	Design and Implementation of Ternary Logic Integrated Circuits by Using Novel Two-Dimensional Materials. Applied Sciences (Switzerland), 2019, 9, 4212.	1.3	33
7	High Performance Black Phosphorus Electronic and Photonic Devices with HfLaO Dielectric. IEEE Electron Device Letters, 2018, 39, 127-130.	2.2	31
8	High field transport of high performance black phosphorus transistors. Applied Physics Letters, 2017, 110, .	1.5	27
9	Optimized Transport Properties in Lithium Doped Black Phosphorus Transistors. IEEE Electron Device Letters, 2018, 39, 769-772.	2.2	25
10	Hardware‐Friendly Stochastic and Adaptive Learning in Memristor Convolutional Neural Networks. Advanced Intelligent Systems, 2021, 3, 2100041.	3.3	16
11	Black Phosphorus Radio Frequency Electronics at Cryogenic Temperatures. Advanced Electronic Materials, 2018, 4, 1800138.	2.6	15
12	Global-Gate Controlled One-Transistor One-Digital-Memristor Structure for Low-Bit Neural Network. IEEE Electron Device Letters, 2021, 42, 106-109.	2.2	9
13	Performance Optimization of Atomic Layer Deposited HfO_x Memristor by Annealing With Back-End-of-Line Compatibility. IEEE Electron Device Letters, 2022, 43, 1141-1144.	2.2	9
14	A High Performance Multi-Bit-Width Booth Vector Systolic Accelerator for NAS Optimized Deep Learning Neural Networks. IEEE Transactions on Circuits and Systems I: Regular Papers, 2022, 69, 3619-3631.	3.5	8
15	High-performance two-dimensional transistors and circuits. , 2018, , .		2
16	High performance transistors based on two dimensional materials. , 2017, , .		0