

Diganta Sengupta

List of Publications by Year in descending order

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papers

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2681738

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22
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citing authors

#	ARTICLE	IF	CITATIONS
1	Reversible quantum communication & systems. IET Quantum Communication, 2022, 3, 1-4.	2.2	0
2	Design of synchronous decimal counter using reversible Toffoli's Fredkin Netlist. Innovations in Systems and Software Engineering, 2021, 17, 89-97.	1.6	1
3	Edge Directed Radial Basis Function based Interpolation Towards PCA based PAN-sharpening. International Journal of Remote Sensing, 2021, 42, 9047-9067.	1.3	2
4	Taxonomy on Ambient Computing. International Journal of Ambient Computing and Intelligence, 2020, 11, 1-33.	0.8	4
5	Toffoli Netlist and QCA implementations for existing four variable reversible gates: a comparative analysis. Microsystem Technologies, 2019, 25, 1987-2009.	1.2	4
6	A reversible approach to two's complement addition using a novel reversible TCG gate and its 4 dot 2 electron QCA architecture. Microsystem Technologies, 2019, 25, 1965-1975.	1.2	4
7	Taxonomy of Decimal Multiplier Research. Smart Innovation, Systems and Technologies, 2018, , 3-21.	0.5	1
8	Energy Audit-A Clean, Alternative and Cost-Effective Way to Replace Conventional Energy. , 2018, , .		0
9	IoT Based Home Automation. , 2018, , .		3
10	Logic Design and Quantum Mapping of a Novel Four Variable Reversible s2c2 Gate. Communications in Computer and Information Science, 2018, , 416-427.	0.4	2
11	A Comparative Analysis of CNF and XOR-AND Representations for QCA Majority Gate Estimation. Communications in Computer and Information Science, 2018, , 402-415.	0.4	0
12	Proposal for fast BCD addition. , 2017, , .		2
13	Designing an enhanced ZRP algorithm for MANET and simulation using OPNET. , 2017, , .		7
14	A novel reversible two's complement gate (TCG) and its quantum mapping. , 2017, , .		5
15	Toffoli netlist based synthesis of four variable reversible functions. , 2017, , .		1
16	Digit By Digit Fast Decimal Multiplication. , 2017, , .		0
17	Vedic Sutras. Advances in Computational Intelligence and Robotics Book Series, 2016, , 890-915.	0.4	0
18	Vedivision - A Fast Bcd Division Algorithm Facilitated by Vedic Mathematics. International Journal of Computer Science and Information Technology, 2013, 5, 67-80.	0.3	1

#	ARTICLE	IF	CITATIONS
19	A New Paradigm in Fast BCD Division Using Ancient Indian Vedic Mathematics Sutras. , 2013, , .		2
20	An algorithm facilitating fast BCD division on low end processors using Ancient Indian Vedic Mathematics Sutras. , 2012, , .		2
21	Design and Implementation of an Open Ended Automated Vault Security System. International Journal of Applied Information Systems, 2012, 4, 13-18.	0.1	0