

Hyeonggyu Kim

List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/6119513/publications.pdf>

Version: 2024-02-01

9
papers

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citations

1684129
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10
docs citations

10
times ranked

75
citing authors

#	ARTICLE	IF	CITATIONS
1	The rapid and dense assembly of solution-processed single-wall carbon nanotube semiconducting films via an acid-based additive in the aqueous dispersion. <i>Journal of Materials Chemistry C</i> , 2016, 4, 5461-5468.	5.5	19
2	Tunable Stability of All-Inkjet-Printed Double-Gate Carbon Nanotube Thin Film Transistors. <i>IEEE Electron Device Letters</i> , 2020, 41, 860-863.	3.9	11
3	Multidipping Technique for Fabrication Time Reduction and Performance Improvement of Solution-Processed Single-Walled Carbon Nanotube Thin-Film Transistors. <i>Advanced Engineering Materials</i> , 2020, 22, 1901413.	3.5	10
4	Tunable threshold voltage in solution-processed single-walled carbon nanotube thin-film transistors. <i>Current Applied Physics</i> , 2015, 15, S8-S11.	2.4	9
5	Dense Assembly of Finely Patterned Semiconducting Single-Walled Carbon Nanotubes via a Selective Transfer Method of Nanotube-Attracting Layers. <i>ACS Applied Materials & Interfaces</i> , 2020, 12, 38441-38450.	8.0	6
6	P&C29: Solution-Processed Single-Walled Carbon Nanotube Thin Film Transistors In-situ Patterned by Inkjet-Printing of Surface Treatment Material. <i>Digest of Technical Papers SID International Symposium</i> , 2019, 50, 1321-1324.	0.3	4
7	Enhanced current path by circularly and periodically-aligned semiconducting single-walled carbon nanotubes for logic circuit device. <i>Flexible and Printed Electronics</i> , 2022, 7, 015005.	2.7	3
8	P&C189: Late-News-Poster: In-situ Selective UV-O ₃ based Facile Patterning Method of Random SWCNT Networks for Solution-Processed SWCNT TFT and Circuit Application. <i>Digest of Technical Papers SID International Symposium</i> , 2020, 51, 2113-2116.	0.3	0
9	P&C190: Late-News-Poster: Micrometer-scale Patterning of Self-assembled SWCNT Films and Thin-Film Transistors Using Patterned PLL Layer. <i>Digest of Technical Papers SID International Symposium</i> , 2020, 51, 2117-2120.	0.3	0