

Dean H Lorenz

List of Publications by Year in descending order

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Version: 2024-02-01

20
papers

775
citations

1305906

8
h-index

1637695

9
g-index

21
all docs

21
docs citations

21
times ranked

720
citing authors

#	ARTICLE	IF	CITATIONS
1	AnchorHash: A Scalable Consistent Hash. IEEE/ACM Transactions on Networking, 2021, 29, 517-528.	2.6	12
2	Load balancing with JET. , 2021, , .		1
3	AIOps for a Cloud Object Storage Service. , 2019, , .		16
4	Estimating client QoE from measured network QoS. , 2019, , .		3
5	Enable Advanced QoS-Aware Network Slicing in 5G Networks for Slice-Based Media Use Cases. IEEE Transactions on Broadcasting, 2019, 65, 444-453.	2.5	65
6	Heterogeneous Resource Reservation. , 2018, , .		6
7	Hierarchical load balancing as a service for federated cloud networks. Computer Communications, 2018, 129, 125-137.	3.1	12
8	SliceNet: End-to-End Cognitive Network Slicing and Slice Management Framework in Virtualised Multi-Domain, Multi-Tenant 5G Networks. , 2018, , .		30
9	Network Monitoring in Federated Cloud Environment. , 2017, , .		1
10	CogNETive. , 2017, , .		1
11	Composite-Path Switching. , 2016, , .		4
12	Cloud services brokering for elastic workloads. , 2015, , .		0
13	Guaranteeing High Availability Goals for Virtual Machine Placement. , 2011, , .		90
14	Virtual Appliance Content Distribution for a Global Infrastructure Cloud Service. , 2010, , .		9
15	IP mobility to support live migration of virtual machines across subnets. , 2009, , .		29
16	Efficient QoS Partition and Routing of Unicast and Multicast. IEEE/ACM Transactions on Networking, 2006, 14, 1336-1347.	2.6	21
17	Optimal partition of QoS requirements on unicast paths and multicast trees. IEEE/ACM Transactions on Networking, 2002, 10, 102-114.	2.6	66
18	A simple efficient approximation scheme for the restricted shortest path problem. Operations Research Letters, 2001, 28, 213-219.	0.5	212

#	ARTICLE	IF	CITATIONS
19	QoS routing in networks with uncertain parameters. IEEE/ACM Transactions on Networking, 1998, 6, 768-778.	2.6	179
20	A methodology for processor implementation verification. Lecture Notes in Computer Science, 1996, , 126-142.	1.0	12