

Lee, Ricky S W

List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/6083513/publications.pdf>

Version: 2024-02-01

171
papers

971
citations

933447

10
h-index

794594

19
g-index

172
all docs

172
docs citations

172
times ranked

431
citing authors

#	ARTICLE	IF	CITATIONS
1	Fabrication and Reliability Assessment of Cu Pillar Microbumps With Printed Polymer Cores. Journal of Electronic Packaging, Transactions of the ASME, 2021, 143, .	1.8	7
2	Characterization and Evaluation of 3D-Printed Connectors for Microfluidics. Micromachines, 2021, 12, 874.	2.9	2
3	Evaluation and Reduction of Optical Crosstalk in Quantum Dot Color-Converted Mini/Micro-LED Displays. , 2021, , .		0
4	Quantum Dot Film Patterning on a Trenched Glass Substrate for Defining Pixel Arrays of a Full-color Mini/Micro-LED Display. , 2020, , .		2
5	Study on Light Emitting Surface Temperature of LEDs. , 2020, , .		0
6	Printing Uniform QDs Polymer Thin Film for QLED Applications. , 2020, , .		1
7	UV LED Assisted Printing Platform for Fabrication of Micro-Scale Polymer Pillars. Journal of Microelectromechanical Systems, 2020, 29, 1523-1530.	2.5	6
8	Assessment of Fatigue Induced Pad Cratering With a Universal Expression of Printed Circuit Board Fatigue Resistance. Journal of Electronic Packaging, Transactions of the ASME, 2020, 142, .	1.8	3
9	Dimensionless Correlation between Empirical Modeling and T3ster Measurements for the Dynamic Thermal Characterization of the PWM-mode Current Driving UVLED. , 2020, , .		2
10	Evaluation and Benchmarking of Cu Pillar Micro-bumps with Printed Polymer Core. , 2019, , .		5
11	Long-term Stability Evaluation of Thermal Interface Materials (TIMs). , 2019, , .		2
12	Junction Temperature Prediction of the Multi-LED Module with the Modified Thermal Resistance Matrix. , 2019, , .		0
13	Empirical Modeling and measurement of the Pulsed Junction Temperature of VCSEL. , 2019, , .		3
14	The Effect of Metallic Interconnect Spacing on the Thermal Resistance of Flip-Chip Light-Emitting Diodes With Underfill Encapsulation. IEEE Transactions on Components, Packaging and Manufacturing Technology, 2019, 9, 871-876.	2.5	2
15	Filler Particle-Induced Light Absorption in Underfill-Encapsulated Flip-Chip Light-Emitting Diodes. IEEE Transactions on Components, Packaging and Manufacturing Technology, 2019, 9, 562-566.	2.5	0
16	Inkjet Printing PEDOT:PSS without Coffee Ring Effect for QLED Applications. , 2019, , .		4
17	A Novel Approach to Characterize Phosphor Particles for the Color Tuning of WLEDs. IEEE Photonics Technology Letters, 2018, 30, 513-516.	2.5	3
18	Broadband Emission of Asymmetric Pyramids obtained by Laser Drilling and SAG for Application of Phosphor Free White LEDs. , 2018, , .		0

#	ARTICLE	IF	CITATIONS
19	Investigation of the Influence of Packaging Material Degradation on the Optical Performance of Phosphor Converted White Light Emitting Diodes. , 2018, , .		0
20	Quantum Dot Light Emitting Diodes Based on ZnO Nanoparticles. , 2018, , .		4
21	Measurement of Dynamic Junction Temperature for LED Flash Units of Camera. , 2018, , .		6
22	Analysis of Pulse-Driven LED Junction Temperature and its Reliability. , 2018, , .		4
23	Investigation of Light Scattering Effect Impact on Optical Performance of Phosphor Converted White Light Emitting Diodes. , 2018, , .		0
24	Additive Manufacturing of Micro-channels on a Silicon Substrate based on Coaxial Printing Dispenser with in-situ UV LED Curing. , 2018, , .		0
25	Thermal Simulation for High Power LED Systems with Remote Phosphor Layer. , 2018, , .		0
26	Correlation of Warpage Distribution With the Material Property Scattering for Warpage Range Prediction of PBGA Components. Journal of Electronic Packaging, Transactions of the ASME, 2018, 140, .	1.8	6
27	Super flexible GaN light emitting diodes using microscale pyramid arrays through laser lift-off and dual transfer. Optics Express, 2018, 26, 1817.	3.4	18
28	Solderability evaluation of printed silver solder pad for wafer-level packaging. , 2018, , .		0
29	A power inductor integration technology using a silicon interposer for DC-DC converter applications. , 2018, , .		4
30	Fan-Out Wafer-Level Packaging for Heterogeneous Integration. IEEE Transactions on Components, Packaging and Manufacturing Technology, 2018, 8, 1544-1560.	2.5	46
31	Chip-First Fan-Out Panel-Level Packaging for Heterogeneous Integration. IEEE Transactions on Components, Packaging and Manufacturing Technology, 2018, 8, 1561-1572.	2.5	23
32	Determination of a Meaningful Warpage Acceptance Criterion for Large PBGA Components Through the Correlation with Scattering in Material Properties. , 2018, , .		3
33	Packaging of UV LED with a stacked silicon reflector for converged UV emission. , 2017, , .		5
34	Modeling of residual strain in BGA-PCB assemblies for pad cratering control. , 2017, , .		2
35	Thermal characterization of multi-chip light emitting diodes with thermal resistance matrix. , 2017, , .		6
36	Investigation the effect of silane onto fabricating polymer insulation layer by spin-coating for through silicon vias. , 2017, , .		0

#	ARTICLE	IF	CITATIONS
37	Pad Cratering Based Failure Criterion for the Life Prediction of Board Level Cyclic Bending Test. , 2017, , .		3
38	Growth, characterization, and application of well-defined separated GaN-based pyramid array on micropatterned sapphire substrate. Applied Physics Express, 2017, 10, 092101.	2.4	5
39	Experimental parametric study on the bumping and coining of gold studs for flip chip bonding. , 2016, , .		0
40	Effect of substrate dimensions and boundary conditions on the heat spreading of LED package. , 2016, , .		4
41	Sensitivity study on the precision of data acquisition for LED life prediction based on the degradation of luminous output. , 2016, , .		0
42	Investigation on the influence of phosphor particle size gradient on the optical performance of white light-emitting diodes. , 2016, , .		0
43	Fabricating photosensitive polymer insulation layer by spin-coating for through silicon vias. , 2016, , .		0
44	Correlation of board and joint level test methods with strain dominant failure criteria for improving the resistance to pad cratering. , 2016, , .		6
45	Low-dielectric-constant novel periodic mesoporous organosilica thin film for interlayer dielectric. , 2016, , .		0
46	Effect of the surface curvature of the phosphor layer on the optical performance of a WLED. , 2016, , .		0
47	Void-free underfill encapsulation for flip chip high voltage LED packaging. , 2016, , .		1
48	Characterization of orthotropic CTE of BT substrate for PBGA warpage evaluation. , 2016, , .		6
49	Application of Nano Silver sintering technique on the chip bonding for flip-chip and vertical light emitting diodes. , 2016, , .		1
50	A method for measuring thermal conductivity of paste materials. , 2016, , .		0
51	Numerical prediction and experimental validation of multiple phosphor white LED spectrum. , 2016, , .		3
52	Novel periodic mesoporous organosilica thin film with low dielectric constant and high mechanical property. , 2016, , .		0
53	Investigation of reliability of EMC and SMC on reflectance for UV LED applications. , 2016, , .		6
54	A novel chip-on-board white light-emitting diode design for light extraction enhancement. , 2016, , .		2

#	ARTICLE	IF	CITATIONS
55	Detection of the non-uniformity of junction temperature in large light-emitting diode using an improved forward voltage method. , 2015, , .		0
56	Modeling and Parametric Study of Light Scattering, Absorption and Emission of Phosphor in a White Light-Emitting Diode. , 2015, , .		4
57	Fabricating polymer insulation layer by spin-coating for through silicon vias. , 2015, , .		3
58	Fluxless packaging of an implantable medical device for transcorneal electrical stimulation. , 2015, , .		1
59	Thermally conductive adhesives based on silver coated copper flake fillers. , 2015, , .		0
60	Wafer level bumping technology for high voltage LED packaging. , 2015, , .		6
61	Influence of rubber nanoparticles on the properties of Novolac-diazonaphthoquinone based photoresist. , 2015, , .		0
62	Investigation on the thermal degradation mechanism of Cu-Sn intermetallic compound in SAC solder joints with Cohesive Zone modeling. , 2015, , .		0
63	Investigation of the influence of Ag reflective layer on the Correlated Color Temperature and the Angular Color Uniformity of LED with conformal phosphor coating. , 2015, , .		3
64	Development of a real-time monitoring system with uni-photodetector for LED long term reliability tests. , 2014, , .		3
65	Comparison of Ball Pull Strength Among Various Sn-Cu-Ni Solder Joints With Different Pad Surface Finishes. Journal of Electronic Packaging, Transactions of the ASME, 2014, 136, .	1.8	2
66	Multilayer dispensing of remote phosphor for LED wafer level packaging with pre-formed silicone lens. , 2014, , .		4
67	Lens forming by stack dispensing for LED wafer level packaging. , 2014, , .		3
68	Through silicon underfill dispensing for 3D die/interposer stacking. , 2014, , .		7
69	O ₂ plasma treatment in polymer insulation process for through silicon vias. , 2014, , .		5
70	Remote phosphor deposition on LED arrays with pre-encapsulated silicone lens. , 2013, , .		7
71	Wetting behavior of polymer liquid in insulation process for through silicon via. , 2013, , .		2
72	Quasi-conformal phosphor dispensing on LED for white light illumination. , 2013, , .		5

#	ARTICLE	IF	CITATIONS
73	Investigation on the properties and processability of polymeric insulation layers for through silicon via. , 2013, , .		7
74	Measurement and analysis of interfacial adhesion strength between the silicone encapsulant and the side wall of a SMD LED leadframe cup. , 2013, , .		1
75	Optical characterization of a light bulb with the waffle pack LED WLP module. , 2013, , .		3
76	Thermal characterization of a printed circuit board with thermal vias for the application of high brightness light-emitting diodes. , 2013, , .		0
77	Design and implementation of fine pitch COB LED display. , 2013, , .		2
78	Assessment of solder pad cratering strength using cold pin pull test method with pre-fabricated pin arrays. , 2013, , .		7
79	Experimental characterization of adhesion strength between the silicone encapsulant and the bottom of a SMD LED leadframe cup. , 2013, , .		0
80	Brittle fracture of intermetallic compounds in SAC solder joints under high speed ball pull/pin pull and Charpy impact tests. , 2013, , .		6
81	Knowledge Grows Only by Being Shared. Journal of Japan Institute of Electronics Packaging, 2013, 16, P1-P1.	0.1	0
82	Determination of driving current of RGB LEDs for white light illumination. , 2012, , .		7
83	Reverse wire bonding and phosphor printing for LED wafer level packaging. , 2012, , .		5
84	Assessment of non-uniform temperature effect on BGA de-component process. , 2012, , .		0
85	Effect of die attach adhesive defects on the junction temperature uniformity of LED chips. , 2012, , .		7
86	LED wafer level packaging with a remote phosphor cap. , 2012, , .		2
87	Development of innovative cold pin pull test method for solder pad crater evaluation. , 2012, , .		5
88	Effects of GaN blue LED chip and phosphor on optical performance of white light LED. , 2012, , .		0
89	Investigation of fracture behaviors of Cu-Sn intermetallics using impact test. , 2012, , .		0
90	Design and Fabrication of a Silicon Interposer With TSVs in Cavities for Three-Dimensional IC Packaging. IEEE Transactions on Device and Materials Reliability, 2012, 12, 189-193.	2.0	13

#	ARTICLE	IF	CITATIONS
91	TSV plating using copper methanesulfonate electrolyte with single component suppressor. , 2012, , .		5
92	Emerging trend for LED wafer level packaging. <i>Frontiers of Optoelectronics</i> , 2012, 5, 119-126.	3.7	9
93	Advanced LED wafer level packaging technologies. , 2011, , .		5
94	Enhancement of thermal conductivity of die attach adhesives (DAAs) using nanomaterials for high brightness light-emitting diode (HBLED). , 2011, , .		10
95	Investigation on lead-free solder joint reliability of edge-bonded CBGA under temperature cycling. , 2011, , .		0
96	LED packaging using silicon substrate with cavities for phosphor printing and copper-filled TSVs for 3D interconnection. , 2011, , .		21
97	Study on copper plating solutions for fast filling of through silicon via (TSV) in 3D electronic packaging. , 2011, , .		8
98	A comparison of copper sulfate and methanesulfonate electrolytes in the copper plating process for through silicon via metallization. , 2011, , .		1
99	Shock Test Methods and Test Standards for Portable Electronic Devices. , 2011, , 159-173.		2
100	Evaluation of polymer wafer bonding with silicone adhesive and patterned trenches. , 2011, , .		1
101	Considerations in solution stabilization for thermal fatigue modeling of lead-free solder joints. , 2011, , .		1
102	Effect of interfacial strength between CuSn and CuSn intermetallics on the brittle fracture failure of lead-free solder joints with OSP pad finish. , 2011, , .		7
103	Spacing optimization of high power LED arrays for solid state lighting. <i>Journal of Semiconductors</i> , 2011, 32, 014005.	3.7	9
104	Embedded Three-Dimensional Hybrid Integrated Circuit Integration System-in-Package With Through-Silicon Vias for Opto-Electronic Interconnects in Organic Substrates/Printed Circuit Boards. <i>Journal of Electronic Packaging, Transactions of the ASME</i> , 2011, 133, .	1.8	14
105	Comparison of Fatigue Crack Initiation/Propagation and Daisy-Chain Resistance in Lead-Free Solder Joints Under Temperature Cycling Test. , 2011, , .		1
106	Fast Copper Plating Process for Through Silicon Via (TSV) Filling. , 2011, , .		6
107	Embedded 3D Hybrid IC Integration System-in-Package (SiP) for Opto-Electronic Interconnects in Organic Substrates. , 2010, , .		11
108	Detailed investigation on the creep damage accumulation of lead-free solder joints under accelerated temperature cycling. , 2010, , .		7

#	ARTICLE	IF	CITATIONS
109	Modeling of board level solder joint reliability under mechanical drop test with the consideration of plastic strain hardening of lead-free solder. , 2010, , .		3
110	Nondestructive inspection of through silicon via depth using scanning acoustic microscopy. , 2010, , .		2
111	Thermal-Enhanced and Cost-Effective 3D IC Integration With TSV (Through-Silicon Via) Interposers for High-Performance Applications. , 2010, , .		12
112	Solid state light tube with periodic units of lateral-emitting LEDs. , 2010, , .		1
113	Comparative study of PWB pad cratering subject to reflow soldering and thermal impact. , 2010, , .		8
114	Effects of corner and edgebond epoxy adhesives on board level solder joint reliability of BGA mezzanine connectors. , 2010, , .		11
115	Integration of phosphor printing and encapsulant dispensing processes for wafer level LED array packaging. , 2010, , .		17
116	Investigation of lead-free BGA solder joint reliability under 4-point bending using PWB strain-rate analysis. , 2009, , .		6
117	Solder Ball Attachment Assessment of Reballled Plastic Ball Grid Array Packages. IEEE Transactions on Components and Packaging Technologies, 2009, 32, 901-908.	1.3	13
118	Process development and prototyping for the assembly of LED arrays on flexible printed circuit tape for general solid state lighting. , 2009, , .		1
119	Investigation of mixed SAC and SnPb solder balls under high speed ball shear and pull tests. , 2009, , .		3
120	Comparison of thermal fatigue reliability of SnPb and SAC solders under various stress range conditions. , 2009, , .		6
121	Effect of UBM and BCB layers on the thermo-mechanical reliability of wafer level chip scale package (WLCSP). , 2009, , .		6
122	Critical Review of the Engelmaier Model for Solder Joint Creep Fatigue Reliability. IEEE Transactions on Components and Packaging Technologies, 2009, 32, 693-700.	1.3	42
123	Material characterization of corner and edgebond epoxy adhesives for the improvement of board-level solder joint reliability. , 2009, , .		6
124	Surface analysis of outgassing contamination from edgebond epoxy adhesives on ImAg pads. , 2009, , .		1
125	Assessment of Relative Thermal Fatigue Life of SAC Lead-Free and Tin-Lead Solders With Custom-Made BGA Assemblies Creating Various Stress Ranges. , 2009, , .		0
126	Gold Embrittlement of Solder Joints in Wafer-Level Chip-Scale Package on Printed Circuit Board With Ni/Au Surface Finish. IEEE Transactions on Electronics Packaging Manufacturing, 2008, 31, 185-191.	1.4	8

#	ARTICLE	IF	CITATIONS
127	Wafer level LED packaging with integrated DRIE trenches for encapsulation. , 2008, , .		25
128	Development of Novel Dicing Process by Anisotropic Wet Etching with Convex Corner Compensation. , 2008, , .		2
129	A comprehensive parallel study on the board level reliability of SAC, SACX and SCN solders. , 2008, , .		1
130	Numerical analysis and experimental validation for the prediction of flip chip solder joint standoff height in MEMS microphone application. , 2008, , .		0
131	Design and fabrication of an epoxy flow stopper with convex corner compensation of V-grooves on a silicon optical bench for the passive alignment of optical fibers. , 2008, , .		0
132	Determination of solder bump stand-off height in a flip-chip sub-mount for Micro-Opto-Electro-Mechanical System (MOEMS) packaging applications. , 2008, , .		1
133	Correlation Between Measurement and Simulation of Thermal Warpage in PBCA With Consideration of Molding Compound Residual Strain. IEEE Transactions on Components and Packaging Technologies, 2008, 31, 683-690.	1.3	37
134	Development of surface mount compatible reel-to-reel assembly Process of LED arrays for wide area general lighting. , 2008, , .		0
135	Stress Analysis of Hygrothermal Delamination of Quad Flat No-Lead (QFN) Packages. , 2008, , .		5
136	Investigation of Moisture Sensitivity Related Failure Mechanism of Quad Flat No-Lead (QFN) Packages. , 2008, , .		3
137	Fabrication of Nanoscale Vias by Offset Patterning. , 2008, , .		0
138	High-Speed Solder Ball Shear and Pull Tests vs. Board Level Mechanical Drop Tests: Correlation of Failure Mode and Loading Speed. , 2007, , .		55
139	Screen-Printing of Yellow Phosphor Powder on Blue Light Emitting Diode (LED) Arrays for White Light Illumination. , 2007, , 19.		15
140	Development of 2D Modeling Techniques for the Thermal Fatigue Analysis of Solder Joints of a Module Mounted in a 3D Cavity on a Printed Circuit Board. , 2007, , .		1
141	Effect of V-groove Side Wall Feature on Epoxy Flow in Passive Alignment of Optical Fiber. , 2007, , .		1
142	Effect of Thermal Aging on High Speed Ball Shear and Pull Tests of SnAgCu Lead-free Solder Balls. , 2007, , .		21
143	Brittle Failure Mechanism of SnAgCu and SnPb Solder Balls during High Speed Ball Shear and Cold Ball Pull Tests. , 2007, , .		52
144	Characterization and Comparison of Five SAC-based Solder Pastes for Pb-free Reflow Soldering. , 2007, , .		3

#	ARTICLE	IF	CITATIONS
145	Characteristics of Copper-to-Silicon diffusion in copper wire bonding. , 2007, , .		1
146	Comparison of Joint Strength and Fracture Energy of Lead-free Solder Balls in High Speed Ball Shear/Pull Tests and their Correlation with Board Level Drop Test. , 2007, , .		17
147	Design and Fabrication of MEMS Based Microphone Module by Using 3D Chip-on-Chip Package. , 2007, , .		0
148	Effects of Tooling Tip Wear and Fixture Rigidity on Solder Ball Shear and Ball Pull Tests. , 2007, , .		0
149	Comparison between Experimental Measurement and Numerical Analysis of Warpage in PBGA Package and Assembly with the Consideration of Residual Strain in the Molding Compound. , 2006, , .		2
150	Impact of IMC Thickness on Lead-free Solder Joint Reliability under Thermal Aging: Ball Shear Tests vs. Cold Bump Pull Tests. , 2006, , .		0
151	Design, Process Development and Prototyping of 3D Packaging with Multi-Stacked Flip Chips and Peripheral Through Silicon Via Interconnection. Electronics Manufacturing Technology Symposium (IEMT), IEEE/CPMT International, 2006, , .	0.0	0
152	Experimental Testing and Failure Prediction of PBGA Package Assemblies under 3-Point Bending Condition through Computational Stress Analysis. , 2006, , .		10
153	Thermal Resistance Analysis of a Multi-Stack Flip Chip 3-D Package. , 2006, , .		3
154	Reliability Assessment on the Re-Balling of PBGA from SnPb to Pb-Free Solder Spheres. , 2006, , .		2
155	Wetting Characteristics of Solder Reflow on Copper Traces without Solder Mask. , 2006, , .		1
156	Investigation of the Effect of PCB Base Materials and Pad Surface Finish on the Thermal Fatigue Life of Lead-free Solder Joints of PBGA and Passive Resistors. , 2006, , .		0
157	Effect of Solder Mask Thickness on Shear and Pull Tests of Lead-Free Solder Balls. , 2006, , .		0
158	Thermal-Fatigue Life Prediction Equation for Plastic Ball Grid Array (PBGA) SnAgCu Lead-Free Solder Joints. , 2005, , 1013.		1
159	Fabrication of Sub-Micron Silicon Vias by Deep Reactive Ion Etching. , 2005, , 947.		0
160	Evaluation of Bondability and Reliability of Single Crystal Copper Wire Bonding. , 2005, , .		6
161	Experimental Investigation of the Effect of Reflow Cooling Rate on the IMC Growth of SAC Lead-Free Solder Alloy. , 2005, , .		3
162	Correlation Between the Strain on the Printed Circuit Board and the Stress in Chips for the Failure Prediction of Passive Components. , 2005, , .		3

#	ARTICLE	IF	CITATIONS
163	Formation of Through-Silicon-Vias by Laser Drilling and Deep Reactive Ion Etching. , 2004, , .		2
164	A New Method for the Solder Ball Pull Test Using a Shape Memory Alloy Tube. , 2004, , .		0
165	Effects of Thermal Aging and Au Addition on the Electrical Resistance of Solder Balls in Flip Chip and Ultra-CSP Packages. , 2003, , .		0
166	Passive Alignment of Optical Fiber in a V-Groove With Low Viscosity Epoxy Flow. , 2003, , .		1
167	Micro-machined Conformal Silicon Molds for Wafer Bumping and Probing. International Journal of Nonlinear Sciences and Numerical Simulation, 2002, 3, .	1.0	0
168	Effects of Room Temperature Storage Time on the Shear Strength of PBGA Solder Balls. , 2002, , 259.		26
169	Nonlinear-Time-Dependent Analysis of Micro Via-In-Pad Substrates for Solder Bumped Flip Chip Applications. Journal of Electronic Packaging, Transactions of the ASME, 2002, 124, 205-211.	1.8	17
170	Temperature-Dependent Popcorning Analysis of Plastic Ball Grid Array Package During Solder Reflow With Fracture Mechanics Method. Journal of Electronic Packaging, Transactions of the ASME, 2000, 122, 34-41.	1.8	34
171	Effects of electric fields on the bending behavior of PZT-5H piezoelectric laminates. Smart Materials and Structures, 2000, 9, 824-831.	3.5	29