

# Chia-Sheng Hsu

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/6051214/publications.pdf>

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1684188

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#	ARTICLE	IF	CITATIONS
1	Physics-Based Models for Magneto-Electric Spin-Orbit Logic Circuits. IEEE Journal on Exploratory Solid-State Computational Devices and Circuits, 2022, 8, 10-18.	1.5	4
2	Evaluating the Performances of the Ultralow Power Magnetoelectric Random Access Memory With a Physics-Based Compact Model of the Antiferromagnet/Ferromagnet Bilayer. IEEE Transactions on Electron Devices, 2022, 69, 2331-2337.	3.0	7
3	Hysteresis-Free Negative Capacitance Effect in Metal-Ferroelectric-Insulator-Metal Capacitors with Dielectric Leakage and Interfacial Trapped Charges. Physical Review Applied, 2021, 15, .	3.8	6
4	Understanding the Switching Mechanisms of the Antiferromagnet/Ferromagnet Heterojunction. Nano Letters, 2020, 20, 7919-7926.	9.1	11
5	A Theoretical Study of Multidomain Ferroelectric Switching Dynamics With a Physics-Based SPICE Circuit Model for Phase-Field Simulations. IEEE Transactions on Electron Devices, 2020, 67, 2952-2959.	3.0	10
6	Performance Analysis and Enhancement of Negative Capacitance Logic Devices Based on Internally Resistive Ferroelectrics. IEEE Electron Device Letters, 2018, 39, 765-768.	3.9	11