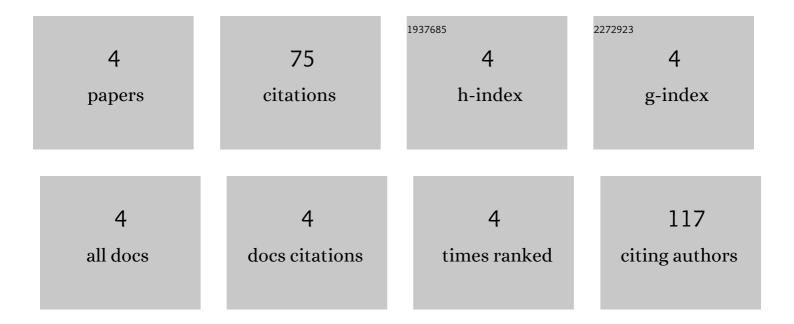
Angada B Sachid

List of Publications by Year in descending order

Source: https://exaly.com/author-pdf/6030560/publications.pdf

Version: 2024-02-01



#	Article	IF	CITATIONS
1	Bulk FinFET With Low- \$kappa \$ Spacers for Continued Scaling. IEEE Transactions on Electron Devices, 2017, 64, 1861-1864.	3.0	38
2	Nanowire FET With Corner Spacer for High-Performance, Energy-Efficient Applications. IEEE Transactions on Electron Devices, 2017, 64, 5181-5187.	3.0	23
3	Modeling of Back-Gate Effects on Gate-Induced Drain Leakage and Gate Currents in UTB SOI MOSFETs. IEEE Transactions on Electron Devices, 2017, 64, 3986-3990.	3.0	9
4	New Mobility Model for Accurate Modeling of Transconductance in FDSOI MOSFETs. IEEE Transactions on Electron Devices, 2018, 65, 463-469.	3.0	5