Muhammad Nadzir Marsono

List of Publications by Year in descending order

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| # | Article | IF | CITATIONS |
|----|---|-----|-----------|
| 1 | ProNoC: A low latency network-on-chip based many-core system-on-chip prototyping platform. Microprocessors and Microsystems, 2017, 54, 60-74. | 2.8 | 47 |
| 2 | Flow-Aware Elephant Flow Detection for Software-Defined Networks. IEEE Access, 2020, 8, 72585-72597. | 4.2 | 36 |
| 3 | Biometric encryption based on a fuzzy vault scheme with a fast chaff generation algorithm. Future Generation Computer Systems, 2013, 29, 800-810. | 7.5 | 33 |
| 4 | FEATURE SELECTION AND MACHINE LEARNING CLASSIFICATION FOR MALWARE DETECTION. Jurnal Teknologi (Sciences and Engineering), 2015, 77, . | 0.4 | 26 |
| 5 | An FPGA-Based Quantum Computing Emulation Framework Based on Serial-Parallel Architecture. International Journal of Reconfigurable Computing, 2016, 2016, 1-18. | 0.2 | 26 |
| 6 | Online NetFPGA decision tree statistical traffic classifier. Computer Communications, 2013, 36, 1329-1340. | 5.1 | 25 |
| 7 | A hardware architecture of Prewitt edge detection. , 2010, , . | | 22 |
| 8 | Low Latency Network-on-Chip Router Microarchitecture Using Request Masking Technique. International Journal of Reconfigurable Computing, 2015, 2015, 1-13. | 0.2 | 21 |
| 9 | A linked list run-length-based single-pass connected component analysis for real-time embedded hardware. Journal of Real-Time Image Processing, 2018, 15, 197-215. | 3.5 | 21 |
| 10 | FPGA-Based Real-Time Moving Target Detection System for Unmanned Aerial Vehicle Application. International Journal of Reconfigurable Computing, 2016, 2016, 1-16. | 0.2 | 19 |
| 11 | Binary LNS-based nail`ve Bayes inference engine for spam control: noise analysis and FPGA implementation. IET Computers and Digital Techniques, 2008, 2, 56. | 1.2 | 18 |
| 12 | Targeting spam control on middleboxes: Spam detection based on layer-3 e-mail content classification. Computer Networks, 2009, 53, 835-848. | 5.1 | 18 |
| 13 | Hardware Acceleration of OpenSSL Cryptographic Functions for High-Performance Internet Security. , 2010, , . | | 18 |
| 14 | Hardware implementation of evolvable block-based neural networks utilizing a cost efficient sigmoid-like activation function. Neurocomputing, 2014, 140, 228-241. | 5.9 | 16 |
| 15 | Stateless Malware Packet Detection by Incorporating Naive Bayes with Known Malware Signatures. Applied Computational Intelligence and Soft Computing, 2014, 2014, 1-8. | 2.3 | 14 |
| 16 | Collaborative Detection and Mitigation of Distributed Denial-of-Service Attacks on Software-Defined Network. Mobile Networks and Applications, 2020, 25, 1338-1347. | 3.3 | 14 |
| 17 | Edge Computing Intelligence Using Robust Feature Selection for Network Traffic Classification in Internet-of-Things. IEEE Access, 2020, 8, 224059-224070. | 4.2 | 13 |
| 18 | Collaborative detection and mitigation of DDoS in software-defined networks. Journal of Supercomputing, 2021, 77, 13166-13190. | 3.6 | 13 |

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|----|--|-----|-----------|
| 19 | Optimization of structure and system latency in evolvable block-based neural networks using genetic algorithm. Neurocomputing, 2014, 145, 285-302. | 5.9 | 12 |
| 20 | HW/SW co-design of reconfigurable hardware-based genetic algorithm in FPGAs applicable to a variety of problems. Computing (Vienna/New York), 2013, 95, 863-896. | 4.8 | 11 |
| 21 | Incorporating known malware signatures to classify new malware variants in network traffic. International Journal of Network Management, 2015, 25, 471-489. | 2.2 | 11 |
| 22 | Online network traffic classification with incremental learning. Evolving Systems, 2016, 7, 129-143. | 3.9 | 11 |
| 23 | Evolvable Block-based Neural Networks for classification of driver drowsiness based on heart rate variability. , 2012, , . | | 10 |
| 24 | Online Incremental Learning for High Bandwidth Network Traffic Classification. Applied Computational Intelligence and Soft Computing, 2016, 2016, 1-13. | 2.3 | 10 |
| 25 | Hardware/software partitioning of embedded System-on-Chip applications. , 2015, , . | | 9 |
| 26 | 2-D DWT System Architecture for Image Compression. Journal of Signal Processing Systems, 2015, 78, 131-137. | 2.1 | 9 |
| 27 | Ping-lock round robin arbiter. Microelectronics Journal, 2017, 63, 81-93. | 2.0 | 9 |
| 28 | Online data stream classification with incremental semi-supervised learning. , 2015, , . | | 8 |
| 29 | Selection of On-line Features for Peer-to-Peer Network Traffic Classification. Advances in Intelligent Systems and Computing, 2014, , 379-390. | 0.6 | 8 |
| 30 | A spam rejection scheme during SMTP sessions based on layer-3 e-mail classification. Journal of Network and Computer Applications, 2009, 32, 236-257. | 9.1 | 7 |
| 31 | Detecting Worms Using Data Mining Techniques: Learning in the Presence of Class Noise. , 2010, , . | | 7 |
| 32 | Distributed Layer-3 E-Mail Classification for Spam Control. , 2006, , . | | 6 |
| 33 | Evolvable Block-based Neural Networks for real-time classification of heart arrhythmia From ECG signals. , 2012, , . | | 6 |
| 34 | GA-based parameter tuning in finger-vein biometric embedded systems for information security. , 2012, , | | 6 |
| 35 | Packet-level open-digest fingerprinting for spam detection on middleboxes. International Journal of Network Management, 2012, 22, 12-26. | 2.2 | 6 |
| 36 | Low latency network-on-chip router using static straight allocator. , 2016, , . | | 6 |

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|----|--|-----|-----------|
| 37 | hpFog: A FPGA-Based Fog Computing Platform. , 2017, , . | | 6 |
| 38 | DTaPO: Dynamic Thermal-Aware Performance Optimization for Dark Silicon Many-Core Systems. Electronics (Switzerland), 2020, 9, 1980. | 3.1 | 6 |
| 39 | Prioritized eâ€mail servicing to reduce nonâ€spam delay and loss: A performance analysis. International Journal of Network Management, 2008, 18, 325-344. | 2.2 | 5 |
| 40 | Embedded vision systems for ship recognition. , 2009, , . | | 5 |
| 41 | Partially adaptive look-ahead routing for low latency Network-on-Chip. , 2014, , . | | 5 |
| 42 | Temperature-Aware Task Scheduling for Dark Silicon Many-Core System-on-Chip. , 2019, , . | | 5 |
| 43 | A three-class heuristics technique: Generating training corpus for Peer-to-Peer traffic classification. , 2010, , . | | 4 |
| 44 | Analysis of features selection for P2P traffic detection using support vector machine. , 2013, , . | | 4 |
| 45 | Network partitioning and GA heuristic crossover for NoC application mapping. , 2013, , . | | 4 |
| 46 | Feasible transition path generation for EFSM-based system testing. , 2013, , . | | 4 |
| 47 | A Closed Loop Transmitting Power Self-Calibration Scheme for Energy Efficient WiNoC Architectures. , 2015, , . | | 4 |
| 48 | Automated Dataset Generation for Training Peer-to-Peer Machine Learning Classifiers. Journal of Network and Systems Management, 2015, 23, 89-110. | 4.9 | 4 |
| 49 | Improved Flow Control for Minimal Fully Adaptive Routing in 2D Mesh NoC. , 2016, , . | | 4 |
| 50 | Early Flow Table Eviction Impact on Delay and Throughput in Software-Defined Networks. , 2021, , . | | 4 |
| 51 | Retraining Mechanism for On-Line Peer-to-Peer Traffic Classification. Advances in Intelligent Systems and Computing, 2013, , 373-382. | 0.6 | 4 |
| 52 | Co-simulation methodology for improved design and verification of hardware neural networks. , 2013, , . | | 3 |
| 53 | FPGA-based quantum circuit emulation: A case study on Quantum Fourier transform. , 2014, , . | | 3 |
| 54 | Hardware transactional memory architecture with adaptive version management for multi-processor FPGA platforms. Journal of Systems Architecture, 2017, 73, 42-52. | 4.3 | 3 |

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|----|---|-----|-----------|
| 55 | PEW: Prediction-Based Early Dark Cores Wake-up Using Online Ridge Regression for Many-Core Systems. IEEE Access, 2021, 9, 124087-124099. | 4.2 | 3 |
| 56 | Multi-stage Feature Selection for On-Line Flow Peer-to-Peer Traffic Identification. Communications in Computer and Information Science, 2017, , 509-523. | 0.5 | 3 |
| 57 | Impact of Packet Inter-arrival Time Features for Online Peer-to-Peer (P2P) Classification. International Journal of Electrical and Computer Engineering, 2018, 8, 2521. | 0.7 | 3 |
| 58 | RtFog: A Real-Time FPGA-Based Fog Node With Remote Dynamically Reconfigurable Application Plane for Fog Analytics Redeployment. IEEE Transactions on Green Communications and Networking, 2022, 6, 341-351. | 5.5 | 3 |
| 59 | Parameterizable Decision Tree Classifier on NetFPGA. Advances in Intelligent Systems and Computing, 2013, , 119-128. | 0.6 | 3 |
| 60 | Performance Analysis of Server-Side Spam Control Strategies Based on Layer-3 Classification. , 2007, , . | | 2 |
| 61 | Network Worm Propagation Model Based on a Campus Network Topology. , 2011, , . | | 2 |
| 62 | Second-stage tuning procedure for analogue CMOS design reuse methodology. Electronics Letters, 2012, 48, 990-992. | 1.0 | 2 |
| 63 | A Hardware/Software Co-design Architecture of Canny Edge Detection. , 2012, , . | | 2 |
| 64 | Network Partitioning Domain Knowledge Multiobjective Application Mapping for Large-Scale Network-on-Chip. Applied Computational Intelligence and Soft Computing, 2014, 2014, 1-10. | 2.3 | 2 |
| 65 | Packet logging mechanism for adaptive online fault detection on Network-on-Chip. , 2014, , . | | 2 |
| 66 | A Closed Loop Control based Power Manager for WiNoC Architectures. , 2014, , . | | 2 |
| 67 | rrBox: A remote dynamically reconfigurable network processing middlebox. , 2015, , . | | 2 |
| 68 | Built-in Self Test Power and Test Time Analysis in On-chip Networks. Circuits, Systems, and Signal Processing, 2015, 34, 1057-1075. | 2.0 | 2 |
| 69 | Virtual Channel and Switch Allocation for Low Latency Network-on-Chip Routers. , 2015, , . | | 2 |
| 70 | Adaptive Packet Relocator in Wireless Network-on-Chip (WiNoC). Communications in Computer and Information Science, 2017, , 719-735. | 0.5 | 2 |
| 71 | Reconfigurable logic embedded architecture of support vector machine linear kernel. , 2017, , . | | 2 |
| 72 | Improved quantum circuit modelling based on Heisenberg representation. Quantum Information Processing, 2018, 17, 1. | 2.2 | 2 |

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|----|--|-----|-----------|
| 73 | A Centralized Token-based Medium Access Control Mechanism for Wireless Network-on-Chip. , 2021, , . | | 2 |
| 74 | Online Data Stream Learning and Classification with Limited Labels. Proceeding of the Electrical Engineering Computer Science and Informatics, 2014, 1, . | 0.0 | 2 |
| 75 | A Semi-Analytical Approach to Study the Energy Consumption of On-Chip Networks Testing. Journal of Low Power Electronics, 2013, 9, 189-197. | 0.6 | 2 |
| 76 | Rejecting Spam during SMTP Sessions. , 2007, , . | | 1 |
| 77 | A framework for automated malcode signatures generation. , 2010, , . | | 1 |
| 78 | CODESL: A Framework for System-Level Modelling, Co-simulation and Design-Space Exploration of Embedded Systems Based on System-on-Chip. , 2010, , . | | 1 |
| 79 | Multi-TAP architecture for IP core testing and debugging on network-on-chip. , 2011, , . | | 1 |
| 80 | s.RABILA2: An optimal VLSI routing algorithm with buffer insertion using iterative RLC model. , 2012, , . | | 1 |
| 81 | A hybrid heuristics-statistical peer-to-peer traffic classifier. , 2012, , . | | 1 |
| 82 | Hardware transactional memory on multi-processor FPGA platform. , 2014, , . | | 1 |
| 83 | rrBox: A Remote Dynamically Reconfigurable Middlebox for Network Protection. , 2014, , . | | 1 |
| 84 | Malware detection using augmented naive Bayes with domain knowledge and under presence of class noise. International Journal of Information and Computer Security, 2014, 6, 179. | 0.2 | 1 |
| 85 | rrBox: Remote dynamically reconfigurable middlebox using NetFPGA. , 2014, , . | | 1 |
| 86 | Cooperative learning for online in-network performance monitoring. , 2015, , . | | 1 |
| 87 | Adaptive Configurable Transactional Memory for Multi-processor FPGA Platforms. , 2015, , . | | 1 |
| 88 | ONLINE PEER-TO-PEER TRAFFIC IDENTIFICATION BASED ON COMPLEX EVENTS PROCESSING OF TRAFFIC EVENT SIGNATURES. Jurnal Teknologi (Sciences and Engineering), 2016, 78, . | 0.4 | 1 |
| 89 | A modular architecture for dynamically reconfigurable middlebox with customized reconfiguration handler. , 2016, , . | | 1 |
| 90 | Rapid Prototyping of NoC-based MPSoC Based on Dataflow Modeling of Real-World Applications. , 2018, , . | | 1 |

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|-----|--|-----|-----------|
| 91 | First Line Defense Against Spreading New Malware in the Network. , 2018, , . | | 1 |
| 92 | Performance Evaluation of Centralized Reconfigurable Transmitting Power Scheme in Wireless Network-on-chip. Telkomnika (Telecommunication Computing Electronics and Control), 2018, 16, 2844. | 0.8 | 1 |
| 93 | A Network-on-Chip simulation framework for homogeneous Multi-Processor System-on-Chip. , 2011, , . | | 0 |
| 94 | An FPGA hardware architecture of Nilsimsa fingerprinting algorithm. , 2011, , . | | 0 |
| 95 | Remote dynamically reconfigurable platform using NetFPGA. , 2014, , . | | 0 |
| 96 | Cooperative Learning for Distributed In-Network Traffic Classification. IOP Conference Series: Materials Science and Engineering, 2017, 190, 012010. | 0.6 | 0 |
| 97 | Incremental high throughput network traffic classifier. , 2017, , . | | 0 |
| 98 | An FPGA-based quantum circuit emulation framework using heisenberg representation. International Journal of Quantum Information, 2018, 16, 1850052. | 1.1 | 0 |
| 99 | drDRM: A PUF-Based Dynamically Reconfigurable DRM Mechanism for FPGA-Based Platform. , 2018, , . | | 0 |
| 100 | Interleaved Incremental/Decremental Support Vector Machine for Embedded System. , 2019, , . | | 0 |
| 101 | A CLOSED LOOP POWER MANAGER FOR TRANSMISSION POWER CONTROL IN WIRELESS NETWORK-ON-CHIP ARCHITECTURES. Jurnal Teknologi (Sciences and Engineering), 2015, 75, . | 0.4 | 0 |
| 102 | A Customized Reconfiguration Controller with Remote Direct ICAP Access for Dynamically Reconfigurable Platform. Telkomnika (Telecommunication Computing Electronics and Control), 2017, 15, 570. | 0.8 | 0 |
| 103 | A streaming multi-class support vector machine classification architecture for embedded systems. Indonesian Journal of Electrical Engineering and Computer Science, 2019, 16, 1286. | 0.8 | 0 |
| 104 | Impact of Early Estimation of Statistical Flow Features in On-line P2P Classification. , 2020, , . | | 0 |
| 105 | An FPGA-based Middlebox with Remote Dynamically Reconfigurable Application Plane. , 2021, , . | | 0 |