

Pr Chidi Chidambaram

List of Publications by Year in descending order

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6
papers

77
citations

1937685

4
h-index

2272923

4
g-index

6
all docs

6
docs citations

6
times ranked

116
citing authors

#	ARTICLE	IF	CITATIONS
1	Comparative Analysis of Semiconductor Device Architectures for 5-nm Node and Beyond. IEEE Electron Device Letters, 2017, 38, 1657-1660.	3.9	34
2	Characterization and Modeling of Flicker Noise in FinFETs at Advanced Technology Node. IEEE Electron Device Letters, 2019, 40, 985-988.	3.9	28
3	Chip-Scale Modeling of Electroplated Copper Surface Profiles. Journal of the Electrochemical Society, 2004, 151, C418.	2.9	7
4	Improved device variability in scaled MOSFETs with deeply retrograde channel profile. Microelectronics Reliability, 2014, 54, 1090-1095.	1.7	5
5	Investigation of electrically gate-all-around hexagonal nanowire FET (HexFET) architecture for 5 nm node logic and SRAM applications. , 2017, , .		2
6	Low power embedded memory design – process to system level considerations. , 2011, , .		1