

Shaolin Yu

List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/5896290/publications.pdf>

Version: 2024-02-01

14
papers

109
citations

1307594

7
h-index

1474206

9
g-index

14
all docs

14
docs citations

14
times ranked

99
citing authors

#	ARTICLE	IF	CITATIONS
1	A High-Efficiency Three-Level ANPC Inverter Based on Hybrid SiC and Si Devices. <i>Energies</i> , 2020, 13, 1159.	3.1	21
2	Comparative Study of 2SiC&4Si Hybrid Configuration Schemes in ANPC Inverter. <i>IEEE Access</i> , 2020, 8, 33934-33943.	4.2	18
3	Common Mode Noise Reduction of Three-Level Active Neutral Point Clamped Inverters With Uncertain Parasitic Capacitance of Photovoltaic Panels. <i>IEEE Transactions on Power Electronics</i> , 2020, 35, 6974-6988.	7.9	13
4	Complete parasitic capacitance model of photovoltaic panel considering the rain water. <i>Chinese Journal of Electrical Engineering</i> , 2017, 3, 77-84.	3.4	11
5	The Potential Impact of Using Traction Inverters With SiC MOSFETs for Electric Buses. <i>IEEE Access</i> , 2021, 9, 51561-51572.	4.2	10
6	Accurate Modeling of the Effective Parasitic Parameters for the Laminated Busbar Connected With Paralleled SiC MOSFETs. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2021, 68, 2107-2120.	5.4	10
7	Soft switching circuit of high-frequency active neutral point clamped inverter based on SiC/Si hybrid device. <i>Journal of Power Electronics</i> , 2021, 21, 71-84.	1.5	8
8	Co-Reduction of Common Mode Noise and Loop Current of Three-Level Active Neutral Point Clamped Inverters. <i>IEEE Journal of Emerging and Selected Topics in Power Electronics</i> , 2021, 9, 1088-1103.	5.4	8
9	Effect of key physical structures on the laminated bus bar inductance. , 2016, , .		5
10	An Extraction Method for the Parasitic Capacitance of the Photovoltaic Module Based on the Oscillation of the Leakage Current. , 2019, , .		2
11	Effect of water on parasitic capacitance of photovoltaic panel. , 2017, , .		1
12	An Optimized Temperature Sensor Calorimetric Power Device Loss Measurement Method. <i>Energies</i> , 2019, 12, 1333.	3.1	1
13	The Method for Decoupling the Parasitic Inductance of the Laminated Busbar with SiC MOSFETs in Parallel. , 2021, , .		1
14	A Practical Method to Eliminate the Common Mode Current of 3-level Active Neutral Point Clamped Inverters with Uncertain Parasitic Capacitance of Photovoltaic Panels. , 2019, , .		0