

Dharmendra Kumar

List of Publications by Year in descending order

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Version: 2024-02-01

3
papers

94
citations

2258059

3
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2550090

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g-index

3
all docs

3
docs citations

3
times ranked

73
citing authors

#	ARTICLE	IF	CITATIONS
1	A systematic approach towards fault-tolerant design of QCA circuits. Analog Integrated Circuits and Signal Processing, 2019, 98, 501-515.	1.4	4
2	On fault-tolerant design of Exclusive-OR gates in QCA. Journal of Computational Electronics, 2017, 16, 896-906.	2.5	14
3	Design of a practical fault-tolerant adder in QCA. Microelectronics Journal, 2016, 53, 90-104.	2.0	76