

# Dharmendra Kumar

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/5873430/publications.pdf>

Version: 2024-02-01

3  
papers

94  
citations

2258059

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2550090

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3  
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3  
docs citations

3  
times ranked

73  
citing authors

#	ARTICLE	IF	CITATIONS
1	Design of a practical fault-tolerant adder in QCA. <i>Microelectronics Journal</i> , 2016, 53, 90-104.	2.0	76
2	On fault-tolerant design of Exclusive-OR gates in QCA. <i>Journal of Computational Electronics</i> , 2017, 16, 896-906.	2.5	14
3	A systematic approach towards fault-tolerant design of QCA circuits. <i>Analog Integrated Circuits and Signal Processing</i> , 2019, 98, 501-515.	1.4	4