

# Ananda Mohan Pemmaraju

## List of Publications by Year in descending order

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Version: 2024-02-01

35  
papers

346  
citations

1307594

7  
h-index

1058476

14  
g-index

35  
all docs

35  
docs citations

35  
times ranked

198  
citing authors

#	ARTICLE	IF	CITATIONS
1	From the Desk of the Editor-in-Chief. IETE Journal of Education Online, 2022, 63, 3-4.	0.6	0
2	From the Desk of the Editor-in-Chief. IETE Journal of Education Online, 2021, 62, 4-5.	0.6	0
3	From the Desk of the Editor-in-Chief. IETE Journal of Education Online, 2021, 62, 58-59.	0.6	0
4	On Actively Compensated Amplifiers Using Negative Impedance Converters. IEEE Transactions on Circuits and Systems II: Express Briefs, 2020, 67, 640-644.	3.0	3
5	An improved RNS-to-binary converter for 7-modulus set $\{2n-5, 2n-3, 2n-2+1, 2n-1, 2n-1+1, 2n, 2n+1\}$ for $n$ even. Sadhana - Academy Proceedings in Engineering Sciences, 2020, 45, 1.	1.3	1
6	From Editor-in-Chief's Desk. IETE Journal of Education Online, 2020, 61, 53-54.	0.6	0
7	Analysis of negative $\mathbb{R}$ assisted integrators and differentiators. Electronics Letters, 2020, 56, 123-125.	1.0	2
8	From Editor-in-Chief's Desk. IETE Journal of Education Online, 2019, 60, 2-2.	0.6	0
9	Novel RNS-to-binary converters for the three-moduli set $\{2m-1, 2m, 2m+1\}$ . Sadhana - Academy Proceedings in Engineering Sciences, 2019, 44, 1.	1.3	1
10	Residue-to-Binary converters for the seven moduli set $\{2n-5-1, 2n-3-1, 2n-2+1, 2n-1-1, 2n-1+1, 2n, 2n+1\}$ for $n$ even. , 2019, , .		2
11	From Editor-in-Chief's Desk. IETE Journal of Education Online, 2019, 60, 49-50.	0.6	0
12	RNS-to-Binary Converters for New Three-Moduli Sets $\{2k^3, 2k^2, 2k+1\}$ and $\{2k+1, 2k+2, 2k+3\}$ . Journal of Circuits, Systems and Computers, 2018, 27, 1850224.	1.5	5
13	Reverse Converters for the Moduli Set $\{2^n, 2^{n-1}-1, 2^{n+1}-1\}$ ( $n$ , $\text{hbox {Even}}$ ). Circuits, Systems, and Signal Processing, 2018, 37, 3605-3634.	2.0	1
14	From Editor-in-Chief's Desk. IETE Journal of Education Online, 2018, 59, 52-52.	0.6	0
15	From Editor-in-Chief's Desk. IETE Journal of Education Online, 2018, 59, 1-1.	0.6	0
16	Reverse Conversion Using Core Function, CRT and Mixed Radix Conversion. Circuits, Systems, and Signal Processing, 2017, 36, 2847-2874.	2.0	7
17	Residue Number Systems. , 2016, , .		90
18	RNS to Binary Conversion Using Diagonal Function and Pirlo and Impedovo Monotonic Function. Circuits, Systems, and Signal Processing, 2016, 35, 1063-1076.	2.0	15

#	ARTICLE	IF	CITATIONS
19	Bandwidth Enhancement of Finite Gain Amplifiers Using CFOA. IETE Journal of Education Online, 2014, 55, 54-63.	0.6	1
20	Implementation of AES Key Schedule Using Look-Ahead Technique. Circuits, Systems, and Signal Processing, 2014, 33, 3663-3670.	2.0	0
21	Comments on "Noise Performance of a Regulated Cascode Transimpedance Amplifier for Radiation Detectors" IEEE Transactions on Circuits and Systems I: Regular Papers, 2013, 60, 2235-2236.	5.4	1
22	A review of some recent stream ciphers. , 2013, , .		11
23	Efficient Implementations for AES Encryption and Decryption. Circuits, Systems, and Signal Processing, 2012, 31, 1765-1785.	2.0	19
24	On the Key Schedule of Mini-AES and S-AES Algorithms. IETE Journal of Education Online, 2011, 52, 49-57.	0.6	1
25	Comments on "Avoiding the Gain-Bandwidth Trade Off in Feedback Amplifiers" IEEE Transactions on Circuits and Systems I: Regular Papers, 2011, 58, 2114-2116.	5.4	9
26	Novel First-Order and Second-Order Current-Mode Filters Using Multiple-Output Operational Transconductance Amplifiers. Circuits, Systems, and Signal Processing, 2010, 29, 553-576.	2.0	39
27	Novel First-order and Second-Order Current Mode filters using dual-output OTAs and Grounded capacitors. , 2008, , .		15
28	Implementation of AES S-Boxes using combinational logic. , 2008, , .		12
29	New residue to binary converters for the moduli set $\{2^k, 2^k, 2^{2k-1}, 2^{k-1}\}$ , , 2008, , .		5
30	Comments on A 4th-Order Active-RC Reconfigurable (UMTS/WLAN) Filter. IEEE Journal of Solid-State Circuits, 2007, 42, 457-458.	5.4	2
31	RNS-to-Binary Converters for Two Four-Moduli Sets $\{2^{n-1}, 2^n, 2^{n+1}, 2^{n+1}-1\}$ and $\{2^{n-1}, 2^n, 2^{n+1}, 2^{n+1}+1\}$ . IEEE Transactions on Circuits and Systems Part I: Regular Papers, 2007, 54, 1245-1254.	0.1	80
32	Reverse Converters for a New Moduli Set $\{2n-1, 2n, 2n+1\}$ . Circuits, Systems, and Signal Processing, 2007, 26, 215-227.	2.0	6
33	FLOATING CAPACITANCE SIMULATION USING CURRENT CONVEYORS. Journal of Circuits, Systems and Computers, 2005, 14, 123-128.	1.5	17
34	A Framework for Teaching Analog Filter Design. IETE Technical Review (Institution of Electronics and) Tj ETQq0 0 0 ggBT /Overlock 10 Tf 3,2 1		
35	On the Effect of Operational Amplifier Gain-bandwidth Product on the Performance of Basic Building Blocks. IETE Journal of Education Online, 0, , 1-12.	0.6	0