

Daniele De Sensi

List of Publications by Year in descending order

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Version: 2024-02-01

26
papers

270
citations

1307594

7
h-index

1125743

13
g-index

27
all docs

27
docs citations

27
times ranked

197
citing authors

#	ARTICLE	IF	CITATIONS
1	Power Logâ€™nâ€™Roll: Power-Efficient Localized Rollback for MPI Applications Using Message Logging Protocols. IEEE Transactions on Parallel and Distributed Systems, 2022, 33, 1276-1288.	5.6	2
2	Simplifying and implementing service level objectives for stream parallelism. Journal of Supercomputing, 2020, 76, 4603-4628.	3.6	7
3	Truly Scalable K-Truss and Max-Truss Algorithms for Community Detection in Graphs. IEEE Access, 2020, 8, 139096-139109.	4.2	7
4	Improving the Performance of Actors on Multi-cores with Parallel Patterns. International Journal of Parallel Programming, 2020, 48, 692-712.	1.5	3
5	Performance and Energy Trade-Offs for Parallel Applications on Heterogeneous Multi-Processing Systems. Energies, 2020, 13, 2409.	3.1	8
6	Transparent Autonomy for OpenMP Applications. Lecture Notes in Computer Science, 2020, , 54-64.	1.3	0
7	Application-Aware Power Capping Using Nornir. Lecture Notes in Computer Science, 2020, , 191-202.	1.3	1
8	Powerâ€™aware pipelining with automatic concurrency control. Concurrency Computation Practice and Experience, 2019, 31, e4652.	2.2	7
9	Autonomic and Latency-Aware Degree of Parallelism Management in SPar. Lecture Notes in Computer Science, 2019, , 28-39.	1.3	7
10	Service Level Objectives via C++11 Attributes. Lecture Notes in Computer Science, 2019, , 745-756.	1.3	1
11	GASSER: An Auto-Tunable System for General Sliding-Window Streaming Operators on GPUs. IEEE Access, 2019, 7, 48753-48769.	4.2	11
12	The RePhrase Extended Pattern Set for Data Intensive Parallel Computing. International Journal of Parallel Programming, 2019, 47, 74-93.	1.5	2
13	Mitigating network noise on Dragonfly networks through application-aware routing. , 2019, , .		21
14	Analysing Multiple QoS Attributes in Parallel Design Patterns-Based Applications. International Journal of Parallel Programming, 2018, 46, 81-100.	1.5	3
15	Reducing Message Latency and CPU Utilization in the CAF Actor Framework. , 2018, , .		2
16	Discovering <tex>\$k</tex>-Trusses in Large-Scale Networks. , 2018, , .		3
17	D2K. , 2018, , .		37
18	Simplifying self-adaptive and power-aware computing with Nornir. Future Generation Computer Systems, 2018, 87, 136-151.	7.5	18

#	ARTICLE	IF	CITATIONS
19	Evaluating Concurrency Throttling and Thread Packing on SMT Multicores. , 2017, , .		6
20	A Power-Aware, Self-Adaptive Macro Data Flow Framework. Parallel Processing Letters, 2017, 27, 1740004.	0.6	9
21	Bringing Parallel Patterns Out of the Corner. Transactions on Architecture and Code Optimization, 2017, 14, 1-26.	2.0	18
22	Mammut: High-level management of system knobs and sensors. SoftwareX, 2017, 6, 150-154.	2.6	17
23	P 3 ARSEC. , 2017, , .		8
24	A Reconfiguration Algorithm for Power-Aware Parallel Applications. Transactions on Architecture and Code Optimization, 2016, 13, 1-25.	2.0	41
25	Predicting Performance and Power Consumption of Parallel Applications. , 2016, , .		21
26	Energy Driven Adaptivity in Stream Parallel Computations. , 2015, , .		9