

# Hani Saleh

## List of Publications by Year in Descending Order

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The third column is the impact factor (IF) of the journal, and the fourth column is the number of citations of the article.

94 papers	686 citations	13 h-index	23 g-index
112 ext. papers	951 ext. citations	2.3 avg, IF	4.35 L-index

#	Paper	IF	Citations
94	Deep Neural Networks Based Weight Approximation and Computation Reuse for 2-D Image Classification. <i>IEEE Access</i> , <b>2022</b> , 1-1	3.5	
93	C3PU: Cross-Coupling Capacitor Processing Unit Using Analog-Mixed Signal for AI Inference. <i>IEEE Access</i> , <b>2021</b> , 9, 167353-167363	3.5	1
92	RRAM-based CAM combined with time-domain circuits for hyperdimensional computing. <i>Scientific Reports</i> , <b>2021</b> , 11, 19848	4.9	2
91	GNNUnlock+: A Systematic Methodology for Designing Graph Neural Networks-based Oracle-less Unlocking Schemes for Provably Secure Logic Locking. <i>IEEE Transactions on Emerging Topics in Computing</i> , <b>2021</b> , 1-1	4.1	2
90	Hyper-Dimensional Computing Challenges and Opportunities for AI Applications. <i>IEEE Access</i> , <b>2021</b> , 1-1	3.5	3
89	DS2B: Dynamic and Secure Substitution Box for Efficient Speech Encryption Engine. <i>IEEE Access</i> , <b>2021</b> , 9, 93902-93915	3.5	
88	GNN-RE: Graph Neural Networks for Reverse Engineering of Gate-Level Netlists. <i>IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems</i> , <b>2021</b> , 1-1	2.5	2
87	. <i>IEEE Transactions on Information Forensics and Security</i> , <b>2021</b> , 16, 2508-2523	8	8
86	Design Exploration of ReRAM-Based Crossbar for AI Inference. <i>IEEE Access</i> , <b>2021</b> , 9, 70430-70442	3.5	0
85	Human Vital Signs Detection Methods and Potential Using Radars: A Review. <i>Sensors</i> , <b>2020</b> , 20,	3.8	30
84	Computational Power Evaluation for Energy-Constrained Wireless Communications Systems. <i>IEEE Open Journal of the Communications Society</i> , <b>2020</b> , 1, 308-319	6.7	9
83	RRAM Crossbar-Based In-Memory Computation of Anisotropic Filters for Image Preprocessing. <i>IEEE Access</i> , <b>2020</b> , 8, 127569-127580	3.5	3
82	FPGA-Based Memristor Emulator Circuit for Binary Convolutional Neural Networks. <i>IEEE Access</i> , <b>2020</b> , 8, 117736-117745	3.5	0
81	Enhanced FPGA realization of the fractional-order derivative and application to a variable-order chaotic system. <i>Nonlinear Dynamics</i> , <b>2020</b> , 99, 3143-3154	5	9
80	TEG-Based Power Management Designs and Characterizations. <i>Analog Circuits and Signal Processing Series</i> , <b>2020</b> , 31-46	0.2	
79	Introduction to TEG-Based Power Management Unit. <i>Analog Circuits and Signal Processing Series</i> , <b>2020</b> , 15-29	0.2	0
78	A 28-GHz Cascode Inverse Class-D Power Amplifier Utilizing Pulse Injection in 22-nm FDSOI. <i>IEEE Access</i> , <b>2020</b> , 8, 97353-97360	3.5	1

77	Dual-Outputs Switched Capacitor Voltage Regulator. <i>Analog Circuits and Signal Processing Series</i> , <b>2020</b> , 47-71	0.2	
76	Introduction to Power Management. <i>Analog Circuits and Signal Processing Series</i> , <b>2020</b> , 1-13	0.2	
75	ASIC Implementation of a Pre-Trained Neural Network for ECG Feature Extraction <b>2020</b> ,		1
74	Functional Reverse Engineering on SAT-Attack Resilient Logic Locking <b>2019</b> ,		9
73	A novel algorithm for the prediction and detection of ventricular arrhythmia. <i>Analog Integrated Circuits and Signal Processing</i> , <b>2019</b> , 99, 413-426	1.2	2
72	Cascaded power management unit characterization for TEG-based IoT devices in 65 nm CMOS. <i>Microelectronics Journal</i> , <b>2019</b> , 90, 285-296	1.8	4
71	Embedded memory options for ultra-low power IoT devices. <i>Microelectronics Journal</i> , <b>2019</b> , 93, 104634	1.8	6
70	Ultra-Low Power CAN Detection and VA Prediction. <i>Analog Circuits and Signal Processing Series</i> , <b>2019</b> , 59-83	0.2	
69	Self-Powered SoC Platform for Wearable Health Care <b>2019</b> , 307-325		
68	Digital Emulation of a Versatile Memristor With Speech Encryption Application. <i>IEEE Access</i> , <b>2019</b> , 7, 174280-174297	3.5	4
67	Reconfigurable, Switched-Capacitor Power Converter for IoT <b>2019</b> , 277-290		
66	IoT for Healthcare. <i>Analog Circuits and Signal Processing Series</i> , <b>2019</b> , 7-12	0.2	4
65	ACLT-Based QRS Detection and ECG Compression Architecture. <i>Analog Circuits and Signal Processing Series</i> , <b>2019</b> , 39-57	0.2	
64	MSER-in-Chip: An Efficient Vision Tool for IoT Devices <b>2019</b> , 245-259		
63	Ultra-Low-Power ECG Processor for IoT SOC's <b>2019</b> , 141-152		
62	Introduction to Ultra-Low Power ECG Processor. <i>Analog Circuits and Signal Processing Series</i> , <b>2019</b> , 1-6	0.2	
61	Combined CLT and DWT-Based ECG Feature Extractor. <i>Analog Circuits and Signal Processing Series</i> , <b>2019</b> , 27-38	0.2	
60	Ultra-Low Power QRS Detection and ECG Compression Architecture for IoT Healthcare Devices. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , <b>2019</b> , 66, 669-679	3.9	43

59	A Nano-Watt ECG Feature Extraction Engine in 65-nm Technology. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , <b>2018</b> , 65, 1099-1103	3.5	9
58	A Nanowatt Real-Time Cardiac Autonomic Neuropathy Detector. <i>IEEE Transactions on Biomedical Circuits and Systems</i> , <b>2018</b> , 12, 739-750	5.1	10
57	A Charge Pump Based Power Management Unit With 66%-Efficiency in 65 nm CMOS <b>2018</b> ,		7
56	. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , <b>2018</b> , 65, 4007-4016	3.9	11
55	Energy Combiner and Power Manager for Multi-Source Energy Harvesting. <i>Analog Circuits and Signal Processing Series</i> , <b>2018</b> , 81-89	0.2	
54	Energy Harvesting Sources, Models, and Circuits. <i>Analog Circuits and Signal Processing Series</i> , <b>2018</b> , 7-35	0.2	1
53	Polarity Mechanism for Thermoelectric Harvester. <i>Analog Circuits and Signal Processing Series</i> , <b>2018</b> , 61-79	0.2	
52	Zero Crossing Switching Control for L-Based DCDC Converters. <i>Analog Circuits and Signal Processing Series</i> , <b>2018</b> , 47-60	0.2	
51	System Design and Development. <i>Analog Circuits and Signal Processing Series</i> , <b>2018</b> , 23-38	0.2	
50	An Efficient and Small Area Multioutput Switched Capacitor Buck Converter for IoTs <b>2018</b> ,		2
49	An Efficient Zero Current Switching Control for L-Based DCDC Converters in TEG Applications. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , <b>2017</b> , 64, 294-298	3.5	19
48	. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , <b>2017</b> , 64, 705-716	3.9	18
47	. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , <b>2017</b> , 64, 2624-2637	3.9	40
46	Assessment of seven reconstruction methods for contemporary compressive sensing <b>2017</b> ,		2
45	Automatic detection of coronary artery disease (CAD) in an ECG signal <b>2017</b> ,		1
44	Characterization of RF energy harvesting at 2.4 GHz <b>2017</b> ,		1
43	Low-Power ECG-Based Processor for Predicting Ventricular Arrhythmia. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , <b>2016</b> , 24, 1962-1974	2.6	68
42	Modeling and Optimization of Memristor and STT-RAM-Based Memory for Low-Power Applications. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , <b>2016</b> , 24, 1003-1014	2.6	30

41	An Efficient Switched-Capacitor DC-DC Buck Converter for Self-Powered Wearable Electronics. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , <b>2016</b> , 63, 1557-1566	3.9	38
40	A biomedical SoC architecture for predicting ventricular arrhythmia <b>2016</b> ,		1
39	An ACDC converter for human body-based vibration energy harvesting. <i>Microelectronics Journal</i> , <b>2016</b> , 55, 1-7	1.8	7
38	Algorithmic foundations for hardware implementation of scale-insensitive MSER features <b>2016</b> ,		3
37	A 65nm ASIC based 256 NIST prime field ECC processor <b>2016</b> ,		2
36	Novel logarithmic ECG feature extraction algorithm based on pan and tomkins <b>2016</b> ,		3
35	Initial optimization of graphene coated fabrics for ECG sensors <b>2016</b> ,		2
34	An 83% efficiency, 0.6V to 1V output switched-capacitor DC-DC converter for micro-watt power applications <b>2016</b> ,		1
33	Power management unit for multi-source energy harvesting in wearable electronics <b>2016</b> ,		15
32	An efficient thermal energy harvesting and power management for $\mu$ Watt wearable BioChips <b>2016</b> ,		3
31	Design Methodologies for Yield Enhancement and Power Efficiency in SRAM-Based SoCs. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , <b>2015</b> , 23, 2054-2064	2.6	5
30	Memory impact on the lifetime of a Wireless Sensor Node using a Semi-Markov model <b>2015</b> ,		4
29	Novel Unified Analysis of Orthogonal Space-Time Block Codes over Generalized-K and AWGGN MIMO Networks <b>2015</b> ,		3
28	A 65-nm low power ECG feature extraction system <b>2015</b> ,		3
27	Adaptive ECG interval extraction <b>2015</b> ,		6
26	An all-digital, CMOS zero current switching circuit for thermal energy harvesting <b>2015</b> ,		4
25	A hardware accelerator for real-time extraction of the linear-time MSER algorithm <b>2015</b> ,		4
24	Evolutionary QR-Based Traffic Sign Recognition System for Next-Generation Intelligent Vehicles <b>2015</b> ,		2

23	A maximally stable extremal regions system-on-chip for real-time visual surveillance <b>2015</b> ,		13
22	Novel fast and scalable parallel union-find ASIC implementation for real-time digital image segmentation <b>2015</b> ,		6
21	. <i>IEEE Journal on Emerging and Selected Topics in Circuits and Systems</i> , <b>2014</b> , 4, 354-363	5.2	92
20	65-nm ASIC implementation of QRS detector based on Pan and Tompkins algorithm <b>2014</b> ,		4
19	Adaptive technique for P and T wave delineation in electrocardiogram signals. <i>Annual International Conference of the IEEE Engineering in Medicine and Biology Society IEEE Engineering in Medicine and Biology Society Annual International Conference</i> , <b>2014</b> , 2014, 90-3	0.9	13
18	A zero-crossing digital phase-locked loop architecture with hyperbolic nonlinearity for high Doppler environments <b>2014</b> ,		2
17	Novel Average Bit Error Rate Analysis of generalized fading channels subject to Additive White Generalized Gaussian Noise <b>2014</b> ,		11
16	Switched capacitor DC-DC converter for ultra-low power applications <b>2014</b> ,		5
15	LDO regulator versus switched inductor DC-DC converter <b>2014</b> ,		4
14	<b>2013</b> ,		7
13	The revolution of glucose monitoring methods and systems: A survey <b>2013</b> ,		1
12	Memristor for energy efficient wireless sensor node <b>2013</b> ,		1
11	Embedded memory design using memristor: Retention time versus write energy <b>2013</b> ,		2
10	A survey of thermal energy harvesting techniques and interface circuitry <b>2013</b> ,		11
9	Energy efficient and high bandwidth embedded memory implementation <b>2013</b> ,		1
8	Efficient power management in wireless sensor networks <b>2013</b> ,		1
7	Modeling of STT-MTJ for low power embedded memory applications: A comparative review <b>2013</b> ,		1
6	Automated real-time video surveillance algorithms for SoC implementation: A survey <b>2013</b> ,		7

5	Practical automatic Arabic license plate recognition system <b>2011,</b>	1
4	A floating-point fused add-subtract unit <b>2008,</b>	21
3	A family of scalable FFT architectures and an implementation of 1024-point radix-2 FFT for real-time communications <b>2008,</b>	6
2	A contention-free Radix-2 8k-point fast Fourier transform engine using single port SRAMs <b>2008,</b>	2
1	Contention-free switch-based implementation of 1024-point Radix-2 Fourier Transform Engine <b>2007,</b>	1