Hani Saleh

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94 686 13 23 g-index

112 951 2.3 4.35 ext. papers ext. citations avg, IF L-index

#	Paper	IF	Citations
94	. IEEE Journal on Emerging and Selected Topics in Circuits and Systems, 2014 , 4, 354-363	5.2	92
93	Low-Power ECG-Based Processor for Predicting Ventricular Arrhythmia. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2016 , 24, 1962-1974	2.6	68
92	Ultra-Low Power QRS Detection and ECG Compression Architecture for IoT Healthcare Devices. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2019 , 66, 669-679	3.9	43
91	. IEEE Transactions on Circuits and Systems I: Regular Papers, 2017 , 64, 2624-2637	3.9	40
90	An Efficient Switched-Capacitor DC-DC Buck Converter for Self-Powered Wearable Electronics. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2016 , 63, 1557-1566	3.9	38
89	Modeling and Optimization of Memristor and STT-RAM-Based Memory for Low-Power Applications. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2016 , 24, 1003-1014	2.6	30
88	Human Vital Signs Detection Methods and Potential Using Radars: A Review. Sensors, 2020 , 20,	3.8	30
87	A floating-point fused add-subtract unit 2008 ,		21
86	An Efficient Zero Current Switching Control for L-Based DCDC Converters in TEG Applications. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2017 , 64, 294-298	3.5	19
85	. IEEE Transactions on Circuits and Systems I: Regular Papers, 2017 , 64, 705-716	3.9	18
84	Power management unit for multi-source energy harvesting in wearable electronics 2016,		15
83	A maximally stable extremal regions system-on-chip for real-time visual surveillance 2015,		13
82	Adaptive technique for P and T wave delineation in electrocardiogram signals. <i>Annual International Conference of the IEEE Engineering in Medicine and Biology Society IEEE Engineering in Medicine and Biology Society Annual International Conference</i> , 2014 , 2014, 90-3	0.9	13
81	. IEEE Transactions on Circuits and Systems I: Regular Papers, 2018 , 65, 4007-4016	3.9	11
80	A survey of thermal energy harvesting techniques and interface circuitry 2013,		11
79	Novel Average Bit Error Rate Analysis of generalized fading channels subject to Additive White Generalized Gaussian Noise 2014 ,		11
78	A Nanowatt Real-Time Cardiac Autonomic Neuropathy Detector. <i>IEEE Transactions on Biomedical Circuits and Systems</i> , 2018 , 12, 739-750	5.1	10

77	Functional Reverse Engineering on SAT-Attack Resilient Logic Locking 2019,		9
76	Computational Power Evaluation for Energy-Constrained Wireless Communications Systems. <i>IEEE Open Journal of the Communications Society</i> , 2020 , 1, 308-319	6.7	9
75	Enhanced FPGA realization of the fractional-order derivative and application to a variable-order chaotic system. <i>Nonlinear Dynamics</i> , 2020 , 99, 3143-3154	5	9
74	A Nano-Watt ECG Feature Extraction Engine in 65-nm Technology. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2018 , 65, 1099-1103	3.5	9
73	. IEEE Transactions on Information Forensics and Security, 2021 , 16, 2508-2523	8	8
72	A Charge Pump Based Power Management Unit With 66%-Efficiency in 65 nm CMOS 2018 ,		7
71	2013,		7
70	Automated real-time video surveillance algorithms for SoC implementation: A survey 2013,		7
69	An ACDC converter for human body-based vibration energy harvesting. <i>Microelectronics Journal</i> , 2016 , 55, 1-7	1.8	7
68	Adaptive ECG interval extraction 2015 ,		6
67	Embedded memory options for ultra-low power IoT devices. <i>Microelectronics Journal</i> , 2019 , 93, 104634	1.8	6
66	Novel fast and scalable parallel union-find ASIC implementation for real-time digital image segmentation 2015 ,		6
65	A family of scalable FFT architectures and an implementation of 1024-point radix-2 FFT for real-time communications 2008 ,		6
64	Design Methodologies for Yield Enhancement and Power Efficiency in SRAM-Based SoCs. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2015 , 23, 2054-2064	2.6	5
63	Switched capacitor DC-DC converter for ultra-low power applications 2014,		5
62	Memory impact on the lifetime of a Wireless Sensor Node using a Semi-Markov model 2015,		4
61	An all-digital, CMOS zero current switching circuit for thermal energy harvesting 2015,		4
60	Cascaded power management unit characterization for TEG-based IoT devices in 65 nm CMOS. <i>Microelectronics Journal</i> , 2019 , 90, 285-296	1.8	4

59	A hardware accelerator for real-time extraction of the linear-time MSER algorithm 2015,		4
58	65-nm ASIC implementation of QRS detector based on Pan and Tompkins algorithm 2014 ,		4
57	LDO regulator versus switched inductor DC-DC converter 2014 ,		4
56	Digital Emulation of a Versatile Memristor With Speech Encryption Application. <i>IEEE Access</i> , 2019 , 7, 174280-174297	3.5	4
55	IoT for Healthcare. Analog Circuits and Signal Processing Series, 2019, 7-12	0.2	4
54	Novel Unified Analysis of Orthogonal Space-Time Block Codes over Generalized-K and AWGGN MIMO Networks 2015 ,		3
53	A 65-nm low power ECG feature extraction system 2015 ,		3
52	RRAM Crossbar-Based In-Memory Computation of Anisotropic Filters for Image Preprocessingloa. <i>IEEE Access</i> , 2020 , 8, 127569-127580	3.5	3
51	Algorithmic foundations for hardware implementation of scale-insensitive MSER features 2016,		3
50	Novel logarithmic ECG feature extraction algorithm based on pan and tompkins 2016,		3
49	An efficient thermal energy harvesting and power management for Watt wearable BioChips 2016,		3
48	Hyper-Dimensional Computing Challenges and Opportunities for AI Applications. <i>IEEE Access</i> , 2021 , 1-1	3.5	3
47	A novel algorithm for the prediction and detection of ventricular arrhythmia. <i>Analog Integrated Circuits and Signal Processing</i> , 2019 , 99, 413-426	1.2	2
46	Assessment of seven reconstruction methods for contemporary compressive sensing 2017,		2
45	Embedded memory design using memristor: Retention time versus write energy 2013,		2
44	Evolutionary QR-Based Traffic Sign Recognition System for Next-Generation Intelligent Vehicles 2015 ,		2
43	A zero-crossing digital phase-locked loop architecture with hyperbolic nonlinearity for high Doppler environments 2014 ,		2
42	A contention-free Radix-2 8k-point fast Fourier transform engine using single port SRAMs 2008 ,		2

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41	RRAM-based CAM combined with time-domain circuits for hyperdimensional computing. <i>Scientific Reports</i> , 2021 , 11, 19848	4.9	2
40	A 65nm ASIC based 256 NIST prime field ECC processor 2016 ,		2
39	Initial optimization of graphene coated fabrics for ECG sensors 2016,		2
38	GNNUnlock+: A Systematic Methodology for Designing Graph Neural Networks-based Oracle-less Unlocking Schemes for Provably Secure Logic Locking. <i>IEEE Transactions on Emerging Topics in Computing</i> , 2021 , 1-1	4.1	2
37	An Efficient and Small Area Multioutput Switched Capacitor Buck Converter for IoTs 2018,		2
36	GNN-RE: Graph Neural Networks for Reverse Engineering of Gate-Level Netlists. <i>IEEE Transactions</i> on Computer-Aided Design of Integrated Circuits and Systems, 2021 , 1-1	2.5	2
35	A biomedical SoC architecture for predicting ventricular arrhythmia 2016 ,		1
34	The revolution of glucose monitoring methods and systems: A survey 2013,		1
33	Memristor for energy efficient wireless sensor node 2013,		1
32	Automatic detection of coronary artery disease (CAD) in an ECG signal 2017,		1
31	Characterization of RF energy harvesting at 2.4 GHz 2017 ,		1
30	Energy efficient and hign bandwidth embedded memory implementation 2013,		1
29	Efficient power management in wireless sensor networks 2013,		1
28	Modeling of STT-MTJ for low power embedded memory applications: A comparative review 2013,		1
27	Practical automatic Arabic license plate recognition system 2011,		1
26	Contention-free switch-based implementation of 1024-point Radix-2 Fourier Transform Engine 2007 ,		1
25	C3PU: Cross-Coupling Capacitor Processing Unit Using Analog-Mixed Signal for AI Inference. <i>IEEE Access</i> , 2021 , 9, 167353-167363	3.5	1
24	A 28-GHz Cascode Inverse Class-D Power Amplifier Utilizing Pulse Injection in 22-nm FDSOI. <i>IEEE Access</i> , 2020 , 8, 97353-97360	3.5	1

23	Energy Harvesting Sources, Models, and Circuits. Analog Circuits and Signal Processing Series, 2018, 7-35	0.2	1
22	ASIC Implementation of a Pre-Trained Neural Network for ECG Feature Extraction 2020,		1
21	An 83% efficiency, 0.6V to 1V output switched-capacitor DC-DC converter for micro-watt power applications 2016 ,		1
20	FPGA-Based Memristor Emulator Circuit for Binary Convolutional Neural Networks. <i>IEEE Access</i> , 2020 , 8, 117736-117745	3.5	O
19	Introduction to TEG-Based Power Management Unit. <i>Analog Circuits and Signal Processing Series</i> , 2020 , 15-29	0.2	O
18	Design Exploration of ReRAM-Based Crossbar for Al Inference. <i>IEEE Access</i> , 2021 , 9, 70430-70442	3.5	O
17	TEG-Based Power Management Designs and Characterizations. <i>Analog Circuits and Signal Processing Series</i> , 2020 , 31-46	0.2	
16	Ultra-Low Power CAN Detection and VA Prediction. <i>Analog Circuits and Signal Processing Series</i> , 2019 , 59-83	0.2	
15	Self-Powered SoC Platform for Wearable Health Care 2019 , 307-325		
14	Dual-Outputs Switched Capacitor Voltage Regulator. <i>Analog Circuits and Signal Processing Series</i> , 2020 , 47-71	0.2	
13	Introduction to Power Management. Analog Circuits and Signal Processing Series, 2020, 1-13	0.2	
12	Energy Combiner and Power Manager for Multi-Source Energy Harvesting. <i>Analog Circuits and Signal Processing Series</i> , 2018 , 81-89	0.2	
11	Polarity Mechanism for Thermoelectric Harvester. <i>Analog Circuits and Signal Processing Series</i> , 2018 , 61	-7 9 .2	
10	Zero Crossing Switching Control for L-Based DCDC Converters. <i>Analog Circuits and Signal Processing Series</i> , 2018 , 47-60	0.2	
9	System Design and Development. Analog Circuits and Signal Processing Series, 2018, 23-38	0.2	
8	Reconfigurable, Switched-Capacitor Power Converter for IoT 2019 , 277-290		
7	ACLT-Based QRS Detection and ECG Compression Architecture. <i>Analog Circuits and Signal Processing Series</i> , 2019 , 39-57	0.2	
6	MSER-in-Chip: An Efficient Vision Tool for IoT Devices 2019 , 245-259		

LIST OF PUBLICATIONS

5 Ultra-Low-Power ECG Processor for IoT SOCs **2019**, 141-152

4	Introduction to Ultra-Low Power ECG Processor. <i>Analog Circuits and Signal Processing Series</i> , 2019 , 1-6	0.2
3	Combined CLT and DWT-Based ECG Feature Extractor. <i>Analog Circuits and Signal Processing Series</i> , 2019 , 27-38	0.2
2	DS2B: Dynamic and Secure Substitution Box for Efficient Speech Encryption Engine. <i>IEEE Access</i> , 2021 , 9, 93902-93915	3.5
1	Deep Neural Networks Based Weight Approximation and Computation Reuse for 2-D Image Classification. <i>IEEE Access</i> , 2022 , 1-1	3.5