Alfonso Carlosena

List of Publications by Year in Descending Order

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The third column is the impact factor (IF) of the journal, and the fourth column is the number of citations of the article.

93	1,231 citations	15	31
papers		h-index	g-index
105 ext. papers	1,466 ext. citations	2.3 avg, IF	4 L-index

#	Paper	IF	Citations
93	AC coupled amplifier with a resistance multiplier technique for ultra-low frequency operation. <i>AEU - International Journal of Electronics and Communications</i> , 2022 , 149, 154176	2.8	1
92	A family of AC amplifiers for ultra-low frequency operation. <i>International Journal of Circuit Theory and Applications</i> , 2021 , 49, 3317	2	1
91	AC amplifiers with ultra-low corner frequency by using bootstrapping. <i>Electronics Letters</i> , 2021 , 57, 203	B- 2 10 ₁ 5	2
90	Performance Comparison and Design Guidelines for Type II and Type III PLLs. <i>Circuits, Systems, and Signal Processing,</i> 2015 , 34, 3395-3408	2.2	3
89	Performance trade-offs between Type II and Type III PLLs 2014 ,		1
88	Novel automatic digital calibration techniques for GMR sensors 2014,		1
87	Low-cost analog interface circuit for resistive bridge sensors 2013,		5
86	Sensor signal linearization techniques: A comparative analysis 2013,		8
85	A phenomenological spice model for magneto-impedance sensors. <i>International Journal of Circuit Theory and Applications</i> , 2012 , 40, 275-286	2	4
84	High-order PLL design with constant Phase Margin 2010 ,		6
83	Performance tradeoffs of integrated CMOS charge amplifiers 2009,		1
82	Low-Voltage MOS Translinear Analog Signal Processing. <i>Circuits, Systems, and Signal Processing</i> , 2009 , 28, 795-804	2.2	2
81	Performance Tradeoffs of Three Novel GMR Contactless Angle Detectors. <i>IEEE Sensors Journal</i> , 2009 , 9, 191-198	4	27
80	. IEEE Transactions on Instrumentation and Measurement, 2008 , 57, 309-320	5.2	17
79	A new multi-platform modular software tool for wide-angle reflection/refraction seismic data processing and representation (WASPAR). <i>Computers and Geosciences</i> , 2008 , 34, 456-463	4.5	O
78	General method for phase-locked loop filter analysis and design. <i>IET Circuits, Devices and Systems</i> , 2008 , 2, 249	1.1	8
77	Generalized Eigenvalues of Nonsquare Pencils with Structure. <i>SIAM Journal on Matrix Analysis and Applications</i> , 2008 , 30, 41-55	1.5	5

76	Loop filter approximations for PLLs 2008,		4
75	Design of High-Order Phase-Lock Loops. <i>IEEE Transactions on Circuits and Systems Part 2: Express Briefs</i> , 2007 , 54, 9-13		14
74	Multiple operating points in a square-root domain first-order filter. <i>International Journal of Circuit Theory and Applications</i> , 2007 , 35, 71-91	2	7
73	Voice Production Mechanisms of Vocal Vibrato in Male Singers. <i>IEEE Transactions on Audio Speech and Language Processing</i> , 2007 , 15, 320-332		4
72	Sensing in Coin Discriminators 2007 ,		8
71	Performance Tradeoffs of Three Contactless Angle Detection Systems 2007,		2
70	Randomized Carrier PWM With Exponential Frequency Mapping. <i>IEEE Transactions on Power Electronics</i> , 2007 , 22, 960-966	7. <u>2</u>	14
69	Versatile Automotive Sensor Interface ASIC 2007 ,		2
68	. IEEE Transactions on Audio Speech and Language Processing, 2006 , 14, 1422-1431		10
67	. IEEE Transactions on Audio Speech and Language Processing, 2006 , 14, 1413-1421		6
66	Signal processing in ocean bottom seismographs for refraction seismology. <i>IEEE Transactions on Instrumentation and Measurement</i> , 2006 , 55, 652-658	5.2	4
65	A Novel Design Method for Phase-Locked Loops of any Order and Type 2006 ,		5
64	Effect of the glottal source and the vocal tract on the partials amplitude of vibrato in male voices. <i>Journal of the Acoustical Society of America</i> , 2006 , 119, 2483-97	2.2	9
63	On asymmetric analysis windows for detection of closely spaced signal components. <i>Mechanical Systems and Signal Processing</i> , 2006 , 20, 702-717	7.8	8
62	The flipped voltage follower: a useful cell for low-voltage low-power circuit design. <i>IEEE Transactions on Circuits and Systems Part 1: Regular Papers</i> , 2005 , 52, 1276-1291		367
61	1.2-V 5-/spl mu/W class-AB CMOS log-domain integrator with multidecade tuning. <i>IEEE Transactions on Circuits and Systems Part 2: Express Briefs</i> , 2005 , 52, 665-668		12
60	1.5-V square-root domain second-order filter with on-chip tuning. <i>IEEE Transactions on Circuits and Systems Part 1: Regular Papers</i> , 2005 , 52, 1996-2006		17
59	1.5-V current-mode CMOS true RMS-DC converter based on class-AB transconductors. <i>IEEE</i> Transactions on Circuits and Systems Part 2: Express Briefs, 2005 , 52, 376-379		23

58	1.5 V tunable Square-Root Domain filter. <i>Electronics Letters</i> , 2004 , 40, 213	1.1	9
57	Low-Voltage CMOS Nonlinear Transconductors and Their Application to Companding Current-Mode Filters. <i>Analog Integrated Circuits and Signal Processing</i> , 2004 , 38, 137-147	1.2	6
56	Very Low Voltage MOS Translinear Loops Based on Flipped Voltage Followers. <i>Analog Integrated Circuits and Signal Processing</i> , 2004 , 40, 71-74	1.2	12
55	A 1 V Micropower FGMOS Class AB Log-Domain Filter. <i>Analog Integrated Circuits and Signal Processing</i> , 2004 , 41, 137-145	1.2	7
54	A Compact Four-Quadrant Floating-Gate MOS Multiplier. <i>Analog Integrated Circuits and Signal Processing</i> , 2004 , 41, 159-166	1.2	16
53	Vibrato in Singing Voice: The Link between Source-Filter and Sinusoidal Models. <i>Eurasip Journal on Advances in Signal Processing</i> , 2004 , 2004, 1	1.9	7
52	1.5 V four-quadrant CMOS current multiplier divider. <i>Electronics Letters</i> , 2003 , 39, 434	1.1	13
51	A 1.5 V Current-Mode CMOS RMS-to-DC Converter. <i>Analog Integrated Circuits and Signal Processing</i> , 2003 , 36, 137-143	1.2	15
50	A CMOS A/D Converter with Piecewise Linear Characteristic and Its Application to Sensor Linearization. <i>Analog Integrated Circuits and Signal Processing</i> , 2003 , 36, 39-46	1.2	17
49	Low-Voltage Rail-to-Rail Tunable FGMOS Transconductor. <i>Analog Integrated Circuits and Signal Processing</i> , 2003 , 36, 251-254	1.2	
48	Very Low Voltage CMOS Current Multiplier with Floating DC Level Shifters. <i>Analog Integrated Circuits and Signal Processing</i> , 2003 , 37, 265-268	1.2	4
47	Analysis of a negative impedance converter as a temperature compensator for bridge sensors. <i>IEEE Transactions on Instrumentation and Measurement</i> , 2003 , 52, 1068-1072	5.2	15
46	DSP-based implementation of an ANSI S1.11 acoustic analyzer. <i>IEEE Transactions on Instrumentation and Measurement</i> , 2003 , 52, 1213-1219	5.2	9
45	1.5-V MOS translinear loops with improved dynamic range and their applications to current-mode signal processing. <i>IEEE Transactions on Circuits and Systems Part 2: Express Briefs</i> , 2003 , 50, 918-927		32
44	. IEEE Transactions on Instrumentation and Measurement, 2002 , 51, 660-665	5.2	12
43	1.5 V CMOS companding filter. <i>Electronics Letters</i> , 2002 , 38, 1346	1.1	15
42	Extending the limits of resolution for narrow-band harmonic and modal analysis: a non-parametric approach. <i>Measurement Science and Technology</i> , 2002 , 13, 2082-2089	2	5
41	Limitations of the MOS Resistive Circuit in MOSFET-C Implementation: Bandwidth, Noise, Offset and Non-Linearity 2001 , 28, 239-252		1

40	Systematic Design of Companding Systems by Component Substitution 2001 , 28, 91-106		28
39	Current-Mode Multiplier/Divider Circuits Based on the MOS Translinear Principle 2001 , 28, 265-278		51
38	Nonparametric spectrum interpolation methods: a comparative study. <i>IEEE Transactions on Instrumentation and Measurement</i> , 2001 , 50, 1127-1132	5.2	7
37	MOSFET-C filter with on-chip tuning and wide programming range. <i>IEEE Transactions on Circuits and Systems Part 2: Express Briefs</i> , 2001 , 48, 944-951		15
36	Automated simplified analysis of analogue circuit behaviour based on fully symbolic relations between parameters. <i>International Journal of Electronics</i> , 2001 , 88, 1103-1115	1.2	
35	A symbolic method for network function approximation with order reduction. <i>International Journal of Circuit Theory and Applications</i> , 2000 , 28, 313-318	2	6
34	Instrument for the measurement of the instantaneous frequency. <i>IEEE Transactions on Instrumentation and Measurement</i> , 2000 , 49, 783-789	5.2	12
33	On the Use of Symbolic Analyzers in Circuit Synthesis 2000 , 25, 67-75		4
32	Design of MOS-translinear Multiplier/Dividers in Analog VLSI. VLSI Design, 2000, 11, 321-329		11
31	Phase-locked loop design for on-chip tuning applications. <i>Electronics Letters</i> , 2000 , 36, 699	1.1	4
30	Phase-locked loop design for on-chip tuning applications. <i>Electronics Letters</i> , 2000 , 36, 699 Novel transimpedance filter topology for instrumentation. <i>IEEE Transactions on Instrumentation and Measurement</i> , 1997 , 46, 862-867	5.2	13
	Novel transimpedance filter topology for instrumentation. <i>IEEE Transactions on Instrumentation and</i>		
30	Novel transimpedance filter topology for instrumentation. <i>IEEE Transactions on Instrumentation and Measurement</i> , 1997 , 46, 862-867 A course on instrumentation: the signal processing approach. <i>IEEE Transactions on Education</i> , 1997 ,	5.2	13
30	Novel transimpedance filter topology for instrumentation. <i>IEEE Transactions on Instrumentation and Measurement</i> , 1997 , 46, 862-867 A course on instrumentation: the signal processing approach. <i>IEEE Transactions on Education</i> , 1997 , 40, 297-297 Analog Universal Active Device: Theory, Design and Applications. <i>Analog Integrated Circuits and</i>	5.2	13
30 29 28	Novel transimpedance filter topology for instrumentation. <i>IEEE Transactions on Instrumentation and Measurement</i> , 1997 , 46, 862-867 A course on instrumentation: the signal processing approach. <i>IEEE Transactions on Education</i> , 1997 , 40, 297-297 Analog Universal Active Device: Theory, Design and Applications. <i>Analog Integrated Circuits and Signal Processing</i> , 1997 , 12, 153-168	5.2 2.1 1.2	13 9 14
30 29 28 27	Novel transimpedance filter topology for instrumentation. <i>IEEE Transactions on Instrumentation and Measurement</i> , 1997 , 46, 862-867 A course on instrumentation: the signal processing approach. <i>IEEE Transactions on Education</i> , 1997 , 40, 297-297 Analog Universal Active Device: Theory, Design and Applications. <i>Analog Integrated Circuits and Signal Processing</i> , 1997 , 12, 153-168 Use of a CCII (a) a universal building block. <i>Microelectronics Journal</i> , 1997 , 28, 543-550	5.2 2.1 1.2	13 9 14 3
30 29 28 27 26	Novel transimpedance filter topology for instrumentation. <i>IEEE Transactions on Instrumentation and Measurement</i> , 1997 , 46, 862-867 A course on instrumentation: the signal processing approach. <i>IEEE Transactions on Education</i> , 1997 , 40, 297-297 Analog Universal Active Device: Theory, Design and Applications. <i>Analog Integrated Circuits and Signal Processing</i> , 1997 , 12, 153-168 Use of a CCII las a universal building block. <i>Microelectronics Journal</i> , 1997 , 28, 543-550 Active RC impedances revisited. <i>International Journal of Circuit Theory and Applications</i> , 1997 , 25, 289-3 On the experimental methods to characterize the opamp response: a critical view. <i>IEEE Transactions</i>	5.2 2.1 1.2	13 9 14 3

22	Approach to the synthesis of canonic RC-active oscillators using CCII. <i>IET Circuits, Devices and Systems</i> , 1994 , 141, 493		37
21	. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 1994 , 41, 906-908		39
20	. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 1994 , 41, 79-81		10
19	. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 1993 , 40, 141-143		13
18	Accurate analysis of R-active circuits. <i>International Journal of Circuit Theory and Applications</i> , 1993 , 21, 101-106	2	3
17	Nullators and norators in voltage to current mode transformations. <i>International Journal of Circuit Theory and Applications</i> , 1993 , 21, 421-424	2	58
16	. IEEE Transactions on Instrumentation and Measurement, 1993 , 42, 775-778	5.2	4
15	. IEEE Transactions on Education, 1992 , 35, 204-210	2.1	2
14	Design criteria for high-frequency RC-active sinusoidal oscillators. <i>International Journal of Circuit Theory and Applications</i> , 1992 , 20, 387-399	2	1
13	Linear Modelling of Op-Amps. <i>International Journal of Electrical Engineering and Education</i> , 1991 , 28, 165-173	0.6	1
12	. IEEE Transactions on Instrumentation and Measurement, 1991 , 40, 644-646	5.2	2
11	Design conditions for second order oscillators using a single operational amplifier. <i>International Journal of Electronics</i> , 1991 , 70, 165-170	1.2	4
10	. IEEE Transactions on Circuits and Systems, 1990 , 37, 543-546		21
9	IEEE-488 Based Instrumentation in Undergraduate Laboratories. <i>European Journal of Engineering Education</i> , 1990 , 15, 59-66	1.5	1
8	Systematic generation of active compensated inverting systems. <i>International Journal of Electronics</i> , 1990 , 68, 79-85	1.2	
7	Unified approach to the synthesis of active compensated opamp. circuits. <i>International Journal of Electronics</i> , 1990 , 68, 247-257	1.2	
6	Active compensation of systems with a single feedback loop. <i>Microelectronics Journal</i> , 1989 , 20, 31-36	1.8	1
5	Loeb's criterion and latch-upleffect in second order oscillators with single OA. <i>International Journal of Electronics</i> , 1987 , 63, 655-660	1.2	8

LIST OF PUBLICATIONS

4	Stability of negative feedback systems. <i>International Journal of Electronics</i> , 1987 , 62, 257-264	1.2	
3	A variational model for transistors. <i>Microelectronics Journal</i> , 1987 , 18, 13-24	1.8	
2	A 1V micropower FGMOS log-domain filter	2	
1	A compact low-voltage four quadrant FGMOS multiplier	5	