

# Alfredo Arnaud

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/5804441/publications.pdf>

Version: 2024-02-01

24  
papers

141  
citations

1478505

6  
h-index

1281871

11  
g-index

26  
all docs

26  
docs citations

26  
times ranked

117  
citing authors

#	ARTICLE	IF	CITATIONS
1	Nano-power integrated precision rectifiers for implantable medical devices. International Journal of Circuit Theory and Applications, 2021, 49, 878-889.	2.0	0
2	CMOS level shifters from 0 to 18V output. Analog Integrated Circuits and Signal Processing, 2021, 107, 617-628.	1.4	4
3	Stacking Multiple Differential Pairs for a NEF<1 Amplifier aimed at Electroneurographic Signal Recording. , 2020, , .		0
4	Siwa: A custom RISC-V based system on chip (SOC) for low power medical applications. Microelectronics Journal, 2020, 98, 104753.	2.0	4
5	Bulk Linearization Techniques. , 2019, , .		2
6	IoT in the Agribusiness, a Power Consumption View. , 2019, , .		4
7	A current-reuse biomedical amplifier with a NEF<1. Analog Integrated Circuits and Signal Processing, 2018, 95, 283-294.	1.4	3
8	Bulk linearisation of the MOS resistor. Electronics Letters, 2018, 54, 1106-1108.	1.0	5
9	Active Current Mirrors for Low-Voltage Analog Circuit Design. Circuits, Systems, and Signal Processing, 2017, 36, 4869-4885.	2.0	8
10	Blind range level shifters from 0 to 18 V. , 2017, , .		4
11	Step down DC/DC converter for micro-power medical applications. Analog Integrated Circuits and Signal Processing, 2016, 89, 531-539.	1.4	5
12	A compact model for flicker noise in MOSFETs considering both correlated mobility and carrier number fluctuations. Analog Integrated Circuits and Signal Processing, 2016, 89, 611-618.	1.4	4
13	Step down DC/DC converter for micro-power medical applications. , 2015, , .		0
14	Measurements of the 12 nA low frequency oscillator. , 2015, , .		0
15	Nanopower OTAs With Improved Linearity and Low Input Offset Using Bulk Degeneration. IEEE Transactions on Circuits and Systems I: Regular Papers, 2014, 61, 689-698.	5.4	36
16	A programmable charge pump voltage converter for implantable medical devices in a HV technology. , 2013, , .		6
17	Very low frequency cyclostationary 1/f noise in MOS transistors. , 2013, , .		5
18	Analysis of the Low-Frequency Noise in Graded-Channel and Standard SOI nMOSFET. ECS Transactions, 2010, 31, 359-366.	0.5	1

#	ARTICLE	IF	CITATIONS
19	An Integrated Switch in a HV-SOI Wafer Technology, With a Novel Self-Protection Mechanism. Journal of Integrated Circuits and Systems, 2010, 5, 7-15.	0.4	6
20	On the reduction of thermal and flicker noise in ENG signal recording amplifiers. Analog Integrated Circuits and Signal Processing, 2008, 57, 39-48.	1.4	13
21	On the Evaluation of the Exact Output of a Switched Continuous-Time Filter and Applications. IEEE Transactions on Circuits and Systems I: Regular Papers, 2008, 55, 1421-1429.	5.4	2
22	A Study of Flicker Noise in MOS Transistor Under Switched Bias Condition. Journal of Integrated Circuits and Systems, 2008, 3, 63-68.	0.4	7
23	A Study Of Flicker Noise In MOS Transistor Under Switched Bias Condition. ECS Transactions, 2007, 9, 313-322.	0.5	2
24	Fully integrated signal conditioning of an accelerometer for implantable pacemakers. Analog Integrated Circuits and Signal Processing, 2006, 49, 313-321.	1.4	19