John W Fowler

List of Publications by Citations

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The third column is the impact factor (IF) of the journal, and the fourth column is the number of citations of the article.

173 papers	4,325 citations	37 h-index	58 g-index
187 ext. papers	5,072 ext. citations	3.9 avg, IF	5.64 L-index

#	Paper	IF	Citations
173	A survey of problems, solution techniques, and future challenges in scheduling semiconductor manufacturing operations. *Journal of Scheduling*, **2011**, 14, 583-599	1.6	224
172	A multi-population genetic algorithm to solve multi-objective scheduling problems for parallel machines. *Computers and Operations Research*, **2003**, 30, 1087-1102	4.6	160
171	Heuristic scheduling of jobs on parallel batch machines with incompatible job families and unequal ready times. *Computers and Operations Research*, **2005**, 32, 2731-2750	4.6	144
170	Grand Challenges in Modeling and Simulation of Complex Manufacturing Systems. *Simulation*, **2004** , 80, 469-476	1.2	141
169	How simulation modelling can help reduce the impact of COVID-19. *Journal of Simulation*, **2020**, 14, 83-97	1.9	134
168	A modified shifting bottleneck heuristic for minimizing total weighted tardiness in complex job shops. *Journal of Scheduling*, **2002**, 5, 247-262	1.6	132
167	Genetic algorithm based scheduling of parallel batch machines with incompatible job families to minimize total weighted tardiness. *International Journal of Production Research*, **2004**, 42, 1621-1638	7.8	122
166	A new scheduling approach using combined dispatching criteria in wafer fabs. *IEEE Transactions on Semiconductor Manufacturing*, **2003**, 16, 501-510	2.6	107
165	Bi-Criteria Scheduling of Surgical Services for an Outpatient Procedure Center. *Production and Operations Management*, **2011**, 20, 406-417	3.6	97
164	Production Planning and Control for Semiconductor Wafer Fabrication Facilities. *Operations Research/ Computer Science Interfaces Series*, **2013**,	0.3	86
163	. *IEEE Transactions on Semiconductor Manufacturing*, **1992**, 5, 158-163	2.6	84
162	Mixed integer programming formulations for single machine scheduling problems. *Computers and Industrial Engineering*, **2009**, 56, 357-367	6.4	82
161	The use of upstream and downstream information in scheduling semiconductor batch operations. *International Journal of Production Research*, **1995**, 33, 1849-1869	7.8	76
160	Heuristics for minimizing regular performance measures in unrelated parallel machine scheduling problems. *Computers and Operations Research*, **2011**, 38, 901-916	4.6	75
159	Scheduling jobs on parallel machines with setup times and ready times. *Computers and Industrial Engineering*, **2008**, 54, 764-782	6.4	75
158	Minimizing total weighted tardiness on a single batch process machine with incompatible job families. *Computers and Operations Research*, **2005**, 32, 327-341	4.6	72
157	Genetic algorithm-based subproblem solution procedures for a modified shifting bottleneck heuristic for complex job shops. *European Journal of Operational Research*, **2007**, 177, 2100-2118	5.6	71

156	Heuristics for workforce planning with worker differences. *European Journal of Operational Research*, **2008**, 190, 724-740	5.6	62
155	Workload control in the semiconductor industry. *Production Planning and Control*, **2002**, 13, 568-578	4.3	60
154	Modelling inherent worker differences for workforce planning. *International Journal of Production Research*, **2007**, 45, 525-553	7.8	55
153	Modelling and analysis of semiconductor manufacturing in a shrinking world: challenges and successes. *European Journal of Industrial Engineering*, **2011**, 5, 254	1.1	54
152	A SURVEY OF ALGORITHMS FOR SINGLE AND MULTI-OBJECTIVE UNRELATED PARALLEL-MACHINE DETERMINISTIC SCHEDULING PROBLEMS. *Journal of the Chinese Institute of Industrial Engineers*, **2004**, 21, 230-241		53
151	A combined dispatching criteria approach to scheduling semiconductor manufacturing systems. *Computers and Industrial Engineering*, **2001**, 39, 307-324	6.4	53
150	Multi-mode resource-constrained project scheduling problems with non-preemptive activity splitting. *Computers and Operations Research*, **2015**, 53, 275-287	4.6	51
149	Interactive evolutionary multi-objective optimization for quasi-concave preference functions. *European Journal of Operational Research*, **2010**, 206, 417-425	5.6	49
148	D-Optimal Sequential Experiments for Generating a Simulation-Based Cycle Time-Throughput Curve. *Operations Research*, **2002**, 50, 981-990	2.3	49
147	Operational planning and control of semiconductor wafer production. *Production Planning and Control*, **2006**, 17, 639-647	4.3	48
146	Scheduling of unrelated parallel machines: an application to PWB manufacturing. *IIE Transactions*, **2002**, 34, 921-931		44
145	A multi-criteria approach for scheduling semiconductor wafer fabrication facilities. *Journal of Scheduling*, **2008**, 11, 29-47	1.6	43
144	Control of multiproduct bulk server diffusion/oxidation processes. Part 2: multiple servers. *IIE Transactions*, **2000**, 32, 167-176		43
143	Scheduling interfering job sets on parallel machines. *European Journal of Operational Research*, **2009**, 199, 55-67	5.6	41
142	Deploying LSS in a global enterprise project identification. *International Journal of Lean Six Sigma*, **2012**, 3, 187-205	4.6	41
141	A hybrid approach using the analytic hierarchy process and integer programming to screen weapon systems projects. *IEEE Transactions on Engineering Management*, **2003**, 50, 192-203	2.6	41
140	CONTROL OF MULTIPRODUCT BULK SERVICE DIFFUSION/OXIDATION PROCESSES. *IIE Transactions*, **1992**, 24, 84-96		41
139	A survey of semiconductor supply chain models part I: semiconductor supply chains, strategic network design, and supply chain simulation. *International Journal of Production Research*, **2018**, 56, 4524-4545	7.8	40

138	Multiple response optimization using mixture-designed experiments and desirability functions in semiconductor scheduling. *International Journal of Production Research*, **2003**, 41, 939-961	7.8	39
137	Crossdocking Just in Time scheduling: an alternative solution approach. *Journal of the Operational Research Society*, **2009**, 60, 554-564	2	37
136	Heuristics for minimizing total weighted tardiness in complex job shops. *International Journal of Production Research*, **2005**, 43, 1943-1963	7.8	37
135	Multivariate statistical methods for modeling and analysis of wafer probe test data. *IEEE Transactions on Semiconductor Manufacturing*, **2002**, 15, 523-530	2.6	37
134	A Progressive Hedging Approach for Surgery Planning Under Uncertainty. *INFORMS Journal on Computing*, **2015**, 27, 755-772	2.4	34
133	Planning and scheduling in Japanese semiconductor manufacturing. *Journal of Manufacturing Systems*, **1994**, 13, 323-332	9.1	34
132	Optimal batching in a wafer fabrication facility using a multiproduct G/G/c model with batch processing. *International Journal of Production Research*, **2002**, 40, 275-292	7.8	33
131	Determining the optimal target for a process with multiple markets and variable holding costs. *International Journal of Production Economics*, **2000**, 65, 229-242	9.3	32
130	Efficient cycle time-throughput curve generation using a fixed sample size procedure. *International Journal of Production Research*, **2001**, 39, 2595-2613	7.8	31
129	Supply network capacity planning for semiconductor manufacturing with uncertain demand and correlation in demand considerations. *International Journal of Production Economics*, **2011**, 134, 322-332	9.3	30
128	. *IEEE Transactions on Semiconductor Manufacturing*, **2008**, 21, 600-613	2.6	30
127	Semiconductor Manufacturing Scheduling and Dispatching **2006**, 213-241		30
126	Quantitative Comparison of Approximate Solution Sets for Bi-criteria Optimization Problems*. *Decision Sciences*, **2003**, 34, 63-82	3.7	29
125	A survey of semiconductor supply chain models part III: master planning, production planning, and demand fulfilment. *International Journal of Production Research*, **2018**, 56, 4565-4584	7.8	29
124	Multiple-objective heuristics for scheduling unrelated parallel machines. *European Journal of Operational Research*, **2013**, 227, 239-253	5.6	28
123	Optimal control policies for ambulance diversion. *European Journal of Operational Research*, **2014**, 236, 298-312	5.6	25
122	THE INCLUSION OF FUTURE ARRIVALS AND DOWNSTREAM SETUPS INTO WAFER FABRICATION BATCH PROCESSING DECISIONS. *Journal of Electronics Manufacturing*, **2002**, 11, 149-159		25
121	A survey of semiconductor supply chain models Part II: demand planning, inventory management, and capacity planning. *International Journal of Production Research*, **2018**, 56, 4546-4564	7.8	24

120	Batch scheduling on parallel machines with dynamic job arrivals and incompatible job families. *International Journal of Production Research*, **2013**, 51, 2462-2477	7.8	24
119	Minimizing makespan with multiple-orders-per-job in a two-machine flowshop. *European Journal of Operational Research*, **2007**, 182, 63-79	5.6	24
118	Enterprise-wide semiconductor manufacturing resource planning. *IEEE Transactions on Semiconductor Manufacturing*, **2006**, 19, 259-268	2.6	24
117	Point-of-Dispensing Location and Capacity Optimization via a Decision Support System. *Production and Operations Management*, **2015**, 24, 1311-1328	3.6	23
116	Lot-to-order matching for a semiconductor assembly and test facility. *IIE Transactions*, **1999**, 31, 1103-1111		23
115	Impact of permutation enforcement when minimizing total weighted tardiness in dynamic flowshops with uncertain processing times. *Computers and Operations Research*, **2007**, 34, 3055-3068	4.6	22
114	A clustering algorithm for supplier base management. *International Journal of Production Research*, **2010**, 48, 3803-3821	7.8	21
113	Comparison and evaluation of lot-to-order matching policies for a semiconductor assembly and test facility. *International Journal of Production Research*, **2000**, 38, 1841-1853	7.8	21
112	A hybrid scheduling approach for a two-stage flexible flow shop with batch processing machines. *Journal of Scheduling*, **2018**, 21, 209-226	1.6	20
111	Simulation modeling for pandemic decision making: A case study with bi-criteria analysis on school closures. *Decision Support Systems*, **2013**, 55, 564-575	5.6	20
110	Constraint Programming Approach for Scheduling Jobs With Release Times, Non-Identical Sizes, and Incompatible Families on Parallel Batching Machines. *IEEE Transactions on Semiconductor Manufacturing*, **2017**, 30, 500-507	2.6	20
109	A new method of exercising pandemic preparedness through an interactive simulation and visualization. *Journal of Medical Systems*, **2012**, 36, 1475-83	5.1	19
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99	Scheduling of Wet Etch and Furnace Operations with Next Arrival Control Heuristic. *International Journal of Advanced Manufacturing Technology*, **2008**, 38, 1006-1017	3.2	16
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95	Bin covering algorithms in the second stage of the lot to order matching problem. *Journal of the Operational Research Society*, **2001**, 52, 1232-1243	2	15
94	**2011**,		14
93	Scheduling parallel machines with single vehicle delivery. *Journal of Heuristics*, **2014**, 20, 511-537	1.9	13
92	Impact of Number of Interactions, Different Interaction Patterns, and Human Inconsistencies on Some Hybrid Evolutionary Multiobjective Optimization Algorithms*. *Decision Sciences*, **2015**, 46, 981-1006	3.7	13
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90	Petri Net Translation Patterns for the Analysis of eBusiness Collaboration Messaging Protocols. *IEEE Transactions on Systems, Man and Cybernetics, Part A: Systems and Humans*, **2009**, 39, 1022-1034		13
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85	RESEARCH DIRECTIONS IN ELECTRONICS MANUFACTURING. *IIE Transactions*, **1992**, 24, 6-17		12

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82	A survey of scheduling with parallel batch (p-batch) processing. *European Journal of Operational Research*, **2021**,	5.6	11
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79	Control of multiproduct bulk server diffusion/oxidation processes. Part 2: multiple servers. *IIE Transactions*, **2000**, 32, 167-176		10
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75	Comparing designs for computer simulation experiments **2008**,		9
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71	Machine qualification management for a semiconductor back-end facility **2010**,		8
70	Availability-adjusted X-factor. *International Journal of Production Research*, **2005**, 43, 3933-3953	7.8	8
69	Evaluation of nondominated solution sets for k-objective optimization problems: An exact method and approximations. *European Journal of Operational Research*, **2006**, 173, 565-582	5.6	8
68	Stochastic optimization of product machine qualification in a semiconductor back-end facility. *IIE Transactions*, **2015**, 47, 739-750		7
67	Bi-criteria parallel batch machine scheduling to minimize total weighted tardiness and electricity cost. *Journal of Business Economics*, **2020**, 90, 1345-1381	2.3	7

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65	**2009**,		7
64	Run-to-run control charts with contrasts. *Quality and Reliability Engineering International*, **1998**, 14, 261-272	2.2	7
63	Experiments in reactive constraint logic programming. *The Journal of Logic Programming*, **1998**, 37, 185-212		7
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60	A genetic algorithm approach to manage ion implantation processes in wafer fabrication. *International Journal of Manufacturing Technology and Management*, **2000**, 1, 156	0.4	7
59	Extending the boundaries between scheduling and dispatching: hedging and rescheduling techniques. *International Journal of Production Research*, **2017**, 55, 3294-3307	7.8	6
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54	Collaborative Geodesign for Alternative-Fuel Station Location using CollablocationSoftware. *Transportation Research Record*, **2018**, 2672, 98-108	1.7	6
53	Scheduling and Simulation **2006**, 109-133		6
52	. *Journal of Systems Engineering and Electronics*, **2016**, 27, 422-433	1.3	5
51	Bi-criteria analysis of ambulance diversion policies **2010**,		5
50	Integer programming-based real-time scheduler in semiconductor manufacturing **2009**,		5
49	A control chart based approach to monitoring supply network dynamics using Kalman filtering. *International Journal of Production Research*, **2012**, 50, 3137-3151	7.8	5

48	Determining operating criterion of batch processing operations for wafer fabrication. *Journal of Manufacturing Systems*, **2002**, 21, 363-379	9.1	5
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45	Capacity Planning and Allocation for Web-Based Applications. *Decision Sciences*, **2014**, 45, 535-567	3.7	4
44	**2012**,		4
43	**2008**,		4
42	Balanced Machine Workload Dispatching Scheme for Wafer Fab **2007**,		4
41	Partition experimental designs for sequential processes: Part II: Second-order models. *Quality and Reliability Engineering International*, **2002**, 18, 373-382	2.6	4
40	Encyclopedia of Production and Manufacturing Management **2000**, 687-697		4
39	Cross-Training with Imperfect Training Schemes. *Production and Operations Management*, **2016**, 25, 1216-1231	3.1	4
38	Decision Assessment Algorithms for Location and Capacity Optimization under Resource Shortages. *Decision Sciences*, **2021**, 52, 142-181	3.7	4
37	Locating Alternative Fuel Stations for Maximizing Coverage and Ensuring Sufficient Spacing: a Case Study of CNG Truck Fueling. *Process Integration and Optimization for Sustainability*, **2019**, 3, 455-470	2	3
36	Decomposition heuristic for a two-machine flow shop with batch processing **2014**,		3
35	Bootstrapping-based fixed-width confidence intervals for ranking and selection **2010**,		3
34	An introduction to a new journal for Healthcare Systems Engineering. *IIE Transactions on Healthcare Systems Engineering*, **2011**, 1, 1-5		3
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31	Determining an appropriate number of FOUPs in semiconductor wafer fabrication facilities **2008**,		3

30	Framework for execution level capacity allocation decisions for Assembly - Test facilities using integrated optimization - simulation models **2008**,		3
29	An Adaptive Distributed Simulation Framework for a Server Fulfillment Supply Chain **2006**,		3
28	Decision Paradigms in the Semiconductor Supply Chain: A Survey and Analysis **2007**,		3
27	THE IMPACTS OF VARIABILITY ON SCHEDULING APPROACHES FOR A PRINTED WIRING BOARD ASSEMBLY OPERATION. *Journal of Electronics Manufacturing*, **2002**, 11, 19-31		3
26	A Sequential Stopping Rule for a Steady-State Simulation Based on Time-Series Forecasting. *Simulation*, **2002**, 78, 643-654	1.2	3
25	A group-technology-coded literature review of semiconductor manufacturing operations publications: the MASMLAB bibliography web site. *IEEE Transactions on Semiconductor Manufacturing*, **1999**, 12, 259-263	2.6	3
24	Modeling and Analysis of Semiconductor Manufacturing. *Simulation Foundations, Methods and Applications*, **2017**, 301-313	0.6	3
23	ELECTRICITY POWER COST-AWARE SCHEDULING OF JOBS ON PARALLEL BATCH PROCESSING MACHINES **2018**,		3
22	Modelling and analysis of semiconductor supply chains. *International Journal of Production Research* , **2018**, 56, 4521-4523	7.8	3
21	Multiple data sources fusion for enterprise quality improvement by a multilevel latent response model. *IIE Transactions*, **2014**, 46, 512-525		2
20	Semiconductor Manufacturing Process Description. *Operations Research/ Computer Science Interfaces Series*, **2013**, 11-28	0.3	2
19	Production Planning Approaches. *Operations Research/ Computer Science Interfaces Series*, **2013**, 207-246	0.3	2
18	A partition experimental design for a sequential process with a large number of variables. *Quality and Reliability Engineering International*, **2007**, 23, 555-564	2.6	2
17	Indirect Cycle-Time Quantile Estimation for Non-Fifo Dispatching Policies **2006**,		2
16	A note on determining an optimal target by considering the dependence of holding costs and the quality characteristics. *Journal of Applied Statistics*, **2005**, 32, 813-822	1	2
15	Lot-to-order matching for a semiconductor assembly andtest facility. *IIE Transactions*, **1999**, 31, 1103-1111		2
14	Dispatching Approaches. *Operations Research/ Computer Science Interfaces Series*, **2013**, 65-104	0.3	1
13	A hybrid approach for modelling and analysis of an integrated machine. *Proceedings of the Institution of Mechanical Engineers, Part B: Journal of Engineering Manufacture*, **2004**, 218, 1577-1590	2.4	1

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11	Simulation: The past 10 years and the next 10 years **2016**,		1
10	A generalized decision support framework for large-scale project portfolio decisions. *Decision Sciences*,	3.7	1
9	Improving the process efficiency of catheterization laboratories using simulation. *Health Systems*, **2017**, 6, 41-55	2.3	0
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6	State of the Practice and Future Needs for Production Planning and Control Systems. *Operations Research/ Computer Science Interfaces Series*, **2013**, 247-266	0.3	
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4	A three-phase simulation methodology for generating accurate and precise cycle time throughput curves. *International Journal of Simulation and Process Modelling*, **2005**, 1, 35	0.4	
3	The implementation and evaluation of a hypertext system for teaching employment-related language to hearing-impaired school leavers. *Journal of Microcomputer Applications*, **1995**, 18, 319-325		
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1	Guest Editorial Special Section Papers From the 2019 MASM/WSC Conference. *IEEE Transactions on Semiconductor Manufacturing*, **2020**, 33, 493-495	2.6	