

John W Fowler

List of Publications by Year in descending order

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185
papers

5,684
citations

76322

40
h-index

98792

67
g-index

187
all docs

187
docs citations

187
times ranked

2879
citing authors

#	ARTICLE	IF	CITATIONS
1	A survey of problems, solution techniques, and future challenges in scheduling semiconductor manufacturing operations. <i>Journal of Scheduling</i> , 2011, 14, 583-599.	1.9	312
2	How simulation modelling can help reduce the impact of COVID-19. <i>Journal of Simulation</i> , 2020, 14, 83-97.	1.5	261
3	A multi-population genetic algorithm to solve multi-objective scheduling problems for parallel machines. <i>Computers and Operations Research</i> , 2003, 30, 1087-1102.	4.0	206
4	Grand Challenges in Modeling and Simulation of Complex Manufacturing Systems. <i>Simulation</i> , 2004, 80, 469-476.	1.8	181
5	Heuristic scheduling of jobs on parallel batch machines with incompatible job families and unequal ready times. <i>Computers and Operations Research</i> , 2005, 32, 2731-2750.	4.0	174
6	A modified shifting bottleneck heuristic for minimizing total weighted tardiness in complex job shops. <i>Journal of Scheduling</i> , 2002, 5, 247-262.	1.9	158
7	Genetic algorithm based scheduling of parallel batch machines with incompatible job families to minimize total weighted tardiness. <i>International Journal of Production Research</i> , 2004, 42, 1621-1638.	7.5	138
8	A new scheduling approach using combined dispatching criteria in wafer fabs. <i>IEEE Transactions on Semiconductor Manufacturing</i> , 2003, 16, 501-510.	1.7	127
9	Production Planning and Control for Semiconductor Wafer Fabrication Facilities. <i>Operations Research/ Computer Science Interfaces Series</i> , 2013, , .	0.3	127
10	Bi-criteria Scheduling of Surgical Services for an Outpatient Procedure Center. <i>Production and Operations Management</i> , 2011, 20, 406-417.	3.8	118
11	Mixed integer programming formulations for single machine scheduling problems. <i>Computers and Industrial Engineering</i> , 2009, 56, 357-367.	6.3	98
12	Real-time control of multiproduct bulk-service semiconductor manufacturing processes. <i>IEEE Transactions on Semiconductor Manufacturing</i> , 1992, 5, 158-163.	1.7	97
13	A survey of semiconductor supply chain models part I: semiconductor supply chains, strategic network design, and supply chain simulation. <i>International Journal of Production Research</i> , 2018, 56, 4524-4545.	7.5	91
14	Genetic algorithm-based subproblem solution procedures for a modified shifting bottleneck heuristic for complex job shops. <i>European Journal of Operational Research</i> , 2007, 177, 2100-2118.	5.7	89
15	Heuristics for minimizing regular performance measures in unrelated parallel machine scheduling problems. <i>Computers and Operations Research</i> , 2011, 38, 901-916.	4.0	87
16	Minimizing total weighted tardiness on a single batch process machine with incompatible job families. <i>Computers and Operations Research</i> , 2005, 32, 327-341.	4.0	85
17	Scheduling jobs on parallel machines with setup times and ready times. <i>Computers and Industrial Engineering</i> , 2008, 54, 764-782.	6.3	84
18	The use of upstream and downstream information in scheduling semiconductor batch operations. <i>International Journal of Production Research</i> , 1995, 33, 1849-1869.	7.5	83

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19	Workload control in the semiconductor industry. <i>Production Planning and Control</i> , 2002, 13, 568-578.	8.8	80
20	Heuristics for workforce planning with worker differences. <i>European Journal of Operational Research</i> , 2008, 190, 724-740.	5.7	76
21	A combined dispatching criteria approach to scheduling semiconductor manufacturing systems. <i>Computers and Industrial Engineering</i> , 2001, 39, 307-324.	6.3	75
22	Modelling and analysis of semiconductor manufacturing in a shrinking world: challenges and successes. <i>European Journal of Industrial Engineering</i> , 2011, 5, 254.	0.8	74
23	Operational planning and control of semiconductor wafer production. <i>Production Planning and Control</i> , 2006, 17, 639-647.	8.8	70
24	Modelling inherent worker differences for workforce planning. <i>International Journal of Production Research</i> , 2007, 45, 525-553.	7.5	70
25	Multi-mode resource-constrained project scheduling problems with non-preemptive activity splitting. <i>Computers and Operations Research</i> , 2015, 53, 275-287.	4.0	67
26	A SURVEY OF ALGORITHMS FOR SINGLE AND MULTI-OBJECTIVE UNRELATED PARALLEL-MACHINE DETERMINISTIC SCHEDULING PROBLEMS. <i>Journal of the Chinese Institute of Industrial Engineers</i> , 2004, 21, 230-241.	0.5	63
27	Interactive evolutionary multi-objective optimization for quasi-concave preference functions. <i>European Journal of Operational Research</i> , 2010, 206, 417-425.	5.7	62
28	A survey of scheduling with parallel batch (p-batch) processing. <i>European Journal of Operational Research</i> , 2022, 298, 1-24.	5.7	62
29	D-Optimal Sequential Experiments for Generating a Simulation-Based Cycle Time-Throughput Curve. <i>Operations Research</i> , 2002, 50, 981-990.	1.9	59
30	A hybrid approach using the analytic hierarchy process and integer programming to screen weapon systems projects. <i>IEEE Transactions on Engineering Management</i> , 2003, 50, 192-203.	3.5	59
31	A survey of semiconductor supply chain models part III: master planning, production planning, and demand fulfilment. <i>International Journal of Production Research</i> , 2018, 56, 4565-4584.	7.5	55
32	Scheduling of unrelated parallel machines: an application to PWB manufacturing. <i>IIE Transactions</i> , 2002, 34, 921-931.	2.1	52
33	Multiple response optimization using mixture-designed experiments and desirability functions in semiconductor scheduling. <i>International Journal of Production Research</i> , 2003, 41, 939-961.	7.5	50
34	A multi-criteria approach for scheduling semiconductor wafer fabrication facilities. <i>Journal of Scheduling</i> , 2008, 11, 29-47.	1.9	50
35	Deploying LSS in a global enterprise – project identification. <i>International Journal of Lean Six Sigma</i> , 2012, 3, 187-205.	3.3	50
36	CONTROL OF MULTIPRODUCT BULK SERVICE DIFFUSION/OXIDATION PROCESSES. <i>IIE Transactions</i> , 1992, 24, 84-96.	2.1	47

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37	Scheduling interfering job sets on parallel machines. <i>European Journal of Operational Research</i> , 2009, 199, 55-67.	5.7	47
38	A Progressive Hedging Approach for Surgery Planning Under Uncertainty. <i>INFORMS Journal on Computing</i> , 2015, 27, 755-772.	1.7	47
39	Heuristics for minimizing total weighted tardiness in complex job shops. <i>International Journal of Production Research</i> , 2005, 43, 1943-1963.	7.5	46
40	Control of multiproduct bulk server diffusion/oxidation processes. Part 2: multiple servers. <i>IIE Transactions</i> , 2000, 32, 167-176.	2.1	44
41	Crossdockingâ€” Just in Time scheduling: an alternative solution approach. <i>Journal of the Operational Research Society</i> , 2009, 60, 554-564.	3.4	43
42	A survey of semiconductor supply chain models Part II: demand planning, inventory management, and capacity planning. <i>International Journal of Production Research</i> , 2018, 56, 4546-4564.	7.5	43
43	Multivariate statistical methods for modeling and analysis of wafer probe test data. <i>IEEE Transactions on Semiconductor Manufacturing</i> , 2002, 15, 523-530.	1.7	41
44	Semiconductor Manufacturing Scheduling and Dispatching. , 2006, , 213-241.		40
45	Supply network capacity planning for semiconductor manufacturing with uncertain demand and correlation in demand considerations. <i>International Journal of Production Economics</i> , 2011, 134, 322-332.	8.9	38
46	Planning and scheduling in Japanese semiconductor manufacturing. <i>Journal of Manufacturing Systems</i> , 1994, 13, 323-332.	13.9	37
47	Multiple-objective heuristics for scheduling unrelated parallel machines. <i>European Journal of Operational Research</i> , 2013, 227, 239-253.	5.7	37
48	Optimal batching in a wafer fabrication facility using a multiproduct G/G/c model with batch processing. <i>International Journal of Production Research</i> , 2002, 40, 275-292.	7.5	36
49	Levels of Capacity and Material Handling System Modeling for Factory Integration Decision Making in Semiconductor Wafer Fabs. <i>IEEE Transactions on Semiconductor Manufacturing</i> , 2008, 21, 600-613.	1.7	36
50	Determining the optimal target for a process with multiple markets and variable holding costs. <i>International Journal of Production Economics</i> , 2000, 65, 229-242.	8.9	35
51	Efficient cycle time-throughput curve generation using a fixed sample size procedure. <i>International Journal of Production Research</i> , 2001, 39, 2595-2613.	7.5	35
52	Pointâ€”Dispensing Location and Capacity Optimization via a Decision Support System. <i>Production and Operations Management</i> , 2015, 24, 1311-1328.	3.8	35
53	Lot-to-order matching for a semiconductor assembly and test facility. <i>IIE Transactions</i> , 1999, 31, 1103-1111.	2.1	33
54	Optimal control policies for ambulance diversion. <i>European Journal of Operational Research</i> , 2014, 236, 298-312.	5.7	33

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55	Quantitative Comparison of Approximate Solution Sets for Bi-criteria Optimization Problems*. Decision Sciences, 2003, 34, 63-82.	4.5	32
56	A hybrid scheduling approach for a two-stage flexible flow shop with batch processing machines. Journal of Scheduling, 2018, 21, 209-226.	1.9	31
57	Enterprise-Wide Semiconductor Manufacturing Resource Planning. IEEE Transactions on Semiconductor Manufacturing, 2006, 19, 259-268.	1.7	30
58	A clustering algorithm for supplier base management. International Journal of Production Research, 2010, 48, 3803-3821.	7.5	30
59	A Testbed for Simulating Semiconductor Supply Chains. IEEE Transactions on Semiconductor Manufacturing, 2017, 30, 293-305.	1.7	29
60	Simulation modeling for pandemic decision making: A case study with bi-criteria analysis on school closures. Decision Support Systems, 2013, 55, 564-575.	5.9	28
61	Batch scheduling on parallel machines with dynamic job arrivals and incompatible job families. International Journal of Production Research, 2013, 51, 2462-2477.	7.5	28
62	Comparison and evaluation of lot-to-order matching policies for a semiconductor assembly and test facility. International Journal of Production Research, 2000, 38, 1841-1853.	7.5	27
63	Semiconductor lot allocation using robust optimization. European Journal of Operational Research, 2010, 205, 557-570.	5.7	27
64	THE INCLUSION OF FUTURE ARRIVALS AND DOWNSTREAM SETUPS INTO WAFER FABRICATION BATCH PROCESSING DECISIONS. Journal of Electronics Manufacturing, 2002, 11, 149-159.	0.4	26
65	Evaluating the Impacts of Reticle Requirements in Semiconductor Wafer Fabrication. IEEE Transactions on Semiconductor Manufacturing, 2005, 18, 622-632.	1.7	26
66	A New Method of Exercising Pandemic Preparedness Through an Interactive Simulation and Visualization. Journal of Medical Systems, 2012, 36, 1475-1483.	3.6	26
67	Minimizing makespan with multiple-orders-per-job in a two-machine flowshop. European Journal of Operational Research, 2007, 182, 63-79.	5.7	25
68	Indirect cycle time quantile estimation using the Cornish-Fisher expansion. IIE Transactions, 2009, 42, 31-44.	2.1	25
69	Simulation-based experimental design and statistical modeling for lead time quotation. Journal of Manufacturing Systems, 2015, 37, 362-374.	13.9	25
70	Constraint Programming Approach for Scheduling Jobs With Release Times, Non-Identical Sizes, and Incompatible Families on Parallel Batching Machines. IEEE Transactions on Semiconductor Manufacturing, 2017, 30, 500-507.	1.7	25
71	Bin covering algorithms in the second stage of the lot to order matching problem. Journal of the Operational Research Society, 2001, 52, 1232-1243.	3.4	23
72	The Development of the Complete X-Factor Contribution Measurement for Improving Cycle Time and Cycle Time Variability. IEEE Transactions on Semiconductor Manufacturing, 2006, 19, 352-362.	1.7	22

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73	A compact abstraction of manufacturing nodes in a supply network. International Journal of Simulation and Process Modelling, 2007, 3, 115.	0.2	22
74	Impact of permutation enforcement when minimizing total weighted tardiness in dynamic flowshops with uncertain processing times. Computers and Operations Research, 2007, 34, 3055-3068.	4.0	22
75	Design of centralized Ambulance Diversion policies using Simulation-Optimization. , 2011, , .		21
76	Improving the pushâ€“pull strategy in a serial supply chain by a hybrid pushâ€“pull control with multiple pulling points. International Journal of Production Research, 2012, 50, 5651-5668.	7.5	21
77	Robust scaling parameters for composite dispatching rules. IIE Transactions, 2010, 42, 842-853.	2.1	20
78	Cost-constrainedG-efficient Response Surface Designs for Cuboidal Regions. Quality and Reliability Engineering International, 2006, 22, 121-139.	2.3	19
79	Scheduling of Wet Etch and Furnace Operations with Next Arrival Control Heuristic. International Journal of Advanced Manufacturing Technology, 2008, 38, 1006-1017.	3.0	19
80	Quantitative Comparison of Approximate Solution Sets for Multicriteria Optimization Problems with Weighted Tchebycheff Preference Function. Operations Research, 2010, 58, 650-659.	1.9	19
81	Robust demand service achievement for the co-production newsvendor. IIE Transactions, 2012, 44, 327-341.	2.1	19
82	Impact of Number of Interactions, Different Interaction Patterns, and Human Inconsistencies on Some Hybrid Evolutionary Multiobjective Optimization Algorithms*. Decision Sciences, 2015, 46, 981-1006.	4.5	19
83	Scheduling parallel machines with single vehicle delivery. Journal of Heuristics, 2014, 20, 511-537.	1.4	17
84	Bi-criteria parallel batch machine scheduling to minimize total weighted tardiness and electricity cost. Journal of Business Economics, 2020, 90, 1345-1381.	1.9	17
85	Simulation in Production Planning: An Overview with Emphasis on Recent Developments in Cycle Time Estimation. Profiles in Operations Research, 2011, , 565-591.	0.4	17
86	Control of multiproduct bulk server diffusion/oxidation processes. Part 2: multiple servers. IIE Transactions, 2000, 32, 167-176.	2.1	16
87	Batch Production Scheduling for Semiconductor Back-End Operations. IEEE Transactions on Semiconductor Manufacturing, 2011, 24, 249-260.	1.7	16
88	Stochastic optimization of productâ€“machine qualification in a semiconductor back-end facility. IIE Transactions, 2015, 47, 739-750.	2.1	16
89	RESEARCH DIRECTIONS IN ELECTRONICS MANUFACTURING. IIE Transactions, 1992, 24, 6-17.	2.1	15
90	Development and analysis of a customer-demand-driven semiconductor supply chain model using the High Level Architecture. International Journal of Simulation and Process Modelling, 2006, 2, 210.	0.2	15

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91	Using quantiles in ranking and selection procedures. , 2007, , .		15
92	Capacity planning through queueing analysis and simulation-based statistical methods: a case study for semiconductor wafer fabs. International Journal of Production Research, 2011, 49, 4573-4591.	7.5	15
93	Scheduling of unrelated parallel machines: an application to PWB manufacturing. IIE Transactions, 2002, 34, 921-931.	2.1	14
94	Evaluation of heuristics for a class-constrained lot-to-order matching problem in semiconductor manufacturing. International Journal of Production Research, 2008, 46, 3143-3166.	7.5	14
95	Petri Net Translation Patterns for the Analysis of eBusiness Collaboration Messaging Protocols. IEEE Transactions on Systems, Man and Cybernetics, Part A: Systems and Humans, 2009, 39, 1022-1034.	2.9	14
96	Policies for allocating product lots to customer orders in semiconductor manufacturing supply chains. Production Planning and Control, 2011, 22, 69-80.	8.8	14
97	Optimizing service times for a public health emergency using a genetic algorithm: Locating dispensing sites and allocating medical staff. IIE Transactions on Healthcare Systems Engineering, 2014, 4, 178-190.	0.8	14
98	A framework for evaluating remote diagnostics investment decisions for semiconductor equipment suppliers. European Journal of Operational Research, 2007, 180, 1411-1426.	5.7	13
99	Dynamic co-ordinated scheduling in the supply chain considering flexible routes. International Journal of Production Research, 2016, 54, 322-335.	7.5	13
100	Design for customer responsiveness: Decision support system for push-hat pull supply chains with multiple demand fulfillment points. Decision Support Systems, 2019, 123, 113071.	5.9	13
101	Bi-criteria evaluation of an outpatient procedure center via simulation. , 2007, , .		12
102	A pandemic influenza simulation model for preparedness planning. , 2009, , .		12
103	Modeling and analysis of semiconductor manufacturing in a shrinking world: Challenges and successes. , 2008, , .		11
104	Analysis of ambulance diversion policies for a large-size hospital. , 2009, , .		11
105	Machine qualification management for a semiconductor back-end facility. , 2010, , .		11
106	Availability-adjusted X-factor. International Journal of Production Research, 2005, 43, 3933-3953.	7.5	10
107	Comparing designs for computer simulation experiments. , 2008, , .		10
108	Scheduling optimization of task allocation in integrated manufacturing system based on task decomposition. Journal of Systems Engineering and Electronics, 2016, 27, 422-433.	2.2	10

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109	Collaborative Geodesign for Alternative-Fuel Station Location using "Collablocation" Software. Transportation Research Record, 2018, 2672, 98-108.	1.9	10
110	Experiments in reactive constraint logic programming1This paper is the complete version of a previous paper published in [14].1. The Journal of Logic Programming, 1998, 37, 185-212.	1.7	9
111	Evaluation of nondominated solution sets for k-objective optimization problems: An exact method and approximations. European Journal of Operational Research, 2006, 173, 565-582.	5.7	9
112	Multi-Objective Semiconductor Manufacturing Scheduling: A Random Keys Implementation of NSGA-II, 2007, , .		9
113	Single machine scheduling with interfering job sets. Computers and Operations Research, 2014, 45, 97-107.	4.0	9
114	A Decision-Making Framework for Project Portfolio Planning at Intel Corporation. Interfaces, 2015, 45, 391-408.	1.5	9
115	Decision Assessment Algorithms for Location and Capacity Optimization under Resource Shortages. Decision Sciences, 2021, 52, 142-181.	4.5	9
116	Scheduling and Simulation. , 2006, , 109-133.		9
117	Run-to-run control charts with contrasts. Quality and Reliability Engineering International, 1998, 14, 261-272.	2.3	8
118	Bi-criteria analysis of ambulance diversion policies. , 2010, , .		8
119	A genetic algorithm approach to manage ion implantation processes in wafer fabrication. International Journal of Manufacturing Technology and Management, 2000, 1, 156.	0.1	7
120	Partition experimental designs for sequential processes: Part I?First-order models. Quality and Reliability Engineering International, 2001, 17, 429-438.	2.3	7
121	Evaluating solution sets of a posteriori solution techniques for bi-criteria combinatorial optimization problems. Journal of Scheduling, 2005, 8, 75-96.	1.9	7
122	Editorial e-Manufacturing in the Semiconductor Industry. IEEE Transactions on Automation Science and Engineering, 2007, 4, 485-487.	5.2	7
123	A control chart based approach to monitoring supply network dynamics using Kalman filtering. International Journal of Production Research, 2012, 50, 3137-3151.	7.5	7
124	Cross" Training with Imperfect Training Schemes. Production and Operations Management, 2016, 25, 1216-1231.	3.8	7
125	Simulation analysisSIMULATION ANALYSIS OF MANUFACTURING AND LOGISTICS SYSTEMS. , 2000, , 687-697.		6
126	A New Technique to Compare Algorithms for Bi-criteria Combinatorial Optimization Problems. Lecture Notes in Economics and Mathematical Systems, 2001, , 113-123.	0.3	6

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127	Determining operating criterion of batch processing operations for wafer fabrication. Journal of Manufacturing Systems, 2002, 21, 363-379.	13.9	6
128	Balanced Machine Workload Dispatching Scheme for Wafer Fab. , 2007, , .		6
129	Comparison of ambulance diversion policies via simulation. , 2012, , .		6
130	Semiconductor Manufacturing Process Description. Operations Research/ Computer Science Interfaces Series, 2013, , 11-28.	0.3	6
131	Capacity Planning and Allocation for Webâ€Based Applications. Decision Sciences, 2014, 45, 535-567.	4.5	6
132	Extending the boundaries between scheduling and dispatching: hedging and rescheduling techniques. International Journal of Production Research, 2017, 55, 3294-3307.	7.5	6
133	Modelling and analysis of semiconductor supply chains. International Journal of Production Research, 2018, 56, 4521-4523.	7.5	6
134	Integer programming-based real-time scheduler in semiconductor manufacturing. , 2009, , .		5
135	An introduction to a new journal for Healthcare Systems Engineering. IIE Transactions on Healthcare Systems Engineering, 2011, 1, 1-5.	0.8	5
136	ELECTRICITY POWER COST-AWARE SCHEDULING OF JOBS ON PARALLEL BATCH PROCESSING MACHINES. , 2018, , .		5
137	Comparison of Multiple Objective Genetic Algorithms for Parallel Machine Scheduling Problems. Lecture Notes in Computer Science, 2001, , 472-485.	1.3	5
138	A comparison study of the logic of four wafer fabrication simulators. , 1996, , .		4
139	A Sequential Stopping Rule for a Steady-State Simulation Based on Time-Series Forecasting. Simulation, 2002, 78, 643-654.	1.8	4
140	Partition experimental designs for sequential processes: Part II?second-order models. Quality and Reliability Engineering International, 2002, 18, 373-382.	2.3	4
141	An Adaptive Distributed Simulation Framework for a Server Fulfillment Supply Chain. , 2006, , .		4
142	Decision Paradigms in the Semiconductor Supply Chain: A Survey and Analysis. , 2007, , .		4
143	Simulating pandemic influenza preparedness plans for a public university: A hierarchical system dynamics approach. , 2008, , .		4
144	Combinatorics on partial word correlations. Journal of Combinatorial Theory - Series A, 2010, 117, 607-624.	0.8	4

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145	Bootstrapping-based fixed-width confidence intervals for ranking and selection. , 2010, , .		4
146	Multiple data sources fusion for enterprise quality improvement by a multilevel latent response model. IIE Transactions, 2014, 46, 512-525.	2.1	4
147	Processing time generation schemes for parallel machine scheduling problems with various correlation structures. Journal of Scheduling, 2014, 17, 569-586.	1.9	4
148	Simulation: The past 10 years and the next 10 years. , 2016, , .		4
149	A group-technology-coded literature review of semiconductor manufacturing operations publications: the MASMLAB bibliography web site. IEEE Transactions on Semiconductor Manufacturing, 1999, 12, 259-263.	1.7	3
150	THE IMPACTS OF VARIABILITY ON SCHEDULING APPROACHES FOR A PRINTED WIRING BOARD ASSEMBLY OPERATION. Journal of Electronics Manufacturing, 2002, 11, 19-31.	0.4	3
151	Determining an appropriate number of FOUPs in semiconductor wafer fabrication facilities. , 2008, , .		3
152	Framework for execution level capacity allocation decisions for Assembly - Test facilities using integrated optimization - simulation models. , 2008, , .		3
153	Production Planning Approaches. Operations Research/ Computer Science Interfaces Series, 2013, , 207-246.	0.3	3
154	Decomposition heuristic for a two-machine flow shop with batch processing. , 2014, , .		3
155	Locating Alternative Fuel Stations for Maximizing Coverage and Ensuring Sufficient Spacing: a Case Study of CNG Truck Fueling. Process Integration and Optimization for Sustainability, 2019, 3, 455-470.	2.6	3
156	Modeling and Analysis of Semiconductor Manufacturing. Simulation Foundations, Methods and Applications, 2017, , 301-313.	0.1	3
157	Integrated Scheduling of Jobs, Reticles, Machines, AMHS and ARHS in a Semiconductor Manufacturing. , 2020, , .		3
158	Lot-to-order matching for a semiconductor assembly and test facility. IIE Transactions, 1999, 31, 1103-1111.	2.1	2
159	A note on determining an optimal target by considering the dependence of holding costs and the quality characteristics. Journal of Applied Statistics, 2005, 32, 813-822.	1.3	2
160	Indirect Cycle-Time Quantile Estimation for Non-Fifo Dispatching Policies. , 2006, , .		2
161	A partition experimental design for a sequential process with a large number of variables. Quality and Reliability Engineering International, 2007, 23, 555-564.	2.3	2
162	Queuing approximations for capacity planning under common setup rules. IIE Transactions, 2020, , 1-19.	2.4	2

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163	Production-Level Artificial Intelligence Applications in Semiconductor Manufacturing. , 2021, , .		2
164	A hybrid approach for modelling and analysis of an integrated machine. Proceedings of the Institution of Mechanical Engineers, Part B: Journal of Engineering Manufacture, 2004, 218, 1577-1590.	2.4	1
165	Distributed resource allocation for healthcare systems. , 2008, , .		1
166	Dispatching Approaches. Operations Research/ Computer Science Interfaces Series, 2013, , 65-104.	0.3	1
167	Simplification of DES models of M/M/1 tandem queues by approximating WIP-dependent inter-departure times. Simulation, 2014, 90, 1188-1196.	1.8	1
168	Constraint programming approach for scheduling batch operations with incompatible job families in fab and compatible job families in backend: FA: Factory automation. , 2017, , .		1
169	Improving the process efficiency of catheterization laboratories using simulation. Health Systems, 2017, 6, 41-55.	1.2	1
170	A MULTI-METHOD SCHEDULING FRAMEWORK FOR MEDICAL STAFF. , 2018, , .		1
171	A generalized decision support framework for large-scale project portfolio decisions. Decision Sciences, 2022, 53, 1024-1047.	4.5	1
172	Modeling and Analysis Tools. Operations Research/ Computer Science Interfaces Series, 2013, , 29-64.	0.3	1
173	Heuristics for Order-Lot Pegging In Multi-Fab Settings. , 2020, , .		1
174	Predicting Cycle Time Distributions with Aggregate Modelling of Work Areas in a Real-World Wafer Fab. , 2021, , .		1
175	Panel on Simulation Modeling for Covid-19. , 2021, , .		1
176	The implementation and evaluation of a hypertext system for teaching employment-related language to hearing-impaired school leavers. Journal of Microcomputer Applications, 1995, 18, 319-325.	0.1	0
177	Computers in the semiconductor industry. Computers in Industry, 2001, 45, 1.	9.9	0
178	A three-phase simulation methodology for generating accurate and precise cycle time throughput curves. International Journal of Simulation and Process Modelling, 2005, 1, 35.	0.2	0
179	Recent developments in modelling and analysis of semiconductor manufacturing. International Journal of Production Research, 2007, 45, 485-486.	7.5	0
180	Simulation-based multi-mode resource-constrained project scheduling of semiconductor equipment installation and qualification. , 2012, , .		0

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181	State of the Practice and Future Needs for Production Planning and Control Systems. Operations Research/ Computer Science Interfaces Series, 2013, , 247-266.	0.3	0
182	Deterministic Scheduling Approaches. Operations Research/ Computer Science Interfaces Series, 2013, , 105-175.	0.3	0
183	MASM: A look back and a peek ahead. , 2015, , .		0
184	Guest Editorial Special Sectionâ€™Papers From the 2019 MASM/WSC Conference. IEEE Transactions on Semiconductor Manufacturing, 2020, 33, 493-495.	1.7	0
185	Order Release Approaches. Operations Research/ Computer Science Interfaces Series, 2013, , 177-205.	0.3	0