Gernot Heiser

List of Publications by Year
in descending order

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32	3,220	933447	1125743
papers	citations	10 h-index	13 g-index

32	32	32	1722
all docs	docs citations	times ranked	citing authors

#	Article	IF	Citations
1	seL4. , 2009, , .		1,046
2	Last-Level Cache Side-Channel Attacks are Practical. , 2015, , .		566
3	Comprehensive formal verification of an OS microkernel. ACM Transactions on Computer Systems, 2014, 32, 1-70.	0.8	270
4	A survey of microarchitectural timing attacks and countermeasures on contemporary hardware. Journal of Cryptographic Engineering, 2018, 8, 1-27.	1.8	201
5	Modeling and simulation of tunneling through ultra-thin gate dielectrics. Journal of Applied Physics, 1997, 81, 7900-7908.	2.5	196
6	CATalyst: Defeating last-level cache side channel attacks in cloud computing. , 2016, , .		196
7	Reassessment of the intrinsic carrier density in crystalline silicon in view of band-gap narrowing. Journal of Applied Physics, 2003, 93, 1598-1604.	2.5	178
8	User-Level Device Drivers: Achieved Performance. Journal of Computer Science and Technology, 2005, 20, 654-664.	1.5	89
9	Spatially resolved analysis and minimization of resistive losses in high-efficiency Si solar cells. Progress in Photovoltaics: Research and Applications, 1996, 4, 399-414.	8.1	76
10	The mungi single-address-space operating system. Software - Practice and Experience, 1998, 28, 901-928.	3.6	56
11	Timing Analysis of a Protected Operating System Kernel. , 2011, , .		52
12	Numerical quantification and minimization of perimeter losses in high-efficiency silicon solar cells. Progress in Photovoltaics: Research and Applications, 1996, 4, 355-367.	8.1	45
13	The Last Mile. , 2014, , .		43
14	Time Protection. , 2019, , .		27
15	Improving interrupt response time in a verifiable protected microkernel. , 2012, , .		21
16	Decreased emitter sheet resistivity loss in highâ€efficiency silicon solar cells. Progress in Photovoltaics: Research and Applications, 1994, 2, 3-17.	8.1	18
17	Trickle: Automated infeasible path detection using all minimal unsatisfiable subsets. , 2014, , .		18
18	No Security Without Time Protection. , 2018, , .		16

#	Article	IF	Citations
19	Scheduling-context capabilities. , 2018, , .		16
20	The mungi single-address-space operating system. , 1998, 28, 901.		16
21	Sequoll: A framework for model checking binaries. , 2013, , .		11
22	High-assurance timing analysis for a high-assurance real-time operating system. Real-Time Systems, 2017, 53, 812-853.	1.3	11
23	The Jury Is In. , 2018, , .		10
24	Can We Prove Time Protection?. , 2019, , .		9
25	For Safetyâ€™s Sake: We Need a New Hardware-Software Contract!. IEEE Design and Test, 2018, 35, 27-30.	1.2	8
26	Microarchitectural Timing Channels and their Prevention on an Open-Source 64-bit RISC-V Core. , 2021, , .		8
27	Towards Provable Timing-Channel Prevention. Operating Systems Review (ACM), 2020, 54, 1-7.	1.9	6
28	To preempt or not to preempt, that is the question. , 2012, , .		4
29	Benchmarking Flaws Undermine Security Research. IEEE Security and Privacy, 2020, 18, 48-57.	1.2	3
30	Code optimizations using formally verified properties. , 2013, , .		2
31	Numerical quantification and minimization of perimeter losses in high-efficiency silicon solar cells. , 1996, 4, 355.		1
32	Spatially resolved analysis and minimization of resistive losses in high-efficiency Si solar cells. , 1996, 4, 399.		1