

# Yu-Che Chou

## List of Publications by Year in descending order

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2682572

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#	ARTICLE	IF	CITATIONS
1	Neuro-Inspired-in-Memory Computing Using Charge-Trapping MemTransistor on Germanium as Synaptic Device. IEEE Transactions on Electron Devices, 2020, 67, 3605-3609.	3.0	7
2	High-Accuracy Deep Neural Networks Using a Contralateral-Gated Analog Synapse Composed of Ultrathin MoS <sub>2</sub> , nFET and Nonvolatile Charge-Trap Memory. IEEE Electron Device Letters, 2020, 41, 1649-1652.	3.9	3
3	Integration of hetero-structure body-tied Ge FinFET using retrograde-well implantation. , 2016, , .		1
4	Impact of the Crystal Phase of ZrO <sub>2</sub> , on Charge Trapping Memtransistor as Synaptic Device for Neural Network Application. IEEE Journal of the Electron Devices Society, 2020, 8, 572-576.	2.1	1
5	Improving the Performance of Charge Trapping Memtransistor as Synaptic Device by Ti-Doped HfO <sub>2</sub> . IEEE Journal of the Electron Devices Society, 2021, 9, 137-143.	2.1	0