Alak Datta

List of Publications by Year in descending order

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2258059 2053705 14 45 3 5 citations h-index g-index papers 15 15 15 37 citing authors all docs docs citations times ranked

| # | Article | IF | CITATIONS |
|----|--|-----|-----------|
| 1 | 1-Approximation algorithm for bottleneck disjoint path matching. Information Processing Letters, 1995, 55, 41-44. | 0.6 | 10 |
| 2 | A graph based algorithm to minimize total wire length in VLSI channel routing. , $2011,$, . | | 6 |
| 3 | Algorithms for high performance two-layer channel routing. , 2007, , . | | 5 |
| 4 | Approximate spanning cactus. Information Processing Letters, 2015, 115, 828-832. | 0.6 | 4 |
| 5 | Spanning cactus: Complexity and extensions. Discrete Applied Mathematics, 2017, 233, 19-28. | 0.9 | 4 |
| 6 | <inline-formula> <tex-math notation="LaTeX">\$K\$</tex-math> </inline-formula> -Terminal Reliability of <inline-formula> <tex-math notation="LaTeX">\$d\$</tex-math> </inline-formula> -Trapezoid Graphs. IEEE Transactions on Reliability, 2016, 65, 1240-1247. | 4.6 | 3 |
| 7 | An efficient scheme to solve two problems for two-terminal series parallel graphs. Information Processing Letters, 1999, 71, 9-15. | 0.6 | 2 |
| 8 | o(log4 n) time parallel maximal matching algorithm using linear number of processors. International Journal of Parallel, Emergent and Distributed Systems, 2004, 19, 19-32. | 0.4 | 2 |
| 9 | Hardness of crosstalk minimization in two-layer channel routing. The Integration VLSI Journal, 2017, 56, 139-147. | 2.1 | 2 |
| 10 | An efficient parallel algorithm for maximal matching. Lecture Notes in Computer Science, 1992 , , $813-814$. | 1.3 | 2 |
| 11 | A PARALLEL ALGORITHM FOR MAXIMAL MATCHING BASED ON DEPTH FIRST SEARCH. International Journal of Parallel, Emergent and Distributed Systems, 1995, 5, 161-164. | 0.4 | 1 |
| 12 | IMPROVED PARALLEL ALGORITHM FOR MAXIMAL MATCHING BASED ON DEPTH-FIRST-SEARCH. International Journal of Parallel, Emergent and Distributed Systems, 2000, 14, 321-327. | 0.4 | 1 |
| 13 | Generation of random channel specifications for channel routing problem. , 2008, , . | | 0 |
| 14 | Yet an efficient algorithm for computing reduced area VLSI channel routing solutions with floating terminals. , $2011, \dots$ | | 0 |