

# Mridula Gupta

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/5743242/publications.pdf>

Version: 2024-02-01

120  
papers

1,865  
citations

279701

23  
h-index

330025

37  
g-index

121  
all docs

121  
docs citations

121  
times ranked

752  
citing authors

#	ARTICLE	IF	CITATIONS
1	An Investigation of Linearity Performance and Intermodulation Distortion of GME CGT MOSFET for RFIC Design. IEEE Transactions on Electron Devices, 2012, 59, 3263-3268.	1.6	137
2	Investigation of dielectric modulated (DM) double gate (DG) junctionless MOSFETs for application as a biosensors. Superlattices and Microstructures, 2015, 85, 557-572.	1.4	78
3	Impact of gate material engineering(GME) on analog/RF performance of nanowire Schottky-barrier gate all around (GAA) MOSFET for low power wireless applications: 3D T-CAD simulation. Microelectronics Journal, 2014, 45, 1508-1514.	1.1	73
4	TCAD Assessment of Device Design Technologies for Enhanced Performance of Nanoscale DG MOSFET. IEEE Transactions on Electron Devices, 2011, 58, 2936-2943.	1.6	72
5	An analytical drain current model for dual material engineered cylindrical/surrounded gate MOSFET. Microelectronics Journal, 2012, 43, 17-24.	1.1	71
6	Performance Evaluation and Reliability Issues of Junctionless CSG MOSFET for RFIC Design. IEEE Transactions on Device and Materials Reliability, 2014, 14, 418-425.	1.5	71
7	Analytical Model of pH sensing Characteristics of Junctionless Silicon on Insulator ISFET. IEEE Transactions on Electron Devices, 2017, 64, 1742-1750.	1.6	58
8	Gate-All-Around Nanowire MOSFET With Catalytic Metal Gate For Gas Sensing Applications. IEEE Nanotechnology Magazine, 2013, 12, 939-944.	1.1	52
9	Numerical Model of Gate-All-Around MOSFET With Vacuum Gate Dielectric for Biomolecule Detection. IEEE Electron Device Letters, 2012, 33, 1756-1758.	2.2	50
10	Analytical modeling of gate-all-around junctionless transistor based biosensors for detection of neutral biomolecule species. Journal of Computational Electronics, 2018, 17, 288-296.	1.3	50
11	Effect of localised charges on nanoscale cylindrical surrounding gate MOSFET: Analog performance and linearity analysis. Microelectronics Reliability, 2012, 52, 989-994.	0.9	49
12	Gate All Around MOSFET With Vacuum Gate Dielectric for Improved Hot Carrier Reliability and RF Performance. IEEE Transactions on Electron Devices, 2013, 60, 1820-1827.	1.6	48
13	Modeling and Simulation of AlGaIn/GaN MOS-HEMT for Biosensor Applications. IEEE Sensors Journal, 2019, 19, 587-593.	2.4	45
14	An analytical subthreshold current modeling of cylindrical gate all around (CGAA) MOSFET incorporating the influence of device design engineering. Microelectronics Journal, 2014, 45, 408-415.	1.1	40
15	Analytical modeling and simulation of subthreshold behavior in nanoscale dual material gate AlGaIn/GaN HEMT. Superlattices and Microstructures, 2008, 44, 37-53.	1.4	35
16	Modeling of gate underlap junctionless double gate MOSFET as bio-sensor. Materials Science in Semiconductor Processing, 2017, 71, 240-251.	1.9	35
17	Two-Dimensional Analytical Drain Current Model for Double-Gate MOSFET Incorporating Dielectric Pocket. IEEE Transactions on Electron Devices, 2012, 59, 2567-2574.	1.6	31
18	Analytical modeling of Junctionless Accumulation Mode Cylindrical Surrounding Gate MOSFET (JAM-ACSG). International Journal of Numerical Modelling: Electronic Networks, Devices and Fields, 2016, 29, 1036-1043.	1.2	28

#	ARTICLE	IF	CITATIONS
19	Physics based analytical model for surface potential and subthreshold current of cylindrical Schottky Barrier gate all around MOSFET with high-k gate stack. Superlattices and Microstructures, 2016, 90, 215-226.	1.4	28
20	Modeling and simulation of Double Gate Junctionless Transistor considering fringing field effects. Solid-State Electronics, 2015, 107, 20-29.	0.8	27
21	Model of GaSb-InAs p-i-n Gate All Around BioTunnel FET. IEEE Sensors Journal, 2019, 19, 2605-2612.	2.4	24
22	Analytical Modeling and Simulation for Dual Metal Gate Stack Architecture (DMGSA) Cylindrical/Surrounded Gate MOSFET. Journal of Semiconductor Technology and Science, 2012, 12, 458-466.	0.1	24
23	Empirical Model for Nonuniformly Doped Symmetric Double-Gate Junctionless Transistor. IEEE Transactions on Electron Devices, 2018, 65, 314-321.	1.6	23
24	An analysis for AlGaIn/GaN modulation doped field effect transistor using accurate velocity-field dependence for high power microwave frequency applications. Microelectronics Journal, 2006, 37, 1339-1346.	1.1	22
25	Localized Charge-Dependent Threshold Voltage Analysis of Gate-Material-Engineered Junctionless Nanowire Transistor. IEEE Transactions on Electron Devices, 2015, 62, 2598-2605.	1.6	20
26	Charge plasma technique based dopingless accumulation mode junctionless cylindrical surrounding gate MOSFET: analog performance improvement. Applied Physics A: Materials Science and Processing, 2017, 123, 1.	1.1	20
27	Hot-Carrier Reliability of Gate-All-Around MOSFET for RF/Microwave Applications. IEEE Transactions on Device and Materials Reliability, 2013, 13, 245-251.	1.5	19
28	Modeling and Simulation of Junctionless Double Gate Radiation Sensitive FET (RADFET) Dosimeter. IEEE Nanotechnology Magazine, 2018, 17, 49-55.	1.1	19
29	Two Dimensional Analytical Subthreshold Model of Nanoscale Cylindrical Surrounding Gate MOSFET Including Impact of Localised Charges. Journal of Computational and Theoretical Nanoscience, 2012, 9, 602-610.	0.4	18
30	An Accurate Charge-Control-Based Approach for Noise Performance Assessment of a Symmetric Tied-Gate InAlAs/InGaAs DG-HEMT. IEEE Transactions on Electron Devices, 2012, 59, 1644-1652.	1.6	17
31	Performance Investigation of Novel Pt/Pd-SiO <sub>2</sub> Junctionless FinFET as a High Sensitive Hydrogen Gas Sensor for Industrial Applications. IEEE Sensors Journal, 2021, 21, 13356-13363.	2.4	17
32	Carrier-concentration-dependent low-field-mobility model for InAlAs/InGaAs/InP lattice-matched HEMT for microwave application. Microwave and Optical Technology Letters, 2001, 29, 66-70.	0.9	16
33	Temperature dependent drain current model for Gate Stack Insulated Shallow Extension Silicon On Nothing (ISESON) MOSFET for wide operating temperature range. Microelectronics Reliability, 2012, 52, 974-983.	0.9	16
34	A new T-Shaped Source/Drain Extension (T-SSDE) Gate Underlap GAA MOSFET with enhanced subthreshold analog/RF performance for low power applications. Solid-State Electronics, 2014, 101, 13-17.	0.8	16
35	Gate-Material-Engineered Junctionless Nanowire Transistor (JNT) With Vacuum Gate Dielectric for Enhanced Hot-Carrier Reliability. IEEE Transactions on Device and Materials Reliability, 2016, 16, 360-369.	1.5	16
36	Investigation of total ionizing dose effect on SOI tunnel FET. Superlattices and Microstructures, 2019, 133, 106186.	1.4	16

#	ARTICLE	IF	CITATIONS
37	Analytical Model of Double Gate MOSFET for High Sensitivity Low Power Photosensor. Journal of Semiconductor Technology and Science, 2013, 13, 500-510.	0.1	16
38	Study of Gaussian Doped Double Gate JunctionLess (GD-DG-JL) transistor including source drain depletion length: Model for sub-threshold behavior. Superlattices and Microstructures, 2018, 113, 57-70.	1.4	15
39	An Accurate Small Signal Modeling of Cylindrical/Surrounded Gate MOSFET for High Frequency Applications. Journal of Semiconductor Technology and Science, 2012, 12, 377-387.	0.1	14
40	Analytical model of threshold voltage degradation due to localized charges in gate material engineered Schottky barrier cylindrical GAA MOSFETs. Semiconductor Science and Technology, 2016, 31, 105013.	1.0	14
41	Novel junctionless electrolyte-insulator-semiconductor field-effect transistor (JL EISFET) and its application as pH/biosensor. Microsystem Technologies, 2017, 23, 3149-3159.	1.2	14
42	Exploring the applicability of well optimized dielectric pocket tunnel transistor for future low power applications. Superlattices and Microstructures, 2019, 126, 8-16.	1.4	14
43	TCAD-Based Investigation of Double Gate JunctionLess Transistor for UV Photodetector. IEEE Transactions on Electron Devices, 2021, 68, 2841-2847.	1.6	14
44	Temperature dependent subthreshold model of long channel GAA MOSFET including localized charges to study variations in its temperature sensitivity. Microelectronics Reliability, 2014, 54, 37-43.	0.9	13
45	Analytical modeling simulation and characterization of short channel Junctionless Accumulation Mode Surrounding Gate (JLAMSG) MOSFET for improved analog/RF performance. Superlattices and Microstructures, 2016, 100, 1263-1275.	1.4	13
46	Scattering parameter based modeling and simulation of symmetric tied-gate InAlAs/InGaAs DG-HEMT for millimeter-wave applications. Solid-State Electronics, 2011, 63, 149-153.	0.8	12
47	Optimization of " Gate AlGaIn/AlN/GaN HEMTs for Low Noise and High Gain Applications. Silicon, 2022, 14, 393-404.	1.8	12
48	Analysis of gate underlap channel double gate MOS transistor for electrical detection of bio-molecules. Superlattices and Microstructures, 2015, 88, 225-243.	1.4	11
49	Modeling and simulation of cylindrical surrounding double-gate (CSDG) MOSFET with vacuum gate dielectric for improved hot-carrier reliability and RF performance. Journal of Computational Electronics, 2016, 15, 657-665.	1.3	11
50	Open gate AlGaIn/GaN HEMT biosensor: Sensitivity analysis and optimization. Superlattices and Microstructures, 2021, 156, 106968.	1.4	11
51	Performance Analysis and Optimization of Under-Gate Dielectric Modulated Junctionless FinFET Biosensor. IEEE Sensors Journal, 2021, 21, 18897-18904.	2.4	11
52	TCAD Based Investigation of Single Event Transient Effect in Double Channel AlGaIn/GaN HEMT. IEEE Transactions on Device and Materials Reliability, 2021, 21, 416-423.	1.5	11
53	Investigation of Dielectric-Modulated Double-Gate Junctionless MOSFET for detection of biomolecules. , 2013, , .		10
54	Impact of different localized trap charge profiles on the short channel double gate junctionless nanowire transistor based inverter and Ring Oscillator circuit. AEU - International Journal of Electronics and Communications, 2019, 108, 251-261.	1.7	10

#	ARTICLE	IF	CITATIONS
55	TCAD-Based Optimization of Field Plate Length & Passivation Layer of AlGaN/GaN HEMT for Higher Cut-Off Frequency & Breakdown Voltage. IETE Technical Review (Institution of Electronics and) Tj ETQq1 1 0.784314 rgBT /Overlock	1.0	10
56	Comparative Analysis of Junctionless FinFET and Inverted Mode FinFET as Phosphine (PH <sub>3</sub> ) Gas Sensor. , 2020, , .		10
57	Reliability Assessment of GaAs/Al <sub>x</sub> O <sub>y</sub> f Junctionless FinFET in the Presence of Interfacial Layer Defects and Radiations. IEEE Transactions on Device and Materials Reliability, 2020, 20, 452-458.	1.5	10
58	Proton irradiation effects on buffer-free gallium nitride on silicon carbide high electron mobility transistor-based radio frequency power amplifier. Semiconductor Science and Technology, 2021, 36, 045019.	1.0	10
59	Impact of doping concentration and donor-layer thickness on the dc characterization of symmetric double-gate and single-gate InAlAs/InGaAs/InP HEMT for nanometer gate dimension-A comparison. , 2010, , .		9
60	Numerical analysis of localised charges impact on static and dynamic performance of nanoscale cylindrical surrounding gate MOSFET based CMOS inverter. Microelectronics Reliability, 2013, 53, 236-244.	0.9	9
61	Physics-based drain current modeling of gate-all-around junctionless nanowire twin-gate transistor (JN-TGT) for digital applications. Journal of Computational Electronics, 2016, 15, 492-501.	1.3	9
62	Cylindrical gate all around Schottky barrier MOSFET with insulated shallow extensions at source/drain for removal of ambipolarity: a novel approach. Journal of Semiconductors, 2017, 38, 124002.	2.0	9
63	Investigation of Single Event Transient Effects in Junctionless Accumulation Mode MOSFET. IEEE Transactions on Device and Materials Reliability, 2020, 20, 604-608.	1.5	9
64	Temperature-Dependent Analytical Model for Microwave and Noise Performance Characterization of $\text{In}_{0.52}\text{Al}_{0.48}\text{As/In}_m\text{Ga}_{1-m}\text{As}$ ( $0.53 \leq m \leq 1$ ) Tj ETQq0 0.0 rgBT /Overlock 10	1.0	9
65	Sensitivity investigation of gate-all-around junctionless transistor for hydrogen gas detection. , 2016, , .		8
66	Ambipolarity reduction in DMG asymmetric vacuum dielectric Schottky Barrier GAA MOSFET to improve hot carrier reliability. Superlattices and Microstructures, 2017, 111, 10-22.	1.4	8
67	A comparative study on the accuracy of small-signal equivalent circuit modeling for large gate periphery GaN HEMT with different source to drain length and gate width. Microelectronics Journal, 2021, 118, 105258.	1.1	8
68	Optimization of Asymmetric $\Gamma$ -Gate HEMT for Improved Reliability & Frequency Applications. , 2019, , .		7
69	Comparative Analysis of Dielectric Modulated Junctionless FinFET Biosensor and Junctionless DG MOSFET Biosensor for Medical Instrumentation. , 2019, , .		7
70	Modeling and Simulation-Based Investigation of 2-D Symmetric Double Gate Dopingless-TFET and Its Circuit Performance for Low-Power Applications. IETE Technical Review (Institution of Electronics) Tj ETQq0 0 0 rgBT /Overlock 10 Tf 50	1.0	7
71	Analytical Modeling and Simulation of AlGaIn/GaN MOS-HEMT for High Sensitive pH Sensor. IEEE Sensors Journal, 2021, 21, 12998-13005.	2.4	7
72	An improved intrinsic small-signal equivalent circuit model of delta-doped AlGaAs/InGaAs/GaAs HEMT for microwave frequency applications. Microwave and Optical Technology Letters, 2003, 37, 376-379.	0.9	6

#	ARTICLE	IF	CITATIONS
73	Capacitance modeling of gate material engineered cylindrical/surrounded gate MOSFETs for sensor applications. Superlattices and Microstructures, 2015, 88, 271-280.	1.4	6
74	Underlapped FinFET on insulator: Quasi3D analytical model. Solid-State Electronics, 2017, 129, 138-149.	0.8	6
75	Two-dimensional (2D) analytical investigation of an n-type junctionless gate-all-around tunnel field-effect transistor (JL GAA TFET). Journal of Computational Electronics, 2018, 17, 713-723.	1.3	6
76	Comparative study of InGaN and InGaAs based dopingless TFET with different gate engineering techniques. Advances in Natural Sciences: Nanoscience and Nanotechnology, 2019, 10, 035009.	0.7	6
77	Assessment of analog RF performance for insulated shallow extension (ISE) cylindrical surrounding gate (CSC) MOSFET incorporating gate stack. Microsystem Technologies, 2019, 25, 1547-1554.	1.2	6
78	Impact of heavy ion particle strike induced single event transients on conventional and " Gate AlGaIn/GaN HEMTs. Semiconductor Science and Technology, 0, , .	1.0	5
79	Total ionizing dose effects in junctionless accumulation mode MOSFET. Applied Physics A: Materials Science and Processing, 2021, 127, 1.	1.1	5
80	Analytical modeling of a split-gate dielectric modulated metal-oxide-semiconductor field-effect transistor for application as a biosensor. , 2014, , .		4
81	Comparative Study of CMOS based Dosimeters for Gamma Radiation. , 2018, , .		4
82	Investigation of proton irradiated dual field plate AlGaIn/GaN HEMTs: TCAD based assessment. Microelectronics Journal, 2022, 122, 105405.	1.1	4
83	Influence of gate leakage current induced shot noise on the Minimum Noise Figure of InAlAs/InGaAs double-gate HEMT. Superlattices and Microstructures, 2017, 109, 13-22.	1.4	3
84	Simulation Based Breakdown Voltage Analysis Of 3-Step Field Plate AlGaIn/GaN HEMTs. , 2018, , .		3
85	Optimized DL-TFET Design for Enhancing its Performance Parameters by Using Different Engineering Methods. IETE Technical Review (Institution of Electronics and Telecommunication Engineers, India), 2021, 38, 429-437.	2.1	3
86	Degradation Mechanisms in a Proton Irradiated HEMT with 3DEG Conduction and 3DHG as a Back Barrier. , 2021, , .		3
87	Differential multiresonator stacked microstrip antenna for wireless energy harvesting. International Journal of RF and Microwave Computer-Aided Engineering, 2021, 31, e22828.	0.8	3
88	3-dimensional analytical modeling and simulation of fully depleted AlGaIn/GaN modulation doped field effect transistor. , 2007, , .		2
89	RF performance analysis and small signal parameter extraction of Cylindrical Surrounding Double Gate MOSFETs for sub-millimeter wave applications. , 2014, , .		2
90	Design and analysis of spiral circular inductors for GaN based low noise amplifier (MMICs). , 2015, , .		2

#	ARTICLE	IF	CITATIONS
91	Investigation of Analog/RF performance of High-k spacer Junctionless Accumulation-Mode Cylindrical Gate All Around (JLAM-CGAA) MOSFET. , 2016, , .		2
92	Impact of positions of sensing area in a channel of dielectric modulated MOSFET based biosensor. Integrated Ferroelectrics, 2018, 194, 63-71.	0.3	2
93	Optimization of Gate Oxide of Dual-Gate MISHEMTs for Enhanced DC performance. , 2018, , .		2
94	TCAD-Based Assessment of Dual-Gate MISHEMT with Sapphire, SiC, and Silicon Substrate. IETE Technical Review (Institution of Electronics and Telecommunication Engineers, India), 2021, 38, 197-205.	2.1	2
95	Gate stacked dual-gate MISHEMT with 39 THz/V Johnson's figure of merit for V-band applications. Journal of Computational Electronics, 2021, 20, 556-567.	1.3	2
96	Impact of Non-Uniform Doping on the Reliability of Double Gate Junctionless Transistor: A Numerical Investigation. IETE Technical Review (Institution of Electronics and Telecommunication Engineers,) Tj ETQq0 0 0 rg21/Overlook 10 Tf 50		
97	Dielectric Separated Independent Gates Junctionless Transistor (DSIG-JLT) For Highly Scaled Digital Logic Implementation. IEEE Nanotechnology Magazine, 2021, 20, 262-269.	1.1	2
98	Variability Investigation of Double Gate Junctionless (DG-JL) Transistor for Circuit Design Perspective. Communications in Computer and Information Science, 2017, , 496-503.	0.4	2
99	Performance Investigation of Insulated Shallow Extension Silicon On Nothing (ISE-SON) MOSFET for Low Voltage Digital Applications. Journal of Semiconductor Technology and Science, 2013, 13, 622-634.	0.1	2
100	Optimization of Multimode Fibers for Surface Plasmon Resonance Based Sensors Under Spectral and Single Wavelength Intensity Interrogation. Plasmonics, 2022, 17, 665-673.	1.8	2
101	Undoped Drain Graded Doping (UDGD) based TFET design: An innovative concept. Superlattices and Microstructures, 2022, 163, 107147.	1.4	2
102	Interplay Between $\gamma$ -Ray Irradiation and 3DEG for Dosimeter Applications. IEEE Access, 2022, 10, 25811-25827.	2.6	2
103	Material engineering in Cylindrical Surrounding Double Gate (CSDG) MOSFETs for enhanced electrostatic integrity and RF performance. , 2014, , .		1
104	Nanoscale T-shaped Double Gate DG MOSFET: Numerical Investigation for Analog/RF and Digital Performance. Superlattices and Microstructures, 2016, 89, 97-111.	1.4	1
105	Design and analysis of high performance air-bridge spiral circular inductors for GaN MMICs up to ku band. , 2017, , .		1
106	Breakdown Voltage Analysis of Different Field Plate AlGaN/GaN HEMTs: TCAD based Assessment. , 2018, , .		1
107	Sub-Threshold Drain Current Model of Shell-Core Architecture Double Gate Junctionless Transistor. , 2018, , .		1
108	Interface trap-dependent linearity assessment in single and dual metal gate junctionless accumulation mode (surrounding gate) nanowire MOSFET. Applied Physics A: Materials Science and Processing, 2019, 125, 1.	1.1	1



#	ARTICLE	IF	CITATIONS
109	Differential microstrip patch rectenna featuring consistent high gain over a wide operating bandwidth. Microwave and Optical Technology Letters, 2021, 63, 1470-1476.	0.9	1
110	Performance evaluation of dielectric modulation and metalloid T-shaped source/drain on gate-all-around junctionless transistor for improved analog/RF application. Journal of Materials Science: Materials in Electronics, 2021, 32, 10943-10950.	1.1	1
111	Gate-to-Drain Capacitance Dependent Model for Noise Performance Evaluation of InAlAs/InGaAs Double-gate HEMT. Journal of Semiconductor Technology and Science, 2013, 13, 331-341.	0.1	1
112	Simulation Study of Stack Gate Insulated Shallow Extension Silicon On Nothing ISE-SON MOSFET for RFICs Design. , 2011, , .		0
113	A comprehensive charge control based analysis of the effect of donor-layer doping and donor-layer thickness on the P, R and C noise coefficients of a symmetric tied-gate InAlAs/InGaAs DG-HEMT. Proceedings of SPIE, 2012, , .	0.8	0
114	Laterally-asymmetric-channel-insulated-shallow-extension-silicon-on-nothing LAC-ISE-SON MOSFET for improved reliability and digital circuit simulation. , 2012, , .		0
115	Analytical Modeling and Simulation Based Investigation of Advanced TFET Architecture. , 2016, , .		0
116	Analytical model of junctionless double gate radiation sensitive FET (RADFET) dosimeter. , 2016, , .		0
117	Study of Extended Back Gate Double Gate JunctionLess Transistor: Theoretical and Numerical Investigation. Springer Proceedings in Physics, 2019, , 633-642.	0.1	0
118	Equivalent Channel Temperature in GaN HEMT with Field Plate. , 2020, , .		0
119	An Asymmetric $\gamma$ -Gate MOSHEMT Architecture for High Frequency Applications. Lecture Notes in Networks and Systems, 2021, , 453-458.	0.5	0
120	TCAD Investigation for Dual-Gate MISHEMT with Improved Linearity and Current Collapse for LNAs. IETE Technical Review (Institution of Electronics and Telecommunication Engineers, India), 0, , 1-12.	2.1	0