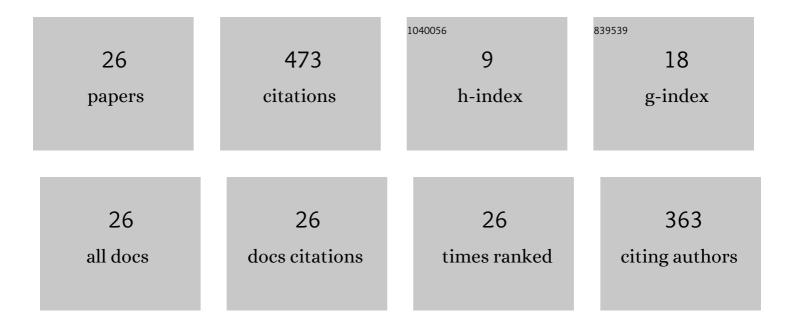
Gupta Shubhrata

List of Publications by Year in descending order

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#	Article	IF	CITATIONS
1	A Deep Attention based U-Net for Airways Segmentation in Computed Tomography Images. Current Medical Imaging, 2022, 18, .	0.8	0
2	A novel two-terminal fault location approach utilizing traveling-waves for series compensated line connected to wind farms. Electric Power Systems Research, 2021, 198, 107362.	3.6	11
3	An improved symbol reduction technique based Huffman coder for efficient entropy coding in the transform coders. IET Image Processing, 2021, 15, 1008-1022.	2.5	1
4	Open-Switch Fault Tolerance Capabilities of Some Reduced Device Count Multilevel Inverter Topologies. Iranian Journal of Science and Technology - Transactions of Electrical Engineering, 2020, 44, 253-264.	2.3	1
5	Intrinsic time decomposition based fault location scheme for <scp>unified power flow controller</scp> compensated transmission line. International Transactions on Electrical Energy Systems, 2020, 30, e12585.	1.9	9
6	Imageâ€independent optimal nonâ€negative integer bit allocation technique for the DCTâ€based image transform coders. IET Image Processing, 2020, 14, 11-24.	2.5	3
7	Resilient multilevel inverter topology with improved reliability. IET Power Electronics, 2020, 13, 3384-3395.	2.1	14
8	Empirical Mode Decomposition Assisted Fault Localization for UPFC Compensated System. , 2020, , .		4
9	Automated Localized Approach for Airway Segmentation in 3D Chest CT Volume. Biomedical and Pharmacology Journal, 2020, 13, 1671-1682.	0.5	0
10	Modified GSA for minimisation of THD of voltage source multilevel inverter. International Journal of Power Electronics, 2020, 12, 191.	0.2	0
11	Reliability and energy sharing analysis of a faultâ€ŧolerant multilevel inverter topology. IET Power Electronics, 2019, 12, 759-768.	2.1	12
12	Improved Optimum Nonnegative Integer Bit Allocation Algorithm Using Fuzzy Domain Variance Estimation and Refinement for the Wavelet-Based Image Compression. Circuits, Systems, and Signal Processing, 2019, 38, 3880-3900.	2.0	0
13	Approach to synthesis of fault tolerant reduced device count multilevel inverters (FT RDC MLIs). IET Power Electronics, 2019, 12, 476-482.	2.1	25
14	Fault-tolerant operation of some reduced-device-count multilevel inverters with improved performance. International Transactions on Electrical Energy Systems, 2019, 29, e2731.	1.9	6
15	Singleâ€phase multilevel inverter topologies with selfâ€voltage balancing capabilities. IET Power Electronics, 2018, 11, 844-855.	2.1	76
16	Reliability Analysis of a Novel Fault Tolerant Multilevel Inverter Topology. , 2018, , .		3
17	Fault Tolerant Analysis of Single Phase Multilevel Inverter. , 2018, , .		4

Fault Tolerance and Energy Sharing Analysis of a Single Phase Multilevel Inverter Topology., 2018,,.

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#	Article	IF	CITATIONS
19	Development of faultâ€tolerant MLI topology. IET Power Electronics, 2018, 11, 1416-1424.	2.1	38
20	Fuel cell power conditioning unit for standalone application with real time validation. International Journal of Hydrogen Energy, 2018, 43, 14629-14637.	7.1	5
21	Dense traffic flow patterns mining in bi-directional road networks using density based trajectory clustering. Advances in Data Analysis and Classification, 2017, 11, 547-561.	1.4	9
22	Reliability improvement of transistor clamped Hâ€bridgeâ€based cascaded multilevel inverter. IET Power Electronics, 2017, 10, 770-781.	2.1	34
23	Hybrid WPTâ€BDCT transform for highâ€quality image compression. IET Image Processing, 2017, 11, 899-909.	2.5	8
24	Reduction in number of devices for symmetrical and asymmetrical multilevel inverters. IET Power Electronics, 2016, 9, 698-709.	2.1	100
25	A Single-Phase Cell-Based Asymmetrical Cascaded Multilevel Inverter. Journal of Power Electronics, 2016, 16, 532-541.	1.5	8
26	Hybrid topology of symmetrical multilevel inverter using less number of devices. IET Power Electronics, 2015, 8, 2125-2135.	2.1	99