

Orazio Aiello

List of Publications by Year in descending order

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38
papers

521
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567281

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docs citations

38
times ranked

264
citing authors

#	ARTICLE	IF	CITATIONS
1	A pW-Power Hz-Range Oscillator Operating With a 0.3â€“1.8-V Unregulated Supply. IEEE Journal of Solid-State Circuits, 2019, 54, 1487-1496.	5.4	38
2	Fully Digital Rail-to-Rail OTA With Sub-1000- μm^2 Area, 250-mV Minimum Supply, and nW Power at 150-pF Load in 180 nm. IEEE Solid-State Circuits Letters, 2020, 3, 474-477.	2.0	35
3	Fully Synthesizable Low-Area Analogue-to-Digital Converters With Minimal Design Effort Based on the Dyadic Digital Pulse Modulation. IEEE Access, 2020, 8, 70890-70899.	4.2	35
4	Fully Synthesizable Low-Area Digital-to-Analog Converter With Graceful Degradation and Dynamic Power-Resolution Scaling. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 2865-2875.	5.4	31
5	Standard Cell-Based Ultra-Compact DACs in 40-nm CMOS. IEEE Access, 2019, 7, 126479-126488.	4.2	30
6	Ultra-Low-Voltage Inverter-Based Operational Transconductance Amplifiers with Voltage Gain Enhancement by Improved Composite Transistors. Electronics (Switzerland), 2020, 9, 1410.	3.1	30
7	Electromagnetic Susceptibility of Battery Management Systemsâ€™ ICs for Electric Vehicles: Experimental Study. Electronics (Switzerland), 2020, 9, 510.	3.1	30
8	Design of Digital OTAs With Operation Down to 0.3 V and nW Power for Direct Harvesting. IEEE Transactions on Circuits and Systems I: Regular Papers, 2021, 68, 3693-3706.	5.4	29
9	breaking the boundaries between analogue and digital. Electronics Letters, 2019, 55, 672-673.	1.0	23
10	Hall-Effect Current Sensors Susceptibility to EMI: Experimental Study. Electronics (Switzerland), 2019, 8, 1310.	3.1	21
11	Self-Biased and Supply-Voltage Scalable Inverter-Based Operational Transconductance Amplifier with Improved Composite Transistors. Electronics (Switzerland), 2021, 10, 935.	3.1	21
12	A new MagFET-based integrated current sensor highly immune to EMI. Microelectronics Reliability, 2013, 53, 573-581.	1.7	19
13	Fully Synthesizable, Rail-to-Rail Dynamic Voltage Comparator for Operation down to 0.3 V. , , .		19
14	A New Mirroring Circuit for Power MOS Current Sensing Highly Immune to EMI. Sensors, 2013, 13, 1856-1871.	3.8	17
15	Susceptibility to EMI of a Battery Management System IC for electric vehicles. , 2015, , .		17
16	Rail-to-Rail Dynamic Voltage Comparator Scalable Down to pW-Range Power and 0.15-V Supply. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 2675-2679.	3.0	17
17	A 300mV-Supply, Sub-nW-Power Digital-Based Operational Transconductance Amplifier. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 3073-3077.	3.0	16
18	On the Susceptibility of Embedded Thermal Shutdown Circuit to Radio Frequency Interference. IEEE Transactions on Electromagnetic Compatibility, 2012, 54, 405-412.	2.2	14

#	ARTICLE	IF	CITATIONS
19	A 1.9 nW, Sub-1 V, 542 pA/V Linear Bulk-Driven OTA with 154 dB CMRR for Bio-Sensing Applications. Journal of Low Power Electronics and Applications, 2021, 11, 40.	2.0	10
20	A Sub-Leakage PW-Power HZ-Range Relaxation Oscillator Operating with 0.3V-1.8V Unregulated Supply. , 2018, , .		9
21	A 300mV-Supply Standard-Cell-Based OTA with Digital PWM Offset Calibration. , 2019, , .		9
22	Characterization of the Susceptibility to EMI of a BMS IC for Electric Vehicles by Direct Power and Bulk Current Injection. IEEE Letters on EMC Practice and Applications, 2021, 3, 101-107.	1.1	9
23	Design of Relaxation Digital-to-Analog Converters for Internet of Things Applications in 40nm CMOS. , 2019, , .		8
24	Wake-Up Oscillators with pW Power Consumption in Dynamic Leakage Suppression Logic. , 2019, , .		7
25	Fully-Synthesizable Current-Input ADCs for Ultra-Low Area and Minimal Design Effort. , 2019, , .		5
26	On the DC Offset Current Generated during Biphasic Stimulation: Experimental Study. Electronics (Switzerland), 2020, 9, 1198.	3.1	5
27	Design of a neural recording amplifier robust to EMI. , 2013, , .		3
28	Minimum-Effort Design of Ultra-Low Power Interfaces for the Internet of Things. , 2019, , .		3
29	Ultra-Low Power and Minimal Design Effort Interfaces for the Internet of Things: Invited paper. , 2019, , .		3
30	A Two-Stage Single-Ended OTA with Improved Composite Transistors. , 2021, , .		2
31	CMOS inverter linearization technique with active source degeneration. , 2021, , .		2
32	Instrumented flexible active electrode matrix suitable for human-computer interface applications. Biomedical Physics and Engineering Express, 2016, 2, 035020.	1.2	1
33	Temperature Characterization of a Fully-synthesizable Rail-to-Rail Dynamic Voltage Comparator operating down to 0.15-V : (Invited paper). , 2021, , .		1
34	Design of an Ultra-Low Voltage Bias Current Generator Highly Immune to Electromagnetic Interference. Journal of Low Power Electronics and Applications, 2021, 11, 6.	2.0	1
35	Fully-differential inverter-based OTA with improved composite transistors. , 2021, , .		1
36	STDP with adaptive synaptic delay for robot navigation control. , 2007, , .		0

#	ARTICLE	IF	CITATIONS
37	A new current sensor based on the Miller effect highly immune to EMI. , 2012, , .		0
38	Ultra-Low Voltage Current Biasing Highly Immune to EMI. , 2019, , .		0