

Shahram Minaei

List of Publications by Citations

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The third column is the impact factor (IF) of the journal, and the fourth column is the number of citations of the article.

112 papers	2,185 citations	27 h-index	41 g-index
116 ext. papers	2,685 ext. citations	1.7 avg, IF	5.46 L-index

#	Paper	IF	Citations
112	Universal current-mode filters and parasitic impedance effects on the filter performances. <i>International Journal of Circuit Theory and Applications</i> , 2008 , 36, 161-171	2	84
111	A Modified CFOA and Its Applications to Simulated Inductors, Capacitance Multipliers, and Analog Filters. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2008 , 55, 266-275	3.9	83
110	Novel Voltage-Mode All-Pass Filter Based on Using DVCCs. <i>Circuits, Systems, and Signal Processing</i> , 2010 , 29, 391-402	2.2	76
109	New resistorless and electronically tunable realization of dual-output VM all-pass filter using VDIBA. <i>Analog Integrated Circuits and Signal Processing</i> , 2013 , 74, 141-154	1.2	73
108	Positive/negative lossy/lossless grounded inductance simulators employing single VDCC and only two passive elements. <i>AEU - International Journal of Electronics and Communications</i> , 2014 , 68, 73-78	2.8	71
107	Limitations of the Simulated Inductors Based on a Single Current Conveyor. <i>IEEE Transactions on Circuits and Systems Part 1: Regular Papers</i> , 2006 , 53, 2860-2867		71
106	A Novel Grounded Inductor Realization Using a Minimum Number of Active and Passive Components. <i>ETRI Journal</i> , 2005 , 27, 427-432	1.4	65
105	A mixed-mode KHN-biquad using DVCC and grounded passive elements suitable for direct cascading. <i>International Journal of Circuit Theory and Applications</i> , 2009 , 37, 793-810	2	61
104	A Versatile Active Circuit for Realising Floating Inductance, Capacitance, FDNR and Admittance Converter. <i>Analog Integrated Circuits and Signal Processing</i> , 2006 , 47, 199-202	1.2	58
103	General configuration for realizing current-mode first-order all-pass filter using DVCC. <i>International Journal of Electronics</i> , 2005 , 92, 347-356	1.2	55
102	All-Grounded Passive Elements Voltage-Mode DVCC-Based Universal Filters. <i>Circuits, Systems, and Signal Processing</i> , 2010 , 29, 295-309	2.2	54
101	A Simple Schmitt Trigger Circuit with Grounded Passive Elements and Its Application to Square/Triangular Wave Generator. <i>Circuits, Systems, and Signal Processing</i> , 2012 , 31, 877-888	2.2	50
100	Resistorless floating immittance function simulators employing current controlled conveyors and a grounded capacitor. <i>Electrical Engineering</i> , 2006 , 88, 519-525	1.5	48
99	New CCII-based versatile structure for realizing PID controller and instrumentation amplifier. <i>Microelectronics Journal</i> , 2010 , 41, 311-316	1.8	45
98	On the Realization of Simulated Inductors with Reduced Parasitic Impedance Effects. <i>Circuits, Systems, and Signal Processing</i> , 2009 , 28, 451-465	2.2	44
97	A new full-wave rectifier circuit employing single dual-X current conveyor. <i>International Journal of Electronics</i> , 2008 , 95, 777-784	1.2	44
96	CCII-Based Grounded to Floating Immittance Converter and a Floating Inductance Simulator. <i>Analog Integrated Circuits and Signal Processing</i> , 2006 , 46, 287-291	1.2	43

95	DXCCII-based grounded inductance simulators and filter applications. <i>Microelectronics Journal</i> , 2011 , 42, 1074-1081	1.8	41
94	Memstor, memstance simulations via a versatile 4-port built with new adder and subtractor circuits. <i>International Journal of Electronics</i> , 2015 , 102, 911-931	1.2	40
93	Electronically tunable, active only floating inductance simulation. <i>International Journal of Electronics</i> , 2003 , 89, 905-912	1.2	39
92	NOVEL FLOATING INDUCTANCE AND FDNR SIMULATORS EMPLOYING CCII+s. <i>Journal of Circuits, Systems and Computers</i> , 2006 , 15, 75-81	0.9	37
91	A low power current controllable single-input three-output current-mode filter using MOS transistors only. <i>AEU - International Journal of Electronics and Communications</i> , 2014 , 68, 1205-1213	2.8	34
90	Unity/Variable-gain Voltage-mode/Current-mode First-order All-pass Filters Using Single Dual-X Second-generation Current Conveyor. <i>IETE Journal of Research</i> , 2010 , 56, 305-312	0.9	33
89	Universal Current-Mode Active-C Filter Employing Minimum Number of Passive Elements. <i>Analog Integrated Circuits and Signal Processing</i> , 2006 , 46, 169-171	1.2	32
88	New current-mode current-controlled universal filter with single input and three outputs. <i>International Journal of Electronics</i> , 2001 , 88, 333-337	1.2	30
87	Novel floating simulated inductors with wider operating-frequency ranges. <i>Microelectronics Journal</i> , 2009 , 40, 928-938	1.8	27
86	Current-Mode Electronically Tunable Universal Filter Using Only Plus-Type Current Controlled Conveyors and Grounded Capacitors. <i>ETRI Journal</i> , 2004 , 26, 292-296	1.4	27
85	On the realization of high-order current-mode filter employing current controlled conveyors. <i>Computers and Electrical Engineering</i> , 2008 , 34, 165-172	4.3	26
84	High output impedance current-mode lowpass, bandpass and highpass filters using current controlled conveyors. <i>International Journal of Electronics</i> , 2001 , 88, 915-922	1.2	26
83	Component reduced current-mode full-wave rectifier circuits using single active component. <i>IET Circuits, Devices and Systems</i> , 2016 , 10, 1-11	1.1	24
82	A First-Order Fully Cascadable Current-Mode Universal Filter Composed of Dual Output CCII and a Grounded Capacitor. <i>Journal of Circuits, Systems and Computers</i> , 2016 , 25, 1650042	0.9	24
81	High Input Impedance NMOS-based Phase Shifter with Minimum Number of Passive Elements. <i>Circuits, Systems, and Signal Processing</i> , 2012 , 31, 51-60	2.2	24
80	A new ICCII based resistor-less current-mode first-order universal filter with electronic tuning capability. <i>Microelectronics Journal</i> , 2017 , 67, 101-110	1.8	24
79	CCII-based universal current-mode analog filter employing only grounded passive components. <i>Analog Integrated Circuits and Signal Processing</i> , 2009 , 58, 161-169	1.2	24
78	A Novel Resistor-Free Electronically Adjustable Current-Mode Instrumentation Amplifier. <i>Circuits, Systems, and Signal Processing</i> , 2013 , 32, 1025-1038	2.2	21

77	ELECTRONICALLY TUNABLE CURRENT-MODE UNIVERSAL BIQUAD FILTER USING DUAL-X CURRENT CONVEYORS. <i>Journal of Circuits, Systems and Computers</i> , 2009 , 18, 665-680	0.9	20
76	New CFOA-based first-order all-pass filters and their applications. <i>AEU - International Journal of Electronics and Communications</i> , 2019 , 103, 57-63	2.8	19
75	A second-generation voltage conveyor (VCII)Based simulated grounded inductor. <i>International Journal of Circuit Theory and Applications</i> , 2020 , 48, 1180-1193	2	19
74	An Electronically Fine-Tunable Multi-InputSingle-Output Universal Filter. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2011 , 58, 356-360	3.5	19
73	ALL GROUNDED PASSIVE ELEMENTS CURRENT-MODE ALL-PASS FILTER. <i>Journal of Circuits, Systems and Computers</i> , 2009 , 18, 31-43	0.9	19
72	A simple CMOS-based inductor simulator and frequency performance improvement techniques. <i>AEU - International Journal of Electronics and Communications</i> , 2012 , 66, 884-891	2.8	18
71	Design and application examples of CMOS fractional-order differentiators and integrators. <i>Microelectronics Journal</i> , 2019 , 83, 155-167	1.8	18
70	All-pass sections with rich cascability and IC realization suitability. <i>International Journal of Circuit Theory and Applications</i> , 2012 , 40, 477-488	2	17
69	CMOS FIRST-ORDER CURRENT-MODE ALL-PASS FILTER WITH ELECTRONIC TUNING CAPABILITY AND ITS APPLICATIONS. <i>Journal of Circuits, Systems and Computers</i> , 2013 , 22, 1350007	0.9	17
68	ICCI-Based Voltage-Mode Filter with Single Input and Six Outputs Employing Grounded Capacitors. <i>Circuits, Systems, and Signal Processing</i> , 2006 , 25, 559-566	2.2	17
67	A novel COA-based electronically adjustable current-mode instrumentation amplifier topology. <i>AEU - International Journal of Electronics and Communications</i> , 2017 , 82, 285-293	2.8	16
66	REALIZATION OF FIRST-ORDER CURRENT-MODE FILTERS WITH LOW NUMBER OF MOS TRANSISTORS. <i>Journal of Circuits, Systems and Computers</i> , 2013 , 22, 1250071	0.9	16
65	NOVEL CMOS TECHNOLOGY-BASED LINEAR GROUNDED VOLTAGE CONTROLLED RESISTOR. <i>Journal of Circuits, Systems and Computers</i> , 2011 , 20, 447-455	0.9	16
64	A Low-Voltage Low-Power Resistor-Based Current Mirror and Its Applications. <i>Journal of Circuits, Systems and Computers</i> , 2017 , 26, 1750180	0.9	15
63	A new DVCC-based fully cascable voltage-mode full-wave rectifier. <i>Journal of Computational Electronics</i> , 2016 , 15, 1440-1449	1.8	15
62	A New Transresistance-Mode Instrumentation Amplifier with Low Number of MOS Transistors and Electronic Tuning Opportunity. <i>Journal of Circuits, Systems and Computers</i> , 2016 , 25, 1650022	0.9	15
61	Electronically Tunable Simulated Transformer and Its Application to Stagger-Tuned Filter. <i>IEEE Transactions on Instrumentation and Measurement</i> , 2008 , 57, 2083-2088	5.2	15
60	Commercially Available Active Device Based Grounded Inductor Simulator and Universal Filter with Improved Low Frequency Performances. <i>Journal of Circuits, Systems and Computers</i> , 2017 , 26, 1750052	0.9	14

59	Realization of arbitrary current transfer functions based on commercially available CCII + s. <i>International Journal of Circuit Theory and Applications</i> , 2014 , 42, 659-670	2	13
58	A novel phase shifter using two NMOS transistors and passive elements. <i>Analog Integrated Circuits and Signal Processing</i> , 2010 , 62, 77-81	1.2	13
57	Low voltage low power CMOS current differencing buffered amplifier. <i>Analog Integrated Circuits and Signal Processing</i> , 2010 , 62, 237-244	1.2	13
56	. <i>IEEE Transactions on Instrumentation and Measurement</i> , 2007 , 56, 2782-2787	5.2	13
55	CURRENT-MODE ACTIVE-C FILTER EMPLOYING REDUCED NUMBER OF CCCII+s. <i>Journal of Circuits, Systems and Computers</i> , 2007 , 16, 507-516	0.9	13
54	Analysis and design of a new COA-based current-mode instrumentation amplifier with robust performance against mismatches. <i>AEU - International Journal of Electronics and Communications</i> , 2018 , 89, 105-109	2.8	12
53	Inverting voltage buffer based lossless grounded inductor simulators. <i>AEU - International Journal of Electronics and Communications</i> , 2018 , 83, 131-137	2.8	12
52	Enhanced dynamic range analog filter topologies with a notch/all-pass circuit example. <i>Analog Integrated Circuits and Signal Processing</i> , 2007 , 51, 181-189	1.2	12
51	Ultra low-power electronically tunable current-mode instrumentation amplifier for biomedical applications. <i>AEU - International Journal of Electronics and Communications</i> , 2020 , 117, 153120	2.8	11
50	A HIGH INPUT IMPEDANCE VOLTAGE-MODE ALL-PASS/NOTCH FILTER USING A SINGLE VARIABLE GAIN CURRENT CONVEYOR. <i>Journal of Circuits, Systems and Computers</i> , 2008 , 17, 827-834	0.9	10
49	New simple transistor realizations of second- generation voltage conveyor. <i>International Journal of Circuit Theory and Applications</i> , 2020 , 48, 2023-2038	2	10
48	Realisation of nth-order current transfer function employing ECCIIs and application examples. <i>International Journal of Electronics</i> , 2009 , 96, 1115-1126	1.2	9
47	Parasitic compensation in CCI-based circuits for reduced power consumption. <i>Analog Integrated Circuits and Signal Processing</i> , 2010 , 65, 157-162	1.2	9
46	A NEW ACTIVE NETWORK SUITABLE FOR REALIZING LADDER FILTERS AND TRANSFORMER SIMULATOR. <i>Journal of Circuits, Systems and Computers</i> , 2007 , 16, 29-41	0.9	9
45	ON THE REALIZATION OF HIGH PERFORMANCE CURRENT CONVEYORS AND THEIR APPLICATIONS. <i>Journal of Circuits, Systems and Computers</i> , 2013 , 22, 1350015	0.9	8
44	Trade-offs in the OTA-based analog filter design. <i>Analog Integrated Circuits and Signal Processing</i> , 2009 , 60, 205-213	1.2	8
43	A CMOS Classifier Circuit Using Neural Networks With Novel Architecture. <i>IEEE Transactions on Neural Networks</i> , 2007 , 18, 1845-1850		8
42	Low input impedance trans-impedance type multifunction filter using only active elements. <i>International Journal of Electronics</i> , 2005 , 92, 385-392	1.2	8

41	New mixed-mode second-generation voltage conveyor based first-order all-pass filter. <i>IET Circuits, Devices and Systems</i> , 2020 , 14, 901-907	1.1	8
40	Neural CMOS-integrated circuit and its application to data classification. <i>IEEE Transactions on Neural Networks and Learning Systems</i> , 2012 , 23, 717-24	10.3	7
39	A novel full-wave rectifier/sinusoidal frequency doubler topology based on CFOAs. <i>Analog Integrated Circuits and Signal Processing</i> , 2017 , 93, 351-362	1.2	7
38	A new differential configuration suitable for realization of high CMRR, all-pass/notch filters. <i>Electrical Engineering</i> , 2006 , 88, 317-326	1.5	7
37	Derivation of low-power first-order low-pass, high-pass and all-pass filters. <i>Analog Integrated Circuits and Signal Processing</i> , 2012 , 70, 151-156	1.2	6
36	Electronically tunable MOSFET-C voltage-mode all-pass filter based on universal voltage conveyor 2011 ,		6
35	A simple low voltage, high output impedance resistor based current mirror with extremely low input and output voltage requirements 2016 ,		6
34	Metamutator applications: a quadrature MOS only oscillator and transconductance/transimpedance amplifiers. <i>Analog Integrated Circuits and Signal Processing</i> , 2016 , 89, 801-808	1.2	6
33	A voltage-mode PID controller using a single CFOA and only grounded capacitors. <i>Microelectronics Journal</i> , 2018 , 81, 84-93	1.8	6
32	High-order realisation of MOSFET-only band-pass filters for RF applications. <i>IET Circuits, Devices and Systems</i> , 2018 , 12, 467-477	1.1	5
31	Lossless grounded inductance simulation using only one modified dual output DDCC 2011 ,		4
30	CCII+ based fully CMOS four-quadrant multiplier 2011 ,		4
29	A new first-order universal filter consisting of two ICCII + s and a grounded capacitor. <i>AEU - International Journal of Electronics and Communications</i> , 2021 , 137, 153802	2.8	4
28	A compact rail-to-rail CMOS buffer amplifier with very low quiescent current. <i>International Journal of Electronics</i> , 2015 , 102, 982-992	1.2	3
27	Fractional-Order Differentiators and Integrators with Reduced Circuit Complexity 2018 ,		3
26	Modified Gorski-Popiel Technique and Synthetic Floating Transformer Circuit Using Minimum Components. <i>Journal of Circuits, Systems and Computers</i> , 2017 , 26, 1750013	0.9	3
25	New ECCII-based electronically controllable current-mode instrumentation amplifier with high frequency performance 2017 ,		3
24	A flexible current-mode classifier circuit and its applications. <i>International Journal of Circuit Theory and Applications</i> , 2011 , 39, 933-945	2	3

23	HIGH-SLEW RATE LOW-QUIESCENT CURRENT RAIL-TO-RAIL CMOS BUFFER AMPLIFIER FOR FLAT PANEL DISPLAYS. <i>Journal of Circuits, Systems and Computers</i> , 2011 , 20, 1277-1286	0.9	3
22	A TUNABLE CIRCUIT FOR REALIZING ARBITRARY FLOATING IMPEDANCES. <i>Journal of Circuits, Systems and Computers</i> , 2008 , 17, 513-524	0.9	3
21	High-order current-mode low-pass, high-pass and band-pass filter responses employing CCCIs 2007 ,		3
20	Active only integrator and differentiator with tunable time constants. <i>International Journal of Electronics</i> , 2003 , 90, 581-588	1.2	3
19	. <i>IEEE Access</i> , 2021 , 9, 145977-145987	3.5	3
18	± 0.45V CMOS Second-Generation Voltage Conveyor Based on Super Source Follower. <i>Circuits, Systems, and Signal Processing</i> , 2022 , 41, 1819	2.2	3
17	A New Simulated Inductor with Reduced Series Resistor Using a Single VCI±. <i>Electronics (Switzerland)</i> , 2021 , 10, 1693	2.6	3
16	Synthetic Transformer Design Using Commercially Available Active Components. <i>Circuits, Systems, and Signal Processing</i> , 2020 , 39, 3770-3786	2.2	2
15	Dual output filter topology with a single NIC for pole frequency sensitive applications. <i>International Journal of Electronics</i> , 2009 , 96, 699-710	1.2	2
14	Linearly weighted classifier circuit 2009 ,		2
13	Novel grounded capacitor-based resistorless tunable floating/grounded inductance simulator 2016 ,		2
12	Towards Realization of a Low-Voltage Class-AB VCI with High Current Drive Capability. <i>Electronics (Switzerland)</i> , 2021 , 10, 2303	2.6	2
11	MOSFET-C-based grounded active inductors with electronically tunable properties. <i>International Journal of RF and Microwave Computer-Aided Engineering</i> , 2020 , 30, e22274	1.5	1
10	Equivalent circuit models in current-mode circuits for time delay calculations. <i>Analog Integrated Circuits and Signal Processing</i> , 2014 , 81, 43-52	1.2	1
9	Voltage-mode all-pass filter design using simple CMOS transconductor: Non-ideal case study 2015 ,		1
8	Voltage-mode multiphase sinusoidal oscillators using CBTAs 2012 ,		1
7	Pulse Width Modulation using a recently developed CMOS core circuit 2013 ,		1
6	TO-CCII based voltage-mode universal biquadratic filter 2011 ,		1

5	SIFO voltage-mode universal filters employing TO-CCIs 2012 ,		1
4	Low-voltage low-power and high-swing current differencing buffered amplifier 2009 ,		1
3	A novel design for voltage inverting metamutator and its applications. <i>Microelectronics Journal</i> , 2021 , 113, 105096	1.8	1
2	A New Active Device Namely S-CCI and Its Applications: Simulated Floating Inductor and Quadrature Oscillators. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2022 , 1-11	3.9	0
1	An 8-Bit 50-MS/s CMOS Digital-Analog Converter. <i>Analog Integrated Circuits and Signal Processing</i> , 2004 , 39, 213-217	1.2	