

Nobuyuki Yoshikawa

List of Publications by Year in descending order

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325
times ranked

852
citing authors

#	ARTICLE	IF	CITATIONS
1	Design of Discrete Hopfield Neural Network Using a Single Flux Quantum Circuit. IEEE Transactions on Applied Superconductivity, 2022, 32, 1-4.	1.1	2
2	Boltzmann Machine Using Superconducting Circuits. IEEE Transactions on Applied Superconductivity, 2022, 32, 1-5.	1.1	0
3	Research Trends on Low-power, High-performance Superconducting Computer Technology. IEIJ Transactions on Fundamentals and Materials, 2022, 142, 19-20.	0.2	0
4	Design of Binary Convolution Operation Circuit for Binarized Neural Networks Using Single-Flux-Quantum Circuit. IEEE Transactions on Applied Superconductivity, 2022, 32, 1-5.	1.1	1
5	A 16-Bit Parallel Prefix Carry Look-Ahead Kogge-Stone Adder Implemented in Adiabatic Quantum-Flux-Parametron Logic. IEICE Transactions on Electronics, 2022, E105.C, 270-276.	0.3	4
6	Adiabatic Quantum-Flux-Parametron: A Tutorial Review. IEICE Transactions on Electronics, 2022, E105.C, 251-263.	0.3	17
7	Adiabatic Quantum-Flux-Parametron With Delay-Line Clocking Using Square Excitation Currents. IEICE Transactions on Electronics, 2022, , .	0.3	0
8	Compact RSFQ microwave pulse generator based on an integrated RF module for controlling superconducting qubits. Applied Physics Letters, 2022, 120, .	1.5	6
9	Timing Controller for a Superconductor Microwave Switch using Adiabatic Quantum Flux Parametron Circuits. IEIJ Transactions on Fundamentals and Materials, 2022, 142, 197-201.	0.2	0
10	Demonstration of Single-Flux-Quantum 64-B Lookup Table With Cryo-CMOS Decoders for Reconfiguration. IEEE Transactions on Applied Superconductivity, 2022, 32, 1-5.	1.1	2
11	Transmission Line Effects of Long Gate-to-Gate Interconnections in Adiabatic Quantum-Flux-Parametron Logic Circuits. IEEE Transactions on Applied Superconductivity, 2022, 32, 1-7.	1.1	1
12	Trends in Low-Temperature Circuit Technology to Control Quantum Bits for Large-Scale Quantum Computers. IEIJ Transactions on Fundamentals and Materials, 2021, 141, 20-21.	0.2	0
13	Synthesis and Design of Quasi-Canonical Planar Filters Comprising Cascaded Frequency-Variant Blocks. IEEE Transactions on Microwave Theory and Techniques, 2021, 69, 671-681.	2.9	15
14	Towards AQFP-Capable Physical Design Automation. , 2021, , .		3
15	Binary Counters Using Adiabatic Quantum-Flux-Parametron Logic. IEEE Transactions on Applied Superconductivity, 2021, 31, 1-5.	1.1	9
16	Demonstration of an efficient single flux quantum logic circuit by introducing a local magnetic flux biasing. Superconductor Science and Technology, 2021, 34, 055007.	1.8	0
17	Compact Superconducting Lookup Table Composed of Two-Dimensional Memory Cell Array Reconfigured by External DC Control Currents. IEEE Transactions on Applied Superconductivity, 2021, 31, 1-6.	1.1	5
18	MANA: A Monolithic Adiabatic iNtegration Architecture Microprocessor Using 1.4-zJ/op Unshunted Superconductor Josephson Junction Devices. IEEE Journal of Solid-State Circuits, 2021, 56, 1152-1165.	3.5	49

#	ARTICLE	IF	CITATIONS
19	Evaluation of flux trapping moat position on AQFP cell performance. Journal of Physics: Conference Series, 2021, 1975, 012027.	0.3	4
20	Design and Evaluation of 2-bit-Input Single-Flux-Quantum Autocorrelator System for Astronomical Data Analysis. IEEE Transactions on Applied Superconductivity, 2021, 31, 1-4.	1.1	0
21	Low-Autocorrelation Random Number Generator Based on Adiabatic Quantum-Flux-Parametron Logic. IEEE Transactions on Applied Superconductivity, 2021, 31, 1-5.	1.1	8
22	An Adiabatic Superconductor Comparator With 46 nA Sensitivity. IEEE Transactions on Applied Superconductivity, 2021, 31, 1-5.	1.1	5
23	A Compact Interface Between Adiabatic Quantum-Flux-Parametron and Rapid Single-Flux-Quantum Circuits. IEEE Transactions on Applied Superconductivity, 2021, 31, 1-5.	1.1	10
24	Demonstration of a 47.8ÅGHz High-Speed FFT Processor Using Single-Flux-Quantum Technology. IEEE Transactions on Applied Superconductivity, 2021, 31, 1-5.	1.1	13
25	Logic Synthesis of Sequential Logic Circuits for Adiabatic Quantum-Flux-Parametron Logic. IEEE Transactions on Applied Superconductivity, 2021, 31, 1-5.	1.1	10
26	Design and Demonstration of Directly Coupled Quantum-Flux-Parametron Circuits With Optimized Parameters. IEEE Transactions on Applied Superconductivity, 2021, 31, 1-5.	1.1	4
27	Impedance Design of Excitation Lines in Adiabatic Quantum-Flux-Parametron Logic Using InductEx. IEEE Transactions on Applied Superconductivity, 2021, 31, 1-5.	1.1	9
28	Frequency synchronization of single flux quantum oscillators. Superconductor Science and Technology, 2021, 34, 105004.	1.8	1
29	Planarized Nb 4-Layer Fabrication Process for Superconducting Integrated Circuits and Its Fabricated Device Evaluation. IEICE Transactions on Electronics, 2021, E104.C, 435-445.	0.3	4
30	Buffer Reduction Via N-Phase Clocking in Adiabatic Quantum-Flux-Parametron Benchmark Circuits. IEEE Transactions on Applied Superconductivity, 2021, 31, 1-8.	1.1	8
31	Adiabatic quantum-flux-parametron with delay-line clocking: logic gate demonstration and phase skipping operation. Superconductor Science and Technology, 2021, 34, 125002.	1.8	7
32	A high-speed interface based on a Josephson latching driver for adiabatic quantum-flux-parametron logic. IEICE Transactions on Electronics, 2021, , .	0.3	0
33	MANA: A Monolithic Adiabatic iNtegration Architecture Microprocessor using 1.4zJ/op Superconductor Josephson Junction Devices. , 2020, , .		9
34	Design and Characterization of Track Routing Architecture for RSFQ and AQFP Circuits in a Multilayer Process. IEEE Transactions on Applied Superconductivity, 2020, 30, 1-9.	1.1	14
35	Low-latency power-dividing clocking scheme for adiabatic quantum-flux-parametron logic. Applied Physics Letters, 2020, 116, .	1.5	11
36	Investigation of the Effects of 1/f Noise on Superconducting Circuits. IEEE Transactions on Applied Superconductivity, 2020, 30, 1-4.	1.1	0

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37	A semi-custom design methodology and environment for implementing superconductor adiabatic quantum-flux-parametron microprocessors. Superconductor Science and Technology, 2020, 33, 054006.	1.8	22
38	Sharp-selectivity in-line topology low temperature superconducting bandpass filter for superconducting quantum applications. Superconductor Science and Technology, 2020, 33, 035012.	1.8	4
39	A compact AQFP logic cell design using an 8-metal layer superconductor process. Superconductor Science and Technology, 2020, 33, 035010.	1.8	16
40	Scalable readout interface for superconducting nanowire single-photon detectors using AQFP and RSFQ logic families. Optics Express, 2020, 28, 15824.	1.7	18
41	Directly coupled adiabatic superconductor logic. Superconductor Science and Technology, 2020, 33, 065002.	1.8	9
42	Research Unit on Extremely Energy-Efficient processor, Institute of Advanced Sciences/Yoshikawa & Yamanashi Laboratory, Graduate School of Engineering Science, Yokohama National University. TEION KOGAKU (Journal of Cryogenics and Superconductivity Society of Japan), 2020, 55, 201-202.	0.1	0
43	Proposal of ultra-low voltage quantum well optical modulator for optical interconnection in superconducting integrated circuit systems. Japanese Journal of Applied Physics, 2020, 59, SOOB01.	0.8	3
44	ASAP. , 2020, , .		8
45	Demonstration of a Single-Flux-Quantum Microprocessor Operating With Josephson-CMOS Hybrid Memory. IEEE Transactions on Applied Superconductivity, 2020, 30, 1-6.	1.1	11
46	Simulation of the Margins in Single Flux Quantum Circuits Containing Î€-Shifted Josephson Junctions. IEEE Transactions on Applied Superconductivity, 2019, 29, 1-5.	1.1	7
47	A Majority Logic Synthesis Framework for Adiabatic Quantum-Flux-Parametron Superconducting Circuits. , 2019, , .		21
48	Low-latency adiabatic superconductor logic using delay-line clocking. Applied Physics Letters, 2019, 115, .	1.5	23
49	Adiabatic Quantum-Flux-Parametron: Towards Building Extremely Energy-Efficient Circuits and Systems. Scientific Reports, 2019, 9, 10514.	1.6	27
50	IDE Development, Logic Synthesis and Buffer/Splitter Insertion Framework for Adiabatic Quantum-Flux-Parametron Superconducting Circuits. , 2019, , .		6
51	A stochastic-computing based deep learning framework using adiabatic quantum-flux-parametron superconducting technology. , 2019, , .		27
52	Design and Demonstration of an Adiabatic-Quantum-Flux-Parametron Field-Programmable Gate Array Using Josephson-CMOS Hybrid Memories. IEEE Transactions on Applied Superconductivity, 2019, 29, 1-6.	1.1	10
53	ColdFlux Superconducting EDA and TCAD Tools Project: Overview and Progress. IEEE Transactions on Applied Superconductivity, 2019, 29, 1-7.	1.1	50
54	Advanced Direct Synthesis Approach for High Selectivity In-Line Topology Filters Comprising $N \times 1$ Adjacent Frequency-Variant Couplings. IEEE Access, 2019, 7, 41659-41668.	2.6	27

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55	Superconductor Amoeba-Inspired Problem Solvers for Combinatorial Optimization. Physical Review Applied, 2019, 11, .	1.5	12
56	Demonstration of Picosecond Time Resolution in Double-Oscillator Time-to-Digital Converter Using Single-Flux-Quantum Circuits. IEEE Transactions on Applied Superconductivity, 2019, 29, 1-5.	1.1	4
57	Fabrication of Adiabatic Quantum-Flux-Parametron Integrated Circuits Using an Automatic Placement Tool Based on Genetic Algorithms. IEEE Transactions on Applied Superconductivity, 2019, 29, 1-6.	1.1	14
58	A Feedback-Friendly Large-Scale Clocking Scheme for Adiabatic Quantum-Flux-Parametron Logic Datapaths. IEEE Transactions on Applied Superconductivity, 2019, 29, 1-5.	1.1	1
59	Adiabatic quantum-flux-parametron with π Josephson junctions. Journal of Applied Physics, 2019, 125, .	1.1	8
60	Demonstration of a Superconducting Nanowire Single-Photon Detector using Adiabatic Quantum-Flux-Parametron Logic in a 0.1-W Gifford-McMahon Cryocooler. IEEE Transactions on Applied Superconductivity, 2019, 29, 1-4.	1.1	3
61	Design of Adiabatic Quantum-Flux-Parametron Register Files Using a Top-Down Design Flow. IEEE Transactions on Applied Superconductivity, 2019, 29, 1-5.	1.1	5
62	Superconducting Digital Electronics for Controlling Quantum Computing Systems. IEICE Transactions on Electronics, 2019, E102.C, 217-223.	0.3	9
63	Miniaturization of adiabatic quantum-flux-parametron circuits by adopting offset buffers. Superconductor Science and Technology, 2019, 32, 065007.	1.8	2
64	An adiabatic superconductor 8-bit adder with 24 $k_B T$ energy dissipation per junction. Applied Physics Letters, 2019, 114, .	1.5	47
65	AQFPTX: Adiabatic Quantum-Flux-Parametron Timing eXtraction Tool. , 2019, , .		10
66	A Buffer and Splitter Insertion Framework for Adiabatic Quantum-Flux-Parametron Superconducting Circuits. , 2019, , .		12
67	Standard Cell Layout Synthesis for Row-Based Placement and Routing of RSFQ and AQFP Logic Families. , 2019, , .		6
68	Systematic method to evaluate energy dissipation in adiabatic quantum-flux-parametron logic. Journal of Applied Physics, 2019, 126, .	1.1	11
69	A reversible full adder using adiabatic superconductor logic. Superconductor Science and Technology, 2019, 32, 035005.	1.8	11
70	Trend of Future Technologies based on Superconducting Electronics. IEEE Transactions on Fundamentals and Materials, 2019, 139, 20-21.	0.2	0
71	Josephson Junctions for Digital Applications. Springer Series in Materials Science, 2019, , 611-701.	0.4	3
72	Investigation on Ultra-Low Voltage Quantum Well Optical Modulator for Optical Interconnection for Superconducting Integrated Circuits. , 2019, , .		0

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73	Minimum energy dissipation required for a logically irreversible operation. Physical Review E, 2018, 97, 012124.	0.8	11
74	Matrix Synthesis of Cascaded K-Tuplets Filters with Frequency-Variant Couplings. , 2018, , .		0
75	A random-access-memory cell based on quantum flux parametron with three control lines. Journal of Physics: Conference Series, 2018, 1054, 012063.	0.3	5
76	Design methodology of single-flux-quantum flip-flops composed of both 0- and π -shifted Josephson junctions. Superconductor Science and Technology, 2018, 31, 105003.	1.8	25
77	Recent Progress on Reversible Quantum-Flux-Parametron for Superconductor Reversible Computing. IEICE Transactions on Electronics, 2018, E101.C, 352-358.	0.3	8
78	Thermally Assisted Superconductor Transistors for Josephson-CMOS Hybrid Memories. IEICE Transactions on Electronics, 2018, E101.C, 370-377.	0.3	4
79	Influence of Magnetic Flux Trapped in Moats on Superconducting Integrated Circuit Operation. IEEE Transactions on Applied Superconductivity, 2018, 28, 1-5.	1.1	4
80	Toward the Creation of Future Technologies based on Superconductivity Electronics. IEEE Transactions on Fundamentals and Materials, 2018, 138, 25-27.	0.2	0
81	Reduction of the supply current of single-flux-quantum time-to-digital converters by current recycling techniques. IEEE Transactions on Applied Superconductivity, 2017, , 1-1.	1.1	17
82	Design and Implementation of a 16-Word by 1-Bit Register File Using Adiabatic Quantum Flux Parametron Logic. IEEE Transactions on Applied Superconductivity, 2017, 27, 1-4.	1.1	25
83	Design and Implementation of an SFQ-Based Single-Chip FFT Processor. IEEE Transactions on Applied Superconductivity, 2017, 27, 1-5.	1.1	9
84	Reversibility and energy dissipation in adiabatic superconductor logic. Scientific Reports, 2017, 7, 75.	1.6	22
85	Multi-excitation adiabatic quantum-flux-parametron. Journal of Applied Physics, 2017, 121, ,	1.1	8
86	Adiabatic quantum-flux-parametron cell library designed using a 10 kA cm ⁻² niobium fabrication process. Superconductor Science and Technology, 2017, 30, 035002.	1.8	97
87	Three-dimensional adiabatic quantum-flux-parametron fabricated using a double-active-layered niobium process. Superconductor Science and Technology, 2017, 30, 075003.	1.8	23
88	Synthesis Flow for Cell-Based Adiabatic Quantum-Flux-Parametron Structural Circuit Generation With HDL Back-End Verification. IEEE Transactions on Applied Superconductivity, 2017, 27, 1-5.	1.1	25
89	Majority-Logic-Optimized Parallel Prefix Carry Look-Ahead Adder Families Using Adiabatic Quantum-Flux-Parametron Logic. IEEE Transactions on Applied Superconductivity, 2017, 27, 1-7.	1.1	28
90	Josephson-CMOS Hybrid Memory With Nanocryotrons. IEEE Transactions on Applied Superconductivity, 2017, 27, 1-4.	1.1	21

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91	Fully Functional Operation of Low-Power 64-kb Josephson-CMOS Hybrid Memories. IEEE Transactions on Applied Superconductivity, 2017, 27, 1-7.	1.1	27
92	High-Speed Superconductive Decimation Filter for Sigma-Delta Analog to Digital Converter. Journal of Physics: Conference Series, 2017, 871, 012068.	0.3	0
93	Development and Demonstration of Routing and Placement EDA Tools for Large-Scale Adiabatic Quantum-Flux-Parametron Circuits. IEEE Transactions on Applied Superconductivity, 2017, 27, 1-9.	1.1	23
94	Evaluation of current sensitivity of quantum flux parametron. Superconductor Science and Technology, 2017, 30, 084004.	1.8	11
95	Measurement of low bit-error-rates of adiabatic quantum-flux-parametron logic using a superconductor voltage driver. Applied Physics Letters, 2017, 110, .	1.5	21
96	Design and Implementation of Scalable Register Files Using Adiabatic Quantum Flux Parametron Logic. , 2017, , .		5
97	Adiabatic quantum-flux-parametron interface for the readout of superconducting nanowire single-photon detectors. Optics Express, 2017, 25, 32650.	1.7	18
98	Superconducting Time-of-flight Mass Spectrometry Systems for Biomolecules using Superconducting Digital Circuits. TEION KOGAKU (Journal of Cryogenics and Superconductivity Society of Japan), 2017, 52, 349-354.	0.1	0
99	Research Trends and New Developments in Phase Engineering. IEJ Transactions on Fundamentals and Materials, 2017, 137, 23-25.	0.2	0
100	Majority Gate-Based Feedback Latches for Adiabatic Quantum Flux Parametron Logic. IEICE Transactions on Electronics, 2016, E99.C, 710-716.	0.3	10
101	Demonstration of Signal Transmission between Adiabatic Quantum-Flux-Parametrons and Rapid Single-Flux-Quantum Circuits Using Superconductive Microstrip Lines. IEEE Transactions on Applied Superconductivity, 2016, , 1-1.	1.1	7
102	Power Reduction of Josephson Random Access Memory Using Stochastic Resonance. IEEE Transactions on Applied Superconductivity, 2016, 26, 1-4.	1.1	4
103	High-speed demonstration of low-power 1 k-bit shift-register memories using LR-biasing SFQ circuits. IEICE Electronics Express, 2016, 13, 20160074-20160074.	0.3	5
104	Inductance and Current Distribution Extraction in Nb Multilayer Circuits with Superconductive and Resistive Components. IEICE Transactions on Electronics, 2016, E99.C, 683-691.	0.3	8
105	HDL-Based Modeling Approach for Digital Simulation of Adiabatic Quantum Flux Parametron Logic. IEEE Transactions on Applied Superconductivity, 2016, 26, 1-5.	1.1	25
106	Improvement of Operation Speed of LR-Biased Low-Power Single-Flux Quantum Circuits by Introduction of Dynamic Resetting of Bias Currents. IEEE Transactions on Applied Superconductivity, 2016, 26, 1-5.	1.1	2
107	Statistical analysis of error rate of large-scale single flux quantum logic circuit by considering fluctuation of timing parameters. Physica C: Superconductivity and Its Applications, 2016, 530, 101-103.	0.6	2
108	Adiabatic Quantum-Flux-Parametron Constant Cells using Asymmetrical Structures. IEJ Transactions on Fundamentals and Materials, 2016, 136, 747-752.	0.2	9

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109	Research Progress of Low-power Superconducting Integrated Circuits. IEEJ Transactions on Fundamentals and Materials, 2016, 136, 734-739.	0.2	1
110	30GHz Operation of Single-Flux-Quantum Arithmetic Logic Unit Implemented by Using Dynamically Reconfigurable Gates. IEICE Transactions on Electronics, 2016, E99.C, 692-696.	0.3	0
111	Toward the Creation of Future Technologies based on Superconductivity. IEEJ Transactions on Fundamentals and Materials, 2016, 136, 18-20.	0.2	0
112	Thermodynamic Study of Energy Dissipation in Adiabatic Superconductor Logic. Physical Review Applied, 2015, 4, .	1.5	29
113	Design and demonstration of adiabatic quantum-flux-parametron logic circuits with superconductor magnetic shields. Superconductor Science and Technology, 2015, 28, 045020.	1.8	10
114	Demonstration of 10k Gate-Scale Adiabatic-Quantum-Flux-Parametron Circuits. , 2015, , .		11
115	Magnetically coupled quantum-flux-latch with wide operation margins. Superconductor Science and Technology, 2015, 28, 115013.	1.8	4
116	Grounding Methods to Reduce Stray Coupling in Multi-Layer Layouts. , 2015, , .		2
117	Development of Bit-Serial RSFQ Microprocessors Integrated with Shift-Register-Based Random Access Memories. , 2015, , .		8
118	Fast and accurate inductance and coupling calculation for a multi-layer Nb process. Superconductor Science and Technology, 2015, 28, 035013.	1.8	11
119	Inductance and Coupling of Stacked Vias in a Multilayer Superconductive IC Process. IEEE Transactions on Applied Superconductivity, 2015, 25, 1-4.	1.1	10
120	High-Speed Demonstration of Bit-Serial Floating-Point Adders and Multipliers Using Single-Flux-Quantum Circuits. IEEE Transactions on Applied Superconductivity, 2015, 25, 1-6.	1.1	23
121	High-Speed Operation of a Single Flux Quantum Multiple Input Merger Using a Magnetically Coupled SQUID Stack. IEEE Transactions on Applied Superconductivity, 2015, 25, 1-5.	1.1	3
122	Demonstration of single-flux-quantum readout circuits for time-of-flight mass spectrometry systems using superconducting strip ion detectors. Superconductor Science and Technology, 2015, 28, 074003.	1.8	11
123	Demonstration of Bit-Serial SFQ-Based Computing for Integer Iteration Algorithms. IEEE Transactions on Applied Superconductivity, 2015, 25, 1-4.	1.1	2
124	Design Method of Single-Flux-Quantum Logic Circuits Using Dynamically Reconfigurable Logic Gates. IEEE Transactions on Applied Superconductivity, 2015, 25, 1-5.	1.1	7
125	Adiabatic quantum-flux-parametron cell library adopting minimalist design. Journal of Applied Physics, 2015, 117, .	1.1	90
126	Energy efficiency of adiabatic superconductor logic. Superconductor Science and Technology, 2015, 28, 015003.	1.8	54

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127	50 GHz Demonstration of an Integer-Type Butterfly Processing Circuit for an FFT Processor Using the 10 kA/cm ² ; Nb Process. IEICE Transactions on Electronics, 2015, E98.C, 232-237.	0.3	5
128	Experimental demonstration of quantum-flux-latch-based circuits. IEEE Transactions on Applied Superconductivity, 2014, , 1-1.	1.1	1
129	Novel latch for adiabatic quantum-flux-parametron logic. Journal of Applied Physics, 2014, 115, 103910.	1.1	19
130	Reduction of the jitter of single-flux-quantum time-to-digital converters for time-of-flight mass spectrometry. Physica C: Superconductivity and Its Applications, 2014, 504, 97-101.	0.6	6
131	High-Speed Experimental Demonstration of Adiabatic Quantum-Flux-Parametron Gates Using Quantum-Flux-Latches. IEEE Transactions on Applied Superconductivity, 2014, 24, 1-4.	1.1	7
132	Yield analysis of large-scale adiabatic-quantum-flux-parametron logic: The effect of the distribution of the critical current. Physica C: Superconductivity and Its Applications, 2014, 504, 102-105.	0.6	4
133	Nb 9-Layer Fabrication Process for Superconducting Large-Scale SFQ Circuits and Its Process Evaluation. IEICE Transactions on Electronics, 2014, E97.C, 132-140.	0.3	132
134	A Reconfigurable Data-Path Accelerator Based on Single Flux Quantum Circuits. IEICE Transactions on Electronics, 2014, E97.C, 141-148.	0.3	2
135	Large-Scale Integrated Circuit Design Based on a Nb Nine-Layer Structure for Reconfigurable Data-Path Processors. IEICE Transactions on Electronics, 2014, E97.C, 157-165.	0.3	29
136	Optimization of SFQ Logic Gate Considering Dependence of its Signal Propagation Delay on the Bias Voltage. Physics Procedia, 2014, 58, 216-219.	1.2	0
137	Reversible logic gate using adiabatic superconducting devices. Scientific Reports, 2014, 4, 6354.	1.6	58
138	Design and Evaluation of Magnetic Field Tolerant Single Flux Quantum Circuits for Superconductive Sensing Systems. IEICE Transactions on Electronics, 2014, E97.C, 178-181.	0.3	5
139	Design and Demonstration of a Single-Flux-Quantum Multi-Stop Time-to-Digital Converter for Time-of-Flight Mass Spectrometry. IEICE Transactions on Electronics, 2014, E97.C, 182-187.	0.3	10
140	Design and High-Speed Demonstration of Single-Flux-Quantum Bit-Serial Floating-Point Multipliers Using a 10kA/cm ² ; Nb Process. IEICE Transactions on Electronics, 2014, E97.C, 188-193.	0.3	11
141	Reversible Computing Using Adiabatic Superconductor Logic. Lecture Notes in Computer Science, 2014, , 15-25.	1.0	4
142	Recent Progress of Superconducting Integrated Circuit Technologies Challenging the Limits in Low Power Consumption. IEEJ Transactions on Fundamentals and Materials, 2014, 134, 14-17.	0.2	0
143	Design and Implementation of 64-kb CMOS Static RAMs for Josephson-CMOS Hybrid Memories. IEEE Transactions on Applied Superconductivity, 2013, 23, 1700704-1700704.	1.1	10
144	Pseudo Sigmoid Function Generator for a Superconductive Neural Network. IEEE Transactions on Applied Superconductivity, 2013, 23, 1701004-1701004.	1.1	31

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145	16-Bit Wave-Pipelined Sparse-Tree RSFQ Adder. IEEE Transactions on Applied Superconductivity, 2013, 23, 1700605-1700605.	1.1	24
146	Simulation and implementation of an 8-bit carry look-ahead adder using adiabatic quantum-flux-parametron. , 2013, , .		3
147	Operation of an Adiabatic Quantum-Flux-Parametron Gate Using an On-Chip AC Power Source. IEEE Transactions on Applied Superconductivity, 2013, 23, 1301605-1301605.	1.1	2
148	Development of Pulse Transfer Circuits for Serially Biased SFQ Circuits Using the Nb 9-Layer 1- μm Process. IEEE Transactions on Applied Superconductivity, 2013, 23, 1300504-1300504.	1.1	9
149	Simulation and Experimental Demonstration of Logic Circuits Using an Ultra-Low-Power Adiabatic Quantum-Flux-Parametron. IEEE Transactions on Applied Superconductivity, 2013, 23, 1301105-1301105.	1.1	31
150	Improvement of Operating Margin of SFQ Circuits by Controlling Dependence of Signal Propagation Time on Bias Voltage. IEEE Transactions on Applied Superconductivity, 2013, 23, 1300904-1300904.	1.1	3
151	Asynchronous Digital SQUID Magnetometer With an On-Chip Magnetic Feedback for Improvement of Magnetic Resolution. IEEE Transactions on Applied Superconductivity, 2013, 23, 1601405-1601405.	1.1	3
152	Margin and Energy Dissipation of Adiabatic Quantum-Flux-Parametron Logic at Finite Temperature. IEEE Transactions on Applied Superconductivity, 2013, 23, 1700304-1700304.	1.1	58
153	Multiplexing Techniques of Single Flux Quantum Circuit Based Readout Circuit for a Multi-Channel Sensing System. IEEE Transactions on Applied Superconductivity, 2013, 23, 2500204-2500204.	1.1	4
154	Modeling and calibration of ADP process for inductance calculation with InductEx. , 2013, , .		2
155	Design and high-speed tests of a single-flux-quantum time-to-digital converter for time-of-flight mass spectrometry. , 2013, , .		3
156	Demonstration of fully functional 64-kb Josephson/CMOS hybrid memory. , 2013, , .		1
157	20-GHz 8 \times 8-bit Parallel Carry-Save Pipelined RSFQ Multiplier. IEEE Transactions on Applied Superconductivity, 2013, 23, 1300104-1300104.	1.1	20
158	8-Bit Asynchronous Sparse-Tree Superconductor RSFQ Arithmetic-Logic Unit With a Rich Set of Operations. IEEE Transactions on Applied Superconductivity, 2013, 23, 1700104-1700104.	1.1	24
159	An adiabatic quantum flux parametron as an ultra-low-power logic device. Superconductor Science and Technology, 2013, 26, 035010.	1.8	266
160	Design and demonstration of an on-chip AC power source for adiabatic quantum-flux-parametron logic. Superconductor Science and Technology, 2013, 26, 035018.	1.8	6
161	Simulation of sub- $k_B T$ bit-energy operation of adiabatic quantum-flux-parametron logic with low bit-error-rate. Applied Physics Letters, 2013, 103, .	1.5	38
162	Measurement of 10 zJ energy dissipation of adiabatic quantum-flux-parametron logic using a superconducting resonator. Applied Physics Letters, 2013, 102, .	1.5	65

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163	Pulse-Height Distribution Analysis for Superconducting Nanostripline Ion Detector with a Fast Pulse-Integration Analog-Todigital Converter. <i>Physics Procedia</i> , 2012, 36, 172-176.	1.2	3
164	Experimental Demonstration of an Operand Routing Network Prototype Employing Clock Control and Data Synchronization Scheme. <i>Physics Procedia</i> , 2012, 36, 349-353.	1.2	1
165	100-GHz Single-Flux-Quantum Bit-Serial Adder Based on 10- $\{m \text{ kA/cm}^{\{2\}}$ Niobium Process. <i>IEEE Transactions on Applied Superconductivity</i> , 2011, 21, 792-796.	1.1	26
166	High-Speed Test of a Radix-2 Butterfly Processing Element for Fast Fourier Transforms Using SFQ Circuits. <i>IEEE Transactions on Applied Superconductivity</i> , 2011, 21, 823-826.	1.1	6
167	Design and Implementation of Component Circuits of an SFQ Half-Precision Floating-Point Adder Using 10-kA/cm $\{2\}$ Nb Process. <i>IEEE Transactions on Applied Superconductivity</i> , 2011, 21, 827-830.	1.1	9
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