

# Jiwanjot singh

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/5688096/publications.pdf>

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8  
papers

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1684188  
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docs citations

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44  
citing authors

#	ARTICLE	IF	CITATIONS
1	A New Analytical Approach for Set-point Weighted 2DOF-PID Controller Design for Integrating Plus Time-Delay Processes: An Experimental Study. IETE Journal of Research, 2023, 69, 7026-7040.	2.6	10
2	Evaluation of a grid-connected reduced-component boost multilevel inverter (BMLI) topology. International Journal of Circuit Theory and Applications, 2022, 50, 2075-2107.	2.0	12
3	Single DC Source Boost Multilevel Inverter (SSBMLI) Based Soft Charging with Minimum Device Count. , 2022, , .		2
4	Switched capacitor based single DC source boost multilevel inverter ( $S^{2}$ -MLI) featuring isolation based soft charging with minimum device count. International Journal of Emerging Electric Power Systems, 2021, 22, 493-504.	0.8	7
5	Improvement in Fault Clearance Time of the Cascaded H-Bridge Multilevel Inverter Using Novel Technique Based on Frequency Detection. Smart Innovation, Systems and Technologies, 2021, , 485-493.	0.6	0
6	DC to DC converter based Asymmetrical multilevel inverter with reduced number of components. , 2019, , .		0
7	Buck converter-based cascaded asymmetrical multilevel inverter with reduced components. International Transactions on Electrical Energy Systems, 2018, 28, e2501.	1.9	12
8	Recent research on transformer based single DC source multilevel inverter: A review. Renewable and Sustainable Energy Reviews, 2018, 82, 3207-3224.	16.4	38