

Shiqing Li

List of Publications by Year in descending order

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2258059

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citing authors

#	ARTICLE	IF	CITATIONS
1	Reduced Worst-Case Communication Latency Using Single-Cycle Multihop Traversal Network-on-Chip. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2021, 40, 1381-1394.	2.7	11
2	NVM-Based FPGA Block RAM With Adaptive SLC-MLC Conversion. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2018, 37, 2661-2672.	2.7	9
3	EDLAB: A Benchmark for Edge Deep Learning Accelerators. IEEE Design and Test, 2022, 39, 8-17.	1.2	6
4	A Comprehensive Memory Management Framework for CPU-FPGA Heterogenous SoCs. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2023, 42, 1058-1071.	2.7	2