Lava Bhargava

List of Publications by Year
in descending order

41	244	1307594 7	1125743 13
papers	citations	h-index	g-index

44	44	44	140
all docs	docs citations	times ranked	citing authors

#	Article	IF	Citations
1	Analytical Modeling and Simulation-Based Investigation of AlGaN/AlN/GaN Bio-HEMT Sensor for C-erbB-2 Detection. IEEE Sensors Journal, 2018, 18, 9595-9603.	4.7	40
2	Fabrication and Charge Deduction Based Sensitivity Analysis of GaN MOS-HEMT Device for Glucose, MIG, C-erbB-2, KIM-1, and PSA Detection. IEEE Nanotechnology Magazine, 2019, 18, 747-755.	2.0	27
3	A survey â€“ Energy harvesting sources and techniques for internet of things devices. Materials Today: Proceedings, 2020, 30, 52-56.	1.8	23
4	Linear and Circular AlGaN/AlN/GaN MOS-HEMT-based pH Sensor on Si Substrate: A Comparative Analysis. , 2019, 3, 1-4.		20
5	Mapping techniques in multicore processors: current and future trends. Journal of Supercomputing, 2021, 77, 9308-9363.	3.6	20
6	Fabrication and pH-Sensitivity Analysis of MOS-HEMT Dimensional Variants for Bio-Sensing Applications. IEEE Transactions on Nanobioscience, 2021, 20, 28-34.	3.3	14
7	Impact of AlN Interlayer's in Epitaxial and Passivation Scheme on the DC and Microwave Performance of Doping-Less GaN HEMT. Journal of Nanoelectronics and Optoelectronics, 2018, 13, 971-979.	0.5	11
8	The Machine Learning based Optimized Prediction Method for Breast Cancer Detection. , 2020, , .		8
9	CMOS-memristor inverter circuit design and analysis using Cadence Virtuoso. , 2016, , .		7
10	Dynamic workload-aware DVFS for multicore systems using machine learning. Computing (Vienna/New) Tj ETQq0 0 0 rgBT /Overlock 10	4.8	7
11	Golden IC free methodology for hardware Trojan detection using symmetric path delays. , 2016, , .		6
12	Artificial Neural Network based Task Scheduling for Heterogeneous Systems. , 2020, , .		5
13	Fabrication and Modeling-Based Performance Analysis of Circular GaN MOSHEMT-Based Electrochemical Sensors. IEEE Sensors Journal, 2021, 21, 4216-4224.	4.7	5
14	SRCP: sharing and reuse-aware replacement policy for the partitioned cache in multicore systems. Design Automation for Embedded Systems, 2021, 25, 193-211.	1.0	5
15	Novel hardware implementation of LLR-based non-binary LDPC decoders. , 2013, , .		4
16	Automated Coverage Register Access Technology on UVM Framework for Advanced Verification. , 2018, , .		4
17	Hybrid buffers based coarse-grained power gated network on chip router microarchitecture. International Journal of Electronics, 2020, 107, 272-287.	1.4	4
18	Reinforcement Learning based Routing for Cognitive Network on Chip. , 2016, , .		3

#	Article	IF	Citations
19	Smart Implementation of Computer Vision and Machine Learningfor Pothole Detection. , 2021, , .		3
20	Deep neural network learning for power limited heterogeneous system with workload classification. Computing (Vienna/New York), 2022, 104, 95-122.	4.8	3
21	Dielectric Modulated Underlap Based AlGaN/AlN/GaN MOS-HEMT for Label Free Bio-Detection. Journal of Nanoelectronics and Optoelectronics, 2019, 14, 1064-1071.	0.5	3
22	Imply logic based on TiO² memristor model for computational circuits. , 2015, , .		2
23	A framework for thermal aware reliability estimation in 2D NoC. , 2015, , .		2
24	Reducing FIFO buffer power using architectural alternatives at RTL. , 2016, , .		2
25	Self-Assertive Generic UVM Testbench for Advanced Verification of Bridge IPs. , 2017, , .		2
26	Write energy reduction of STT-MRAM based multi-core cache hierarchies. International Journal of Electronics Letters, 2019, 7, 249-261.	1.2	2
27	A composite SystemC-UVM abstract optimal path selection verification architecture for complex designs. Microelectronics Reliability, 2022, 131, 114508.	1.7	2
28	DC and RF characterzationof field plated AlGaN/GaN HEMT. , 2017, , .		1
29	Novel Variability Aware Path Selection for Self-Referencing Based Hardware Trojan Detection. , 2018, , .		1
30	Dynamic Voltage Frequency Scaling in Multi-core Systems using Adaptive Regression Model. , 2020, , .		1
31	Real-time automated register abstraction active power-aware electronic system level verification framework. The Integration VLSI Journal, 2021, 77, 151-166.	2.1	1
32	Hybrid learning scenario path selection and abstraction framework for smart verification of complex SoCs. Journal of Supercomputing, 2022, 78, 6207-6233.	3.6	1
33	A Power, Thermal and Reliability-Aware Network-on-Chip. , 2017, , .		0
34	MDAB: Module Design Automation Block for Verification using System Verilog Testbench. , 2018, , .		0
35	3D LBDR: Logic-Based Distributed Routing for 3D NoC. Communications in Computer and Information Science, 2019, , 473-482.	0.5	0
36	Comparative Analysis of Control-transfer Instructions On WCET of Real-Time Systems. , 2019, , .		0

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37	Leakage Reduction and gm Enhancement in GaN HEMT for Enhanced Sensitivity in Fibrinogen Detection from Human Plasma. , 2019, , .		0
38	Automated Bug Resistant Test Intent with Register Header Database for Optimized Verification. Journal of Electronic Testing: Theory and Applications (JETTA), 2020, 36, 219-237.	1.2	0
39	A Novel and Robust Implementation of Register Abstraction on UVM Testbench. International Journal of Simulation: Systems, Science and Technology, 0, , .	0.0	0
40	Impact of Furnace Annealing and other Process Failures to be taken care during Fabrication of an AlGaN/GaN MOSHEMT. , 2019, , .		0
41	Reuse-Aware Cache Partitioning Framework for Data-Sharing Multicore Systems. , 2021, , .		0