

Takayuki Onishi

List of Publications by Year in descending order

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#	ARTICLE	IF	CITATIONS
1	A Single-Chip 4K 60-fps 4:2:2 HEVC Video Encoder LSI Employing Efficient Motion Estimation and Mode Decision Framework With Scalability to 8K. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2018, 26, 1930-1938.	3.1	16
2	Single-chip 4K 60fps 4:2:2 HEVC video encoder LSI with 8K scalability. , 2015, , .		12
3	A 120 fps High Frame Rate Real-time HEVC Video Encoder with Parallel Configuration Scalable to 4K. IEEE Transactions on Multi-Scale Computing Systems, 2018, 4, 491-499.	2.4	11
4	A 4K/60p HEVC Real-Time Encoding System With High Quality HDR Color Representations. IEEE Transactions on Consumer Electronics, 2018, 64, 433-441.	3.6	11
5	A 120 fps high frame rate real-time HEVC video encoder with parallel configuration scalable to 4K. , 2017, , .		5
6	Single-Chip MPEG-2 422P@HL CODEC LSI With Multichip Configuration for Large Scale Processing Beyond HDTV Level. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2007, 15, 1055-1059.	3.1	4
7	An H.264/AVC High422 Profile and MPEG-2 422 Profile Encoder LSI for HDTV Broadcasting Infrastructures. IEICE Transactions on Electronics, 2012, E95.C, 432-440.	0.6	3
8	4K 120fps HEVC Temporal Scalable Encoder with Super Low Delay. , 2019, , .		2
9	Low Delay 4K 120fps HEVC Decoder with Parallel Processing Architecture. IEICE Transactions on Electronics, 2020, E103.C, 77-84.	0.6	0