

# Yu-Chih Hsiao

## List of Publications by Year in descending order

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#	ARTICLE	IF	CITATIONS
1	60-GHz Dual-Conversion Down-/Up-Converters Using Schottky Diode in 0.18 $\mu\text{m}$ Foundry CMOS Technology. IEEE Transactions on Microwave Theory and Techniques, 2012, 60, 1684-1698.	4.6	34
2	Broadband CMOS Schottky-Diode Star Mixer Using Coupled-CPW Marchand Dual-Baluns. IEEE Microwave and Wireless Components Letters, 2017, 27, 500-502.	3.2	21
3	Analysis and Design of Broadband LC-Ladder FET LNAs Using Noise Match Network. IEEE Transactions on Microwave Theory and Techniques, 2018, 66, 987-1001.	4.6	19
4	A 2.4-GHz Low-Flicker-Noise CMOS Sub-Harmonic Receiver. IEEE Transactions on Circuits and Systems I: Regular Papers, 2013, 60, 437-447.	5.4	15
5	5/60 GHz 0.18- $\mu\text{m}$ CMOS Dual-Mode Dual-Conversion Receiver Using a Tunable Active Filter for 5-GHz Channel Selection. IEEE Microwave and Wireless Components Letters, 2016, 26, 951-953.	3.2	14
6	A Chebyshev-Response Step-Impedance Phase-Inverter Rat-Race Coupler Directly on Lossy Silicon Substrate and Its Gilbert Mixer Application. IEEE Transactions on Microwave Theory and Techniques, 2011, 59, 882-893.	4.6	12
7	Design Optimization of Single-/Dual-Band FET LNAs Using Noise Transformation Matrix. IEEE Transactions on Microwave Theory and Techniques, 2015, , 1-14.	4.6	9
8	A Miniature 200-GHz Subharmonic Mixer With a Folded $180^\circ$ Hybrid Using Equal-Length Edge- and Broadside-Coupled Lines. IEEE Microwave and Wireless Components Letters, 2018, 28, 338-340.	3.2	9
9	Analytical Noise Optimization of Single-/Dual-Band MOS LNAs With Substrate and Metal Loss Effects of Inductors. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 2454-2467.	5.4	7
10	2.4-GHz Low-Noise Direct-Conversion Receiver With Deep N-Well Vertical-NPN BJT Operating Near Cutoff Frequency. IEEE Transactions on Microwave Theory and Techniques, 2011, 59, 3195-3205.	4.6	6
11	2.4-GHz Enhanced Lumped Ring Filter With Two Transmission Zeros Using 0.18- $\mu\text{m}$ SiGe BiCMOS Process. IEEE Microwave and Wireless Components Letters, 2017, 27, 305-307.	3.2	6
12	0.18 $\mu\text{m}$ SiGe BiCMOS microwave/millimeter-wave dual-mode dual-conversion receiver architecture with a tunable RF channel selection at low-flicker-noise microwave mode. , 2017, , .		5
13	2-GHz 1.35-dB NF pHEMT single-voltage-supply process-independent low-noise amplifier. , 2018, , .		5
14	Low-Power Sub-Harmonic Direct-Conversion Receiver With Tunable RF LNA and Wideband LO Generator at U-NII Bands. IEEE Transactions on Microwave Theory and Techniques, 2012, 60, 555-566.	4.6	4
15	A 0.6-V 30 GHz CMOS Quadrature VCO Using Microwave 1:1:1 Trifilar Transformer. IEEE Microwave and Wireless Components Letters, 2012, 22, 88-90.	3.2	4
16	60-GHz SiGe BiCMOS dual-conversion down-converter: Schottky diode RF mixer and analog Gilbert IF mixer with microwave quadrature generator. , 2016, , .		4
17	15 GHz High-Isolation Sub-Harmonic Mixer With Delay Compensation. IEEE Microwave and Wireless Components Letters, 2009, 19, 810-812.	3.2	3
18	40-GHz high-linearity single-voltage-supply pHEMT gilbert sub-harmonic upconverter using leveled-LO topology. , 2011, , .		3

#	ARTICLE	IF	CITATIONS
19	2.4-GHz Tunable Miniature CMOS Active Bandpass Filter with Two Transmission Zeros Using Lumped Stepped-Impedance Ring Resonator. , 2018, , .		3
20	Quadrature regenerative frequency dividers using HEMT technology. , 2010, , .		2
21	60-GHz 0.18-um CMOS dual-conversion receiver using Schottky diode mixer and slow-wave rat-race hybrid. , 2012, , .		2
22	High LO-to-RF isolation wideband gilbert upconversion micromixer using a phase-inverter rat-race coupler in 0.35&#x00B5;m SiGe HBT technology. , 2012, , .		2
23	Ka-band pHEMT quadrupler with injection and extraction from oscillator frequency doubling points. , 2014, , .		2
24	V-band flip-chip pHEMT balanced power amplifier with CPWGMS-CPWG topology and CPWG Lange couplers. , 2017, , .		2
25	V-band Sub-Harmonic Gate-Pumped Resistive Mixer With a 180<sup>o</sup> Hybrid Using an In-Phase Power Divider Merging With an Out-Of-Phase Marchand Balun. , 2019, , .		2
26	16.4 GHz SiGe BiCMOS sub-harmonic mixer with reactive I/Q generators in RF and LO paths. , 2009, , .		1
27	Low-noise GaInP/GaAS HBT wideband dual feedback amplifiers using Kukielka and Meyer topologies. Microwave and Optical Technology Letters, 2010, 52, 1486-1489.	1.4	1
28	Ku-band SiGe HBT I/Q subharmonic mixer with reactive quadrature generators. Microwave and Optical Technology Letters, 2010, 52, 1516-1520.	1.4	1
29	2.4-GHz 8.5mW 3.7-dB NF 100-kHz 1/f corner direct-conversion receiver using double-balanced passive mixer. , 2011, , .		1
30	16-GHz mHEMT double-quadrature Gilbert down-conversion mixer with polyphase filters. , 2013, , .		1
31	V-band dual-conversion down-converter with Schottky-diode ring-mixer using 0.351¼m SiGe BiCMOS process. , 2015, , .		1
32	5â€“6 GHz CMOS low-noise direct conversion receiver using a differential RF VGA with a differential inductor load. , 2016, , .		1
33	Design optimization of inductively source-degenerated FET LNAs using noise transformation matrix (invited talk). , 2016, , .		1
34	SBD layout optimization with effect of N-well to p-substrate pn junctions in 0.18 Åµm CMOS process. , 2016, , .		1
35	0.35-1¼m SiGe BiCMOS weaver image rejection receiver with 60-GHz double-quadrature sub-harmonic Schottky Diode mixer and 10-GHz double quadrature Gilbert mixer. , 2017, , .		1
36	Status of Analytical Optimization of FET LNAs Using Noise Transformation Matrix (Invited Paper). , 2021, , .		1

#	ARTICLE	IF	CITATIONS
37	X-band Weaver-Hartley low-IF downconverter with a resonant LC load. , 2009, , .		0
38	Comparison of shunt-series shunt-shunt and shunt-series series-shunt dual feedback wideband amplifiers. , 2010, , .		0
39	Isolation performance of sub-harmonic Gilbert mixers. , 2011, , .		0
40	60-GHz dual-conversion down-/up- converters using Schottky diode in 0.18 $\mu\text{m}$ CMOS process: An alternative approach for millimeter-wave transceiver. , 2012, , .		0
41	5.8 GHz low-flicker-noise CMOS direct-conversion receiver using deep-n-well vertical-NPN BJT. , 2012, , .		0
42	60-GHz dual-conversion down-converter using Schottky diode and dual-band rat-race coupler in standard 0.18- $\mu\text{m}$ CMOS process. , 2013, , .		0
43	15 GHz wideband CMOS Gilbert up-converter with stacked spiral-CPS phase-inverter rat-race coupler at RF port. , 2013, , .		0
44	V-band pHEMT dual-conversion down-converter with Schottky diode RF mixer and analog Gilbert IF mixer. , 2014, , .		0
45	5.8 GHz 4.7 mA zero-IF passive mixer receiver using a step-down transformer for gain enhancement. , 2015, , .		0
46	Temperature insensitive PA bias circuit with digital control interface using InGaP/GaAs HBT technology. , 2015, , .		0
47	U-band pHEMT divide-by-three ILFD. , 2015, , .		0
48	Temperature insensitive PA bias circuit with digital control interface using InGaP/GaAs HBT technology. , 2015, , .		0
49	30-GHz mHEMT divide-by-three injection-locked frequency divider with Marchand balun. , 2015, , .		0
50	E-band RF-to-DC converter using Schottky diode in 0.18- $\mu\text{m}$ CMOS technology. , 2016, , .		0
51	Ku-Band single-voltage-supply downconverter using 0.15 $\mu\text{m}$ pHEMT process. , 2016, , .		0
52	Programmable-gain constant-IF-bandwidth SiGe BiCMOS upconversion micromixer at 2.4/5.8 GHz using current-mode approach. , 2017, , .		0
53	pHEMT Single-Voltage-Supply Direct-Conversion Receiver With a 3-10 GHz LC Ladder LNA. , 2019, , .		0
54	Balanced Noise Design of Dual-Band 2.4/5-GHz pHEMT LNAs. , 2020, , .		0