

Alexios Balatsoukas-Stimming

List of Publications by Year in descending order

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55
papers

1,552
citations

840119

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839053

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g-index

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all docs

57
docs citations

57
times ranked

888
citing authors

#	ARTICLE	IF	CITATIONS
1	Dynamic SCL Decoder With Path-Flipping for 5G Polar Codes. IEEE Wireless Communications Letters, 2022, 11, 391-395.	3.2	5
2	A Maximum-Likelihood-Based Two-User Receiver for LoRa Chirp Spread-Spectrum Modulation. IEEE Internet of Things Journal, 2022, 9, 22993-23007.	5.5	3
3	On the Advantage of Coherent LoRa Detection in the Presence of Interference. IEEE Internet of Things Journal, 2021, 8, 11581-11593.	5.5	28
4	OFDM-Based Beam-Oriented Digital Predistortion for Massive MIMO. , 2021, , .		2
5	Threshold-Based Fast Successive-Cancellation Decoding of Polar Codes. IEEE Transactions on Communications, 2021, 69, 3541-3555.	4.9	12
6	A Soft-Aided Staircase Decoder Using Three-Level Channel Reliabilities. Journal of Lightwave Technology, 2021, 39, 6191-6203.	2.7	6
7	Joint Detection and Self-Interference Cancellation in Full-Duplex Systems Using Machine Learning. , 2021, , .		4
8	A Two-User Successive Interference Cancellation LoRa Receiver with Soft-Decoding. , 2021, , .		1
9	On the Error Rate of the LoRa Modulation With Interference. IEEE Transactions on Wireless Communications, 2020, 19, 1292-1304.	6.1	64
10	Coded LoRa Frame Error Rate Analysis. , 2020, , .		13
11	An Open-Source LoRa Physical Layer Prototype on GNU Radio. , 2020, , .		55
12	Training Channel Selection for Learning-Based 1-Bit Precoding in Massive MU-MIMO. , 2020, , .		0
13	Identification of Non-Linear RF Systems using Backpropagation. , 2020, , .		7
14	Implementation of a High-Throughput Fast-SSC Polar Decoder with Sequence Repetition Node. , 2020, , .		6
15	Hardware Implementation of Neural Self-Interference Cancellation. IEEE Journal on Emerging and Selected Topics in Circuits and Systems, 2020, 10, 204-216.	2.7	26
16	A Standalone FPGA-Based Miner for Lyra2REv2 Cryptocurrencies. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 1194-1206.	3.5	6
17	Experimental Demonstration of 9.6 Gbit/s Polar Coded Infrared Light Communication System. IEEE Photonics Technology Letters, 2020, 32, 1539-1542.	1.3	3
18	A Maximum-Likelihood-based Multi-User LoRa Receiver Implemented in GNU Radio. , 2020, , .		4

#	ARTICLE	IF	CITATIONS
19	Non-Linear Self-Interference Cancellation via Tensor Completion. , 2020, , .		4
20	On the Implementation Complexity of Digital Full-Duplex Self-Interference Cancellation. , 2020, , .		4
21	Design and Implementation of a Neural Network Based Predistorter for Enhanced Mobile Broadband. , 2019, , .		20
22	On the Computational Complexity of Blind Detection of Binary Linear Codes. , 2019, , .		3
23	Improving HD-FEC decoding via bit marking. , 2019, , .		2
24	Advanced Machine Learning Techniques for Self-Interference Cancellation in Full-Duplex Radios. , 2019, , .		22
25	A 588-Gb/s LDPC Decoder Based on Finite-Alphabet Message Passing. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2018, 26, 329-340.	2.1	56
26	Faulty Successive Cancellation Decoding of Polar Codes for the Binary Erasure Channel. IEEE Transactions on Communications, 2018, 66, 2322-2332.	4.9	3
27	Fast Low-Complexity Decoders for Low-Rate Polar Codes. Journal of Signal Processing Systems, 2018, 90, 675-685.	1.4	33
28	On the Tradeoff Between Accuracy and Complexity in Blind Detection of Polar Codes. , 2018, , .		10
29	Design and Implementation of a Neural Network Aided Self-Interference Cancellation Scheme for Full-Duplex Radios. , 2018, , .		29
30	Design of LDPC Codes for the Unequal Power Two-User Gaussian Multiple Access Channel. IEEE Wireless Communications Letters, 2018, 7, 868-871.	3.2	11
31	Comparison of Polar Decoders with Existing Low-Density Parity-Check and Turbo Decoders. , 2017, , .		28
32	PolarBear: A 28-nm FD-SOI ASIC for Decoding of Polar Codes. IEEE Journal on Emerging and Selected Topics in Circuits and Systems, 2017, 7, 616-629.	2.7	34
33	Blind detection of polar codes. , 2017, , .		20
34	A multi-Gbps unrolled hardware list decoder for a systematic polar code. , 2016, , .		7
35	Hardware decoders for polar codes: An overview. , 2016, , .		16
36	High-throughput lattice reduction for large-scale MIMO systems based on Seysen's algorithm. , 2016, , .		4

#	ARTICLE	IF	CITATIONS
37	Digital predistortion of power amplifier non-linearities for full-duplex transceivers. , 2016, , .		15
38	Sliding Window Spectrum Sensing for Full-Duplex Cognitive Radios with Low Access-Latency. , 2016, , .		3
39	Partitioned successive-cancellation list decoding of polar codes. , 2016, , .		36
40	An FPGA-based accelerator for rapid simulation of SC decoding of polar codes. , 2015, , .		2
41	Baseband and RF hardware impairments in full-duplex wireless systems: experimental characterisation and suppression. Eurasip Journal on Wireless Communications and Networking, 2015, 2015, .	1.5	27
42	A fully-unrolled LDPC decoder based on quantized message passing. , 2015, , .		30
43	Quantized message passing for LDPC codes. , 2015, , .		22
44	The impact of faulty memory bit cells on the decoding of spatially-coupled LDPC codes. , 2015, , .		5
45	On metric sorting for successive cancellation list decoding of polar codes. , 2015, , .		36
46	LLR-Based Successive Cancellation List Decoding of Polar Codes. IEEE Transactions on Signal Processing, 2015, 63, 5165-5179.	3.2	422
47	A low-complexity improved successive cancellation decoder for polar codes. , 2014, , .		163
48	Enabling complexity-performance trade-offs for successive cancellation decoding of polar codes. , 2014, , .		7
49	Density Evolution for Min-Sum Decoding of LDPC Codes Under Unreliable Message Storage. IEEE Communications Letters, 2014, 18, 849-852.	2.5	46
50	Measurement-based characterization of residual self-interference on a full-duplex MIMO testbed. , 2014, , .		25
51	Hardware Architecture for List Successive Cancellation Decoding of Polar Codes. IEEE Transactions on Circuits and Systems II: Express Briefs, 2014, 61, 609-613.	2.2	92
52	LLR-based successive cancellation list decoding of polar codes. , 2014, , .		32
53	A parallelized layered QC-LDPC decoder for IEEE 802.11ad. , 2013, , .		11
54	A multipurpose testbed for full-duplex wireless communications. , 2013, , .		3

#	ARTICLE	IF	CITATIONS
55	On self-interference suppression methods for low-complexity full-duplex MIMO. , 2013, , .		24