

# Neeraj Gupta

## List of Publications by Year in descending order

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papers

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docs citations

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times ranked

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citing authors

#	ARTICLE	IF	CITATIONS
1	A Study of Conventional and Junctionless MOSFET Using TCAD Simulations. , 2015, , .		10
2	High-k Dielectric Double Gate Junctionless (DG-JL) MOSFET for Ultra Low Power Applications-Analytical Model. Silicon, 2022, 14, 7725-7734.	3.3	8
3	Subthreshold Current Modeling of Stacked Dielectric Triple Material Cylindrical Gate All Around (SD-TM-CGAA) Junctionless MOSFET for Low Power Applications. Silicon, 2022, 14, 6261-6269.	3.3	7
4	Analytical Modeling and Performance Analysis of Surface Potential for Junctionless MOSFET. Journal of Physics: Conference Series, 2021, 1916, 012026.	0.4	6
5	Elicitation of Scattering Parameters of Dual-halo Dual-dielectric Triple-material Surrounding Gate (DH-DD-TM-SG) MOSFET for Microwave Frequency Applications. Advances in Electrical and Electronic Engineering, 2021, 19, .	0.3	5
6	Performance Investigation of Dual-Halo Dual-Dielectric Triple Material Surrounding Gate MOSFET with High- $\epsilon_r$ dielectrics for Low Power Applications. Journal of Semiconductor Technology and Science, 2020, 20, 297-304.	0.4	5
7	Filtering of Noise in Audio/Voice Signal. , 2018, , .		3
8	Numerical Simulation of Temperature Dependency on Performance of Solar PVC. Silicon, 0, , 1.	3.3	2
9	Noise analysis of Dual Halo Dual Dielectric Triple Material Surrounding Gate MOSFET for RF applications. , 2019, , .		1
10	Design and Implementation of Array Multiplier using Compressor for Low Power. , 2022, , .		1
11	Design of an Ultra-Low Power CMOS ADC using Threshold Inverter Quantization for Communication System. , 2022, , .		1
12	Performance Analysis of Fully Depleted SOI (FD-SOI) MOSFET Incorporating Dielectric Engineering. IOP Conference Series: Materials Science and Engineering, 2022, 1228, 012021.	0.6	1
13	Performance Analysis of Noise in Dual Halo Dual Dielectric Triple Material Surrounding Gate MOSFET for RF Applications. International Journal of Nanoscience, 0, , .	0.7	0
14	Low Power Sub-threshold Domino AND Gate. Journal of Physics: Conference Series, 2021, 1854, 012031.	0.4	0
15	A Systematic Study on Mitigating Parameter Variation in MOSFETs. Advances in Intelligent Systems and Computing, 2016, , 99-108.	0.6	0
16	A COMPARATIVE STUDY OF CONVENTIONAL AND DOUBLE GATE SOI MOSFET USING TCAD SIMULATIONS. Far East Journal of Electronics and Communications, 0, , 411-419.	0.2	0
17	Electric Field Modeling and Critiques of Dual-Halo Dual-Dielectric Triple-Material Surrounding-Gate MOSFET. Algorithms for Intelligent Systems, 2020, , 1413-1422.	0.6	0