Mudasir Memon

List of Publications by Year
in descending order

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15 papers	940 citations	933447 10 h-index	1281871 11 g-index
15 all docs	15 docs citations	15 times ranked	663 citing authors

#	Article	IF	Citations
1	Optimal Design of a New Cascaded Multilevel Inverter Topology With Reduced Switch Count. IEEE Access, 2019, 7, 24498-24510.	4.2	207
2	A New Multilevel Inverter Topology With Reduce Switch Count. IEEE Access, 2019, 7, 58584-58594.	4.2	155
3	Selective harmonic elimination in inverters using bio-inspired intelligent algorithms for renewable energy conversion applications: A review. Renewable and Sustainable Energy Reviews, 2018, 82, 2235-2253.	16.4	138
4	Asynchronous Particle Swarm Optimization-Genetic Algorithm (APSO-GA) Based Selective Harmonic Elimination in a Cascaded H-Bridge Multilevel Inverter. IEEE Transactions on Industrial Electronics, 2022, 69, 1477-1487.	7.9	96
5	Single-Phase Step-Up Switched-Capacitor-Based Multilevel Inverter Topology With SHEPWM. IEEE Transactions on Industry Applications, 2021, 57, 3107-3119.	4.9	95
6	Selective harmonic elimination in multilevel inverter using hybrid APSO algorithm. IET Power Electronics, 2018, 11, 1673-1680.	2.1	73
7	A New Configurable Topology for Multilevel Inverter With Reduced Switching Components. IEEE Access, 2020, 8, 188726-188741.	4.2	56
8	A new singleâ€phase cascaded multilevel inverter topology with reduced number of switches and voltage stress. International Transactions on Electrical Energy Systems, 2020, 30, e12191.	1.9	44
9	Mitigation of Power Quality Issues Due to High Penetration of Renewable Energy Sources in Electric Grid Systems Using Three-Phase APF/STATCOM Technologies: A Review. Energies, 2018, 11, 1491.	3.1	41
10	Singleâ€phase hybrid multilevel inverter topology with low switching frequency modulation techniques for lower order harmonic elimination. IET Power Electronics, 2020, 13, 4117-4127.	2.1	12
11	Asymmetrical Multilevel Inverter Topology with Reduced Number of Components. , 2018, , .		10
12	Comparative Analysis of Optimal and Fixed Input DC Sources with Selective Harmonic Elimination Pulse Width Modulation. , 2019, , .		9
13	A High-Frequency Isolated Online Uninterruptible Power Supply (UPS) System with Small Battery Bank for Low Power Applications. Energies, 2017, 10, 418.	3.1	2
14	SHEPWM Based New Hybrid Multilevel Inverter Topology with Reduced Switch Count. , 2019, , .		2
15	Automated Flower Classification using Transfer Learning and Meta-Learners in Deep Learning Framework. Sindh University Research Journal -science Series, 2020, 52, 93-102.	0.1	0